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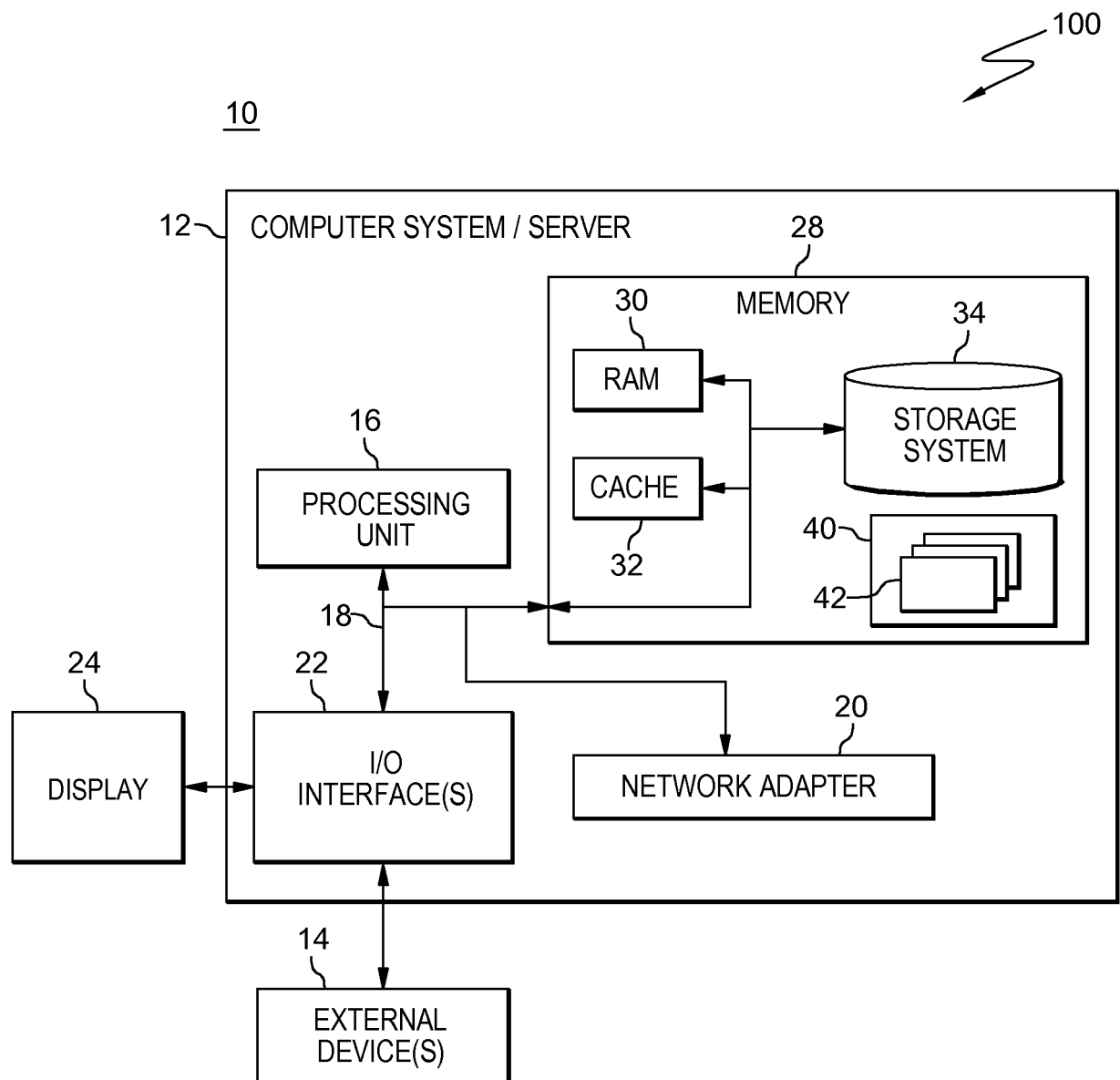


FIG. 1A

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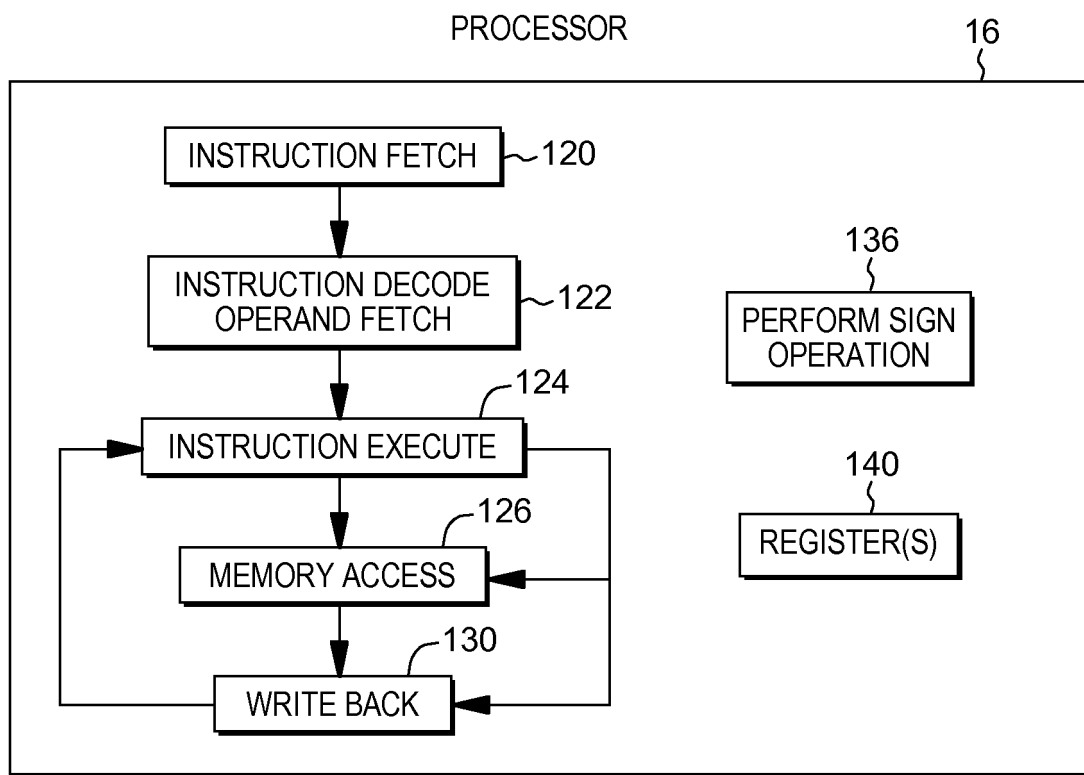


FIG. 1B

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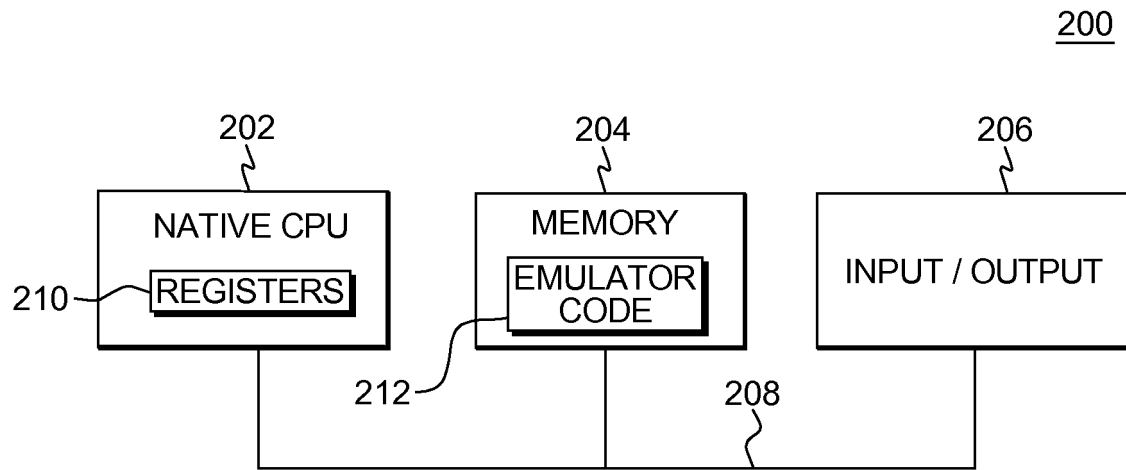


FIG. 2A

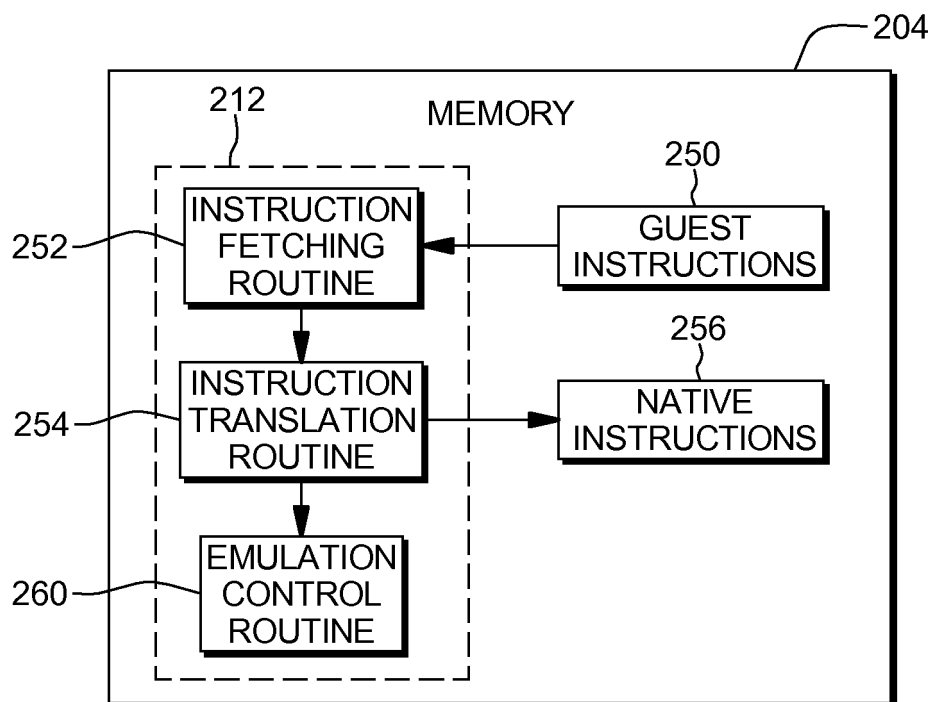


FIG. 2B

300

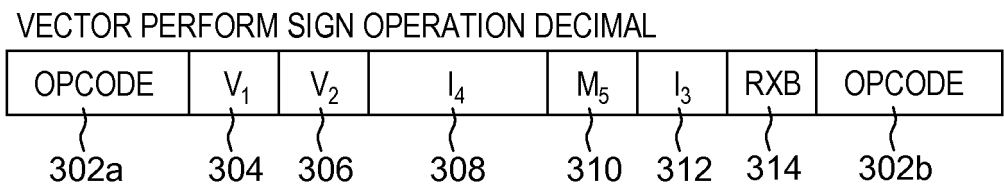


FIG. 3A

308

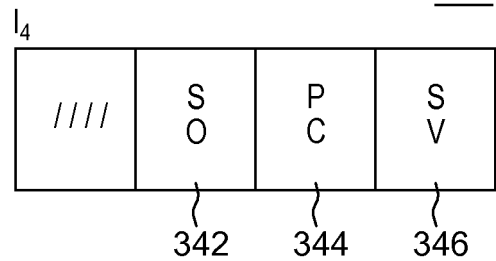


FIG. 3B

310

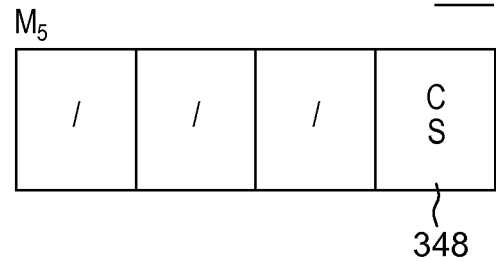


FIG. 3C

312

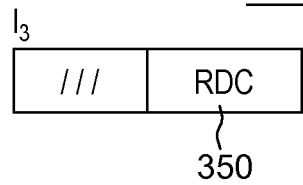


FIG. 3D

400	402	404	406	408	412
Sign Operation (SO)	result magnitude (after RDC applied)	V_2 sign	Positive sign Code (PC)	result sign code (hex)	Input Operand sign code validity check
00 (maintain)	nonzero	positive	0	C positive	perform
00 (maintain)	nonzero	positive	1	F positive	perform
00 (maintain)	nonzero	negative	n/a	D negative	perform
00 (maintain)	zero	n/a	0	C positive	perform
00 (maintain)	zero	n/a	1	F positive	perform
01 (complement)	nonzero	positive	n/a	D negative	perform
01 (complement)	nonzero	negative	0	C positive	perform
01 (complement)	nonzero	negative	1	F positive	perform
01 (complement)	zero	n/a	0	C positive	perform
01 (complement)	zero	n/a	1	F positive	perform
10 (force positive)	n/a	n/a	0	C positive	perform if SV=1
10 (force positive)	n/a	n/a	1	F positive	perform if SV=1
11 (force negative)	nonzero	n/a	n/a	D negative	perform if SV=1
11 (force negative)	zero	n/a	0	C positive	perform if SV=1
11 (force negative)	zero	n/a	1	F positive	perform if SV=1

n/a - results do not depend on this value

FIG. 4

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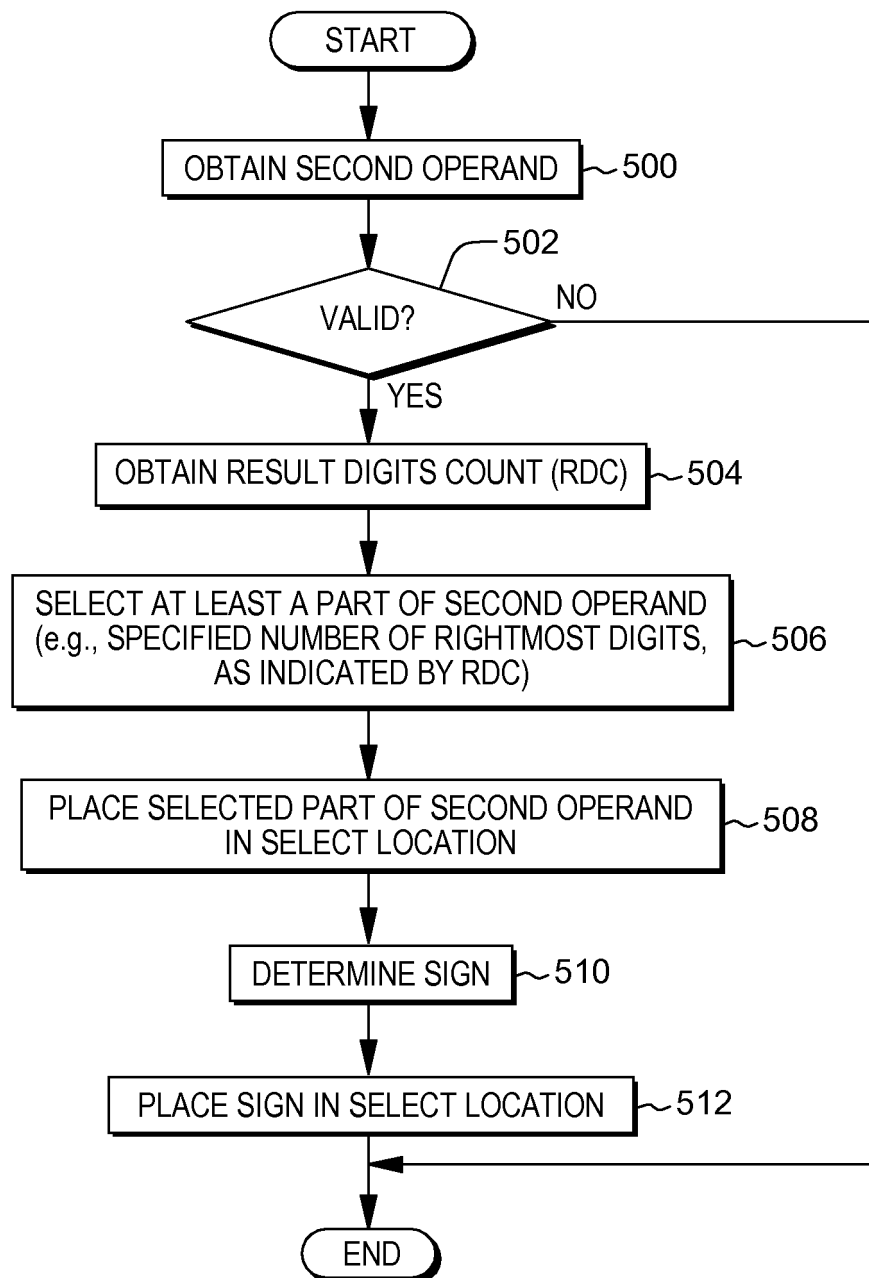


FIG. 5

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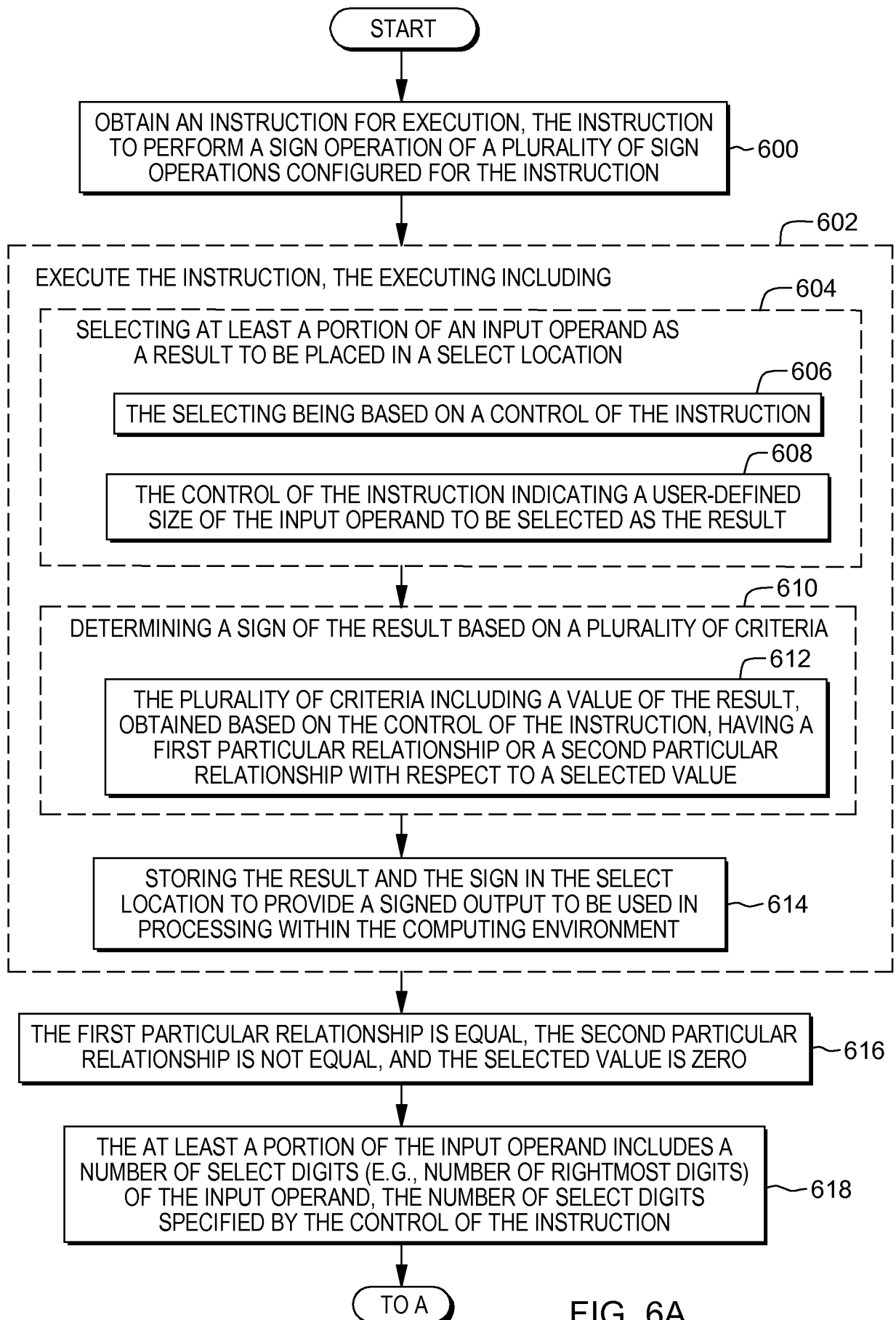


FIG. 6A

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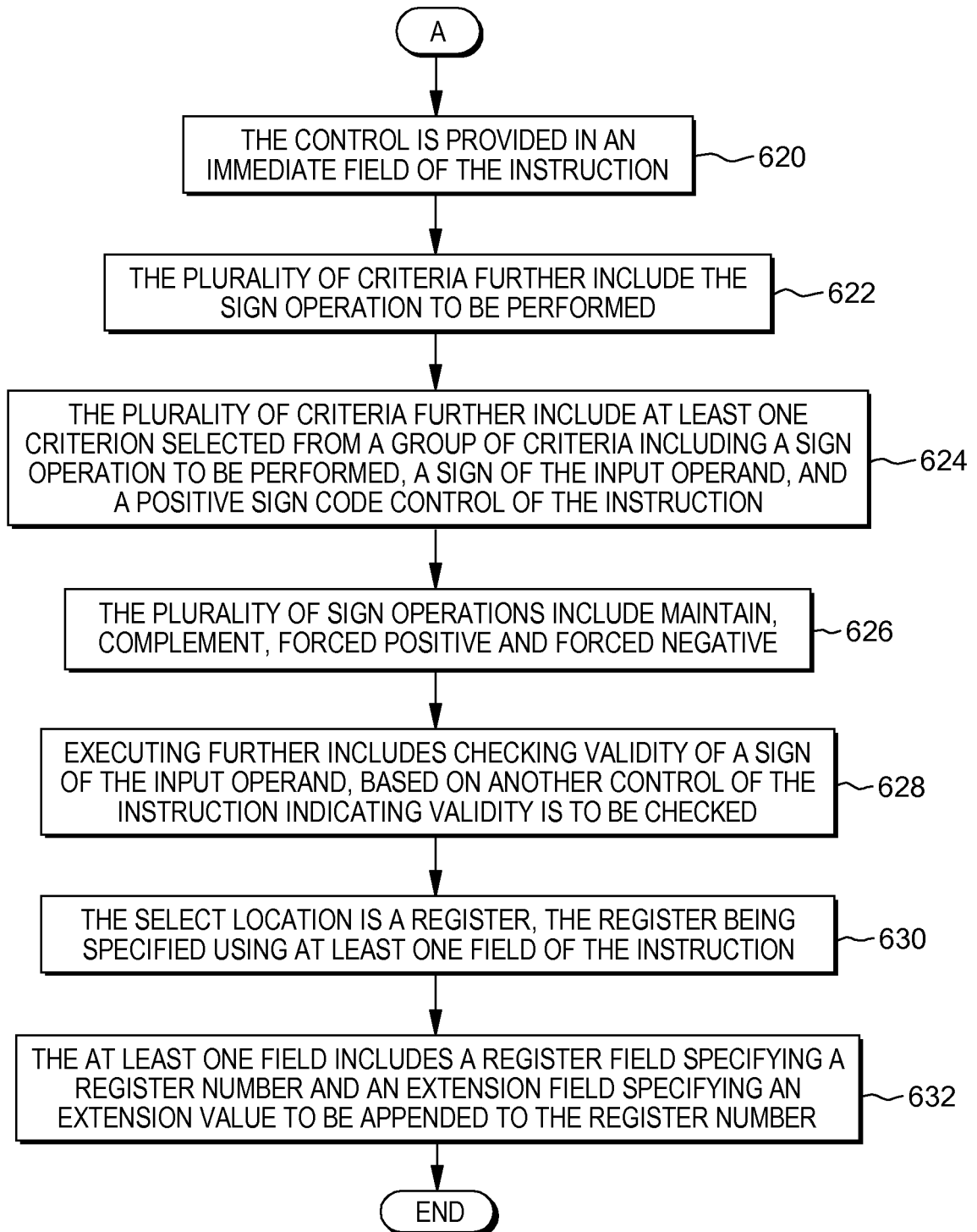


FIG. 6B

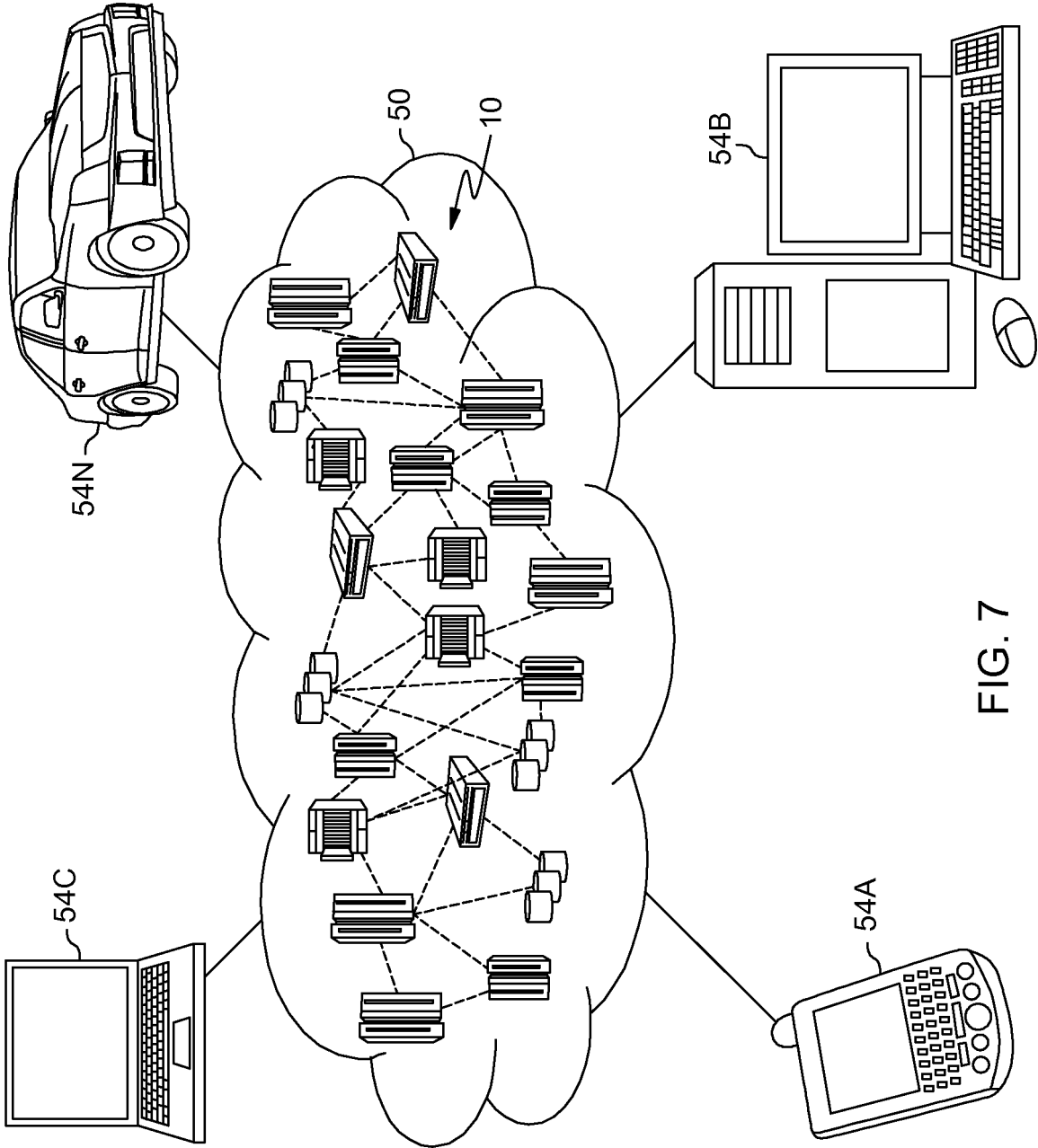


FIG. 7

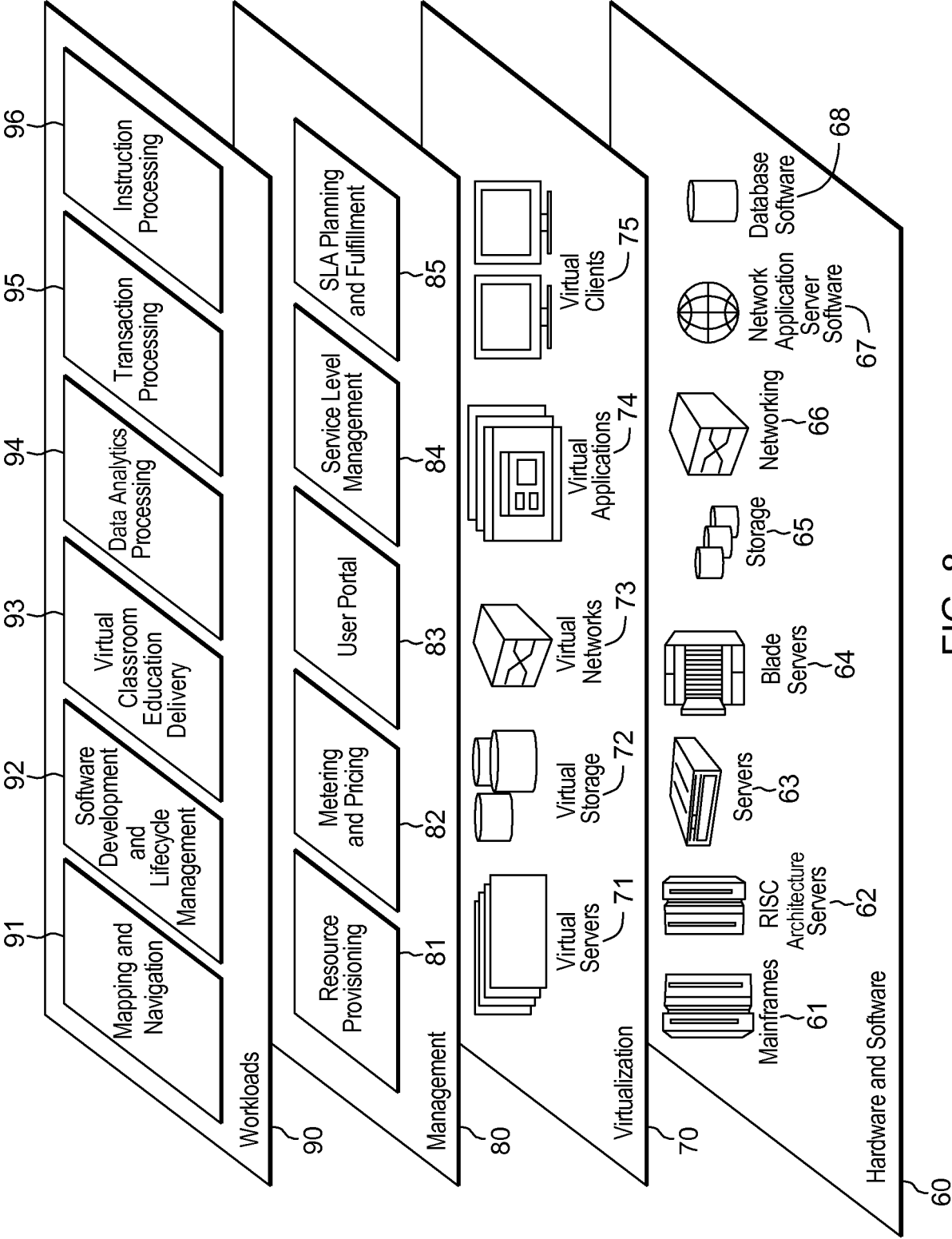


FIG. 8