7. The device of any of claims 1-2, further comprising a connector for connecting the measurement circuitry to a mobile computer device, the mobile computer device being configured for controlling operation of the device and storing results of the verification test of each wire of the bus.

8. The device of any of claims 1, 2, or 7, wherein the verification test further comprises:

detecting a short circuit between the wire under test and another wire of the bus;

detecting a short circuit between the wire under test and a ground potential; and

indicating that the wire passed the verification test in response to not detecting the short circuit between the wire under test and the other wire of the bus and not detecting the short circuit between wire under test and the ground potential.

9. The device of claim 8, wherein detecting the short circuit between the wire under test and the other wire of the bus comprises:

connecting the wire under test to ground by the measurement circuitry;

connecting the other wire to a voltage source of the device by the measurement circuitry;

measuring a voltage across a sense resistor associated with the wire under test by the measurement circuitry; and

comparing the measured voltage to an expected voltage for the wire by the measurement circuitry, the short circuit being detected in response to the measured voltage not corresponding substantially to the expected voltage.

10. The device of ~~any of claims 8-9~~claim 8, wherein detecting the short circuit between the wire under test and the ground potential comprises:

connecting a ground of an airplane including the bus to a ground of the device by the measurement circuitry;

removing any connections to other wires of the bus by the measurement circuitry;

connecting the wire under test to a voltage source of the device by the measurement circuitry;

measuring a voltage across a sense resistor associated with the wire under test by the measurement circuitry; and

comparing the measured voltage to an expected voltage for the wire by the measurement circuitry, wherein the short circuit is detected in response to the measured voltage not corresponding substantially to the expected voltage.

11. A method for verifying wire integrity of a bus, comprising:

detecting connection to a bus connector, the bus connector being configured to connect a plurality of buses to an apparatus and each bus comprising a plurality of wires;

automatically performing a verification test of each bus, the verification test of each bus comprising:

connecting a wire under test in a predetermined configuration relative to the other wire by measurement circuitry for performing the verification test;

measuring a value of an electrical parameter associated with the wire under test; and

comparing the value of the measured electrical parameter associated with the wire under test to an expected value of the electrical parameter, the wire under test passing the verification test in response to the measured value of the electrical parameter corresponding substantially to the expected value of the electrical parameter.

12. The method of claim 11, further comprising reading a configuration pin setting of the bus connector to determine an identification of the ~~apparatus~~ bus from the configuration pin setting.

13. The method of any of claims 11-12, wherein the verification test comprises:

detecting a short circuit between the wire under test and another wire of the bus;

detecting a short circuit between the wire under test and a ground potential; and

indicating that the wire passed the verification test in response to not detecting the short circuit between the wire under test and the other wire of the bus and not detecting the short circuit between wire under test and the ground potential.