

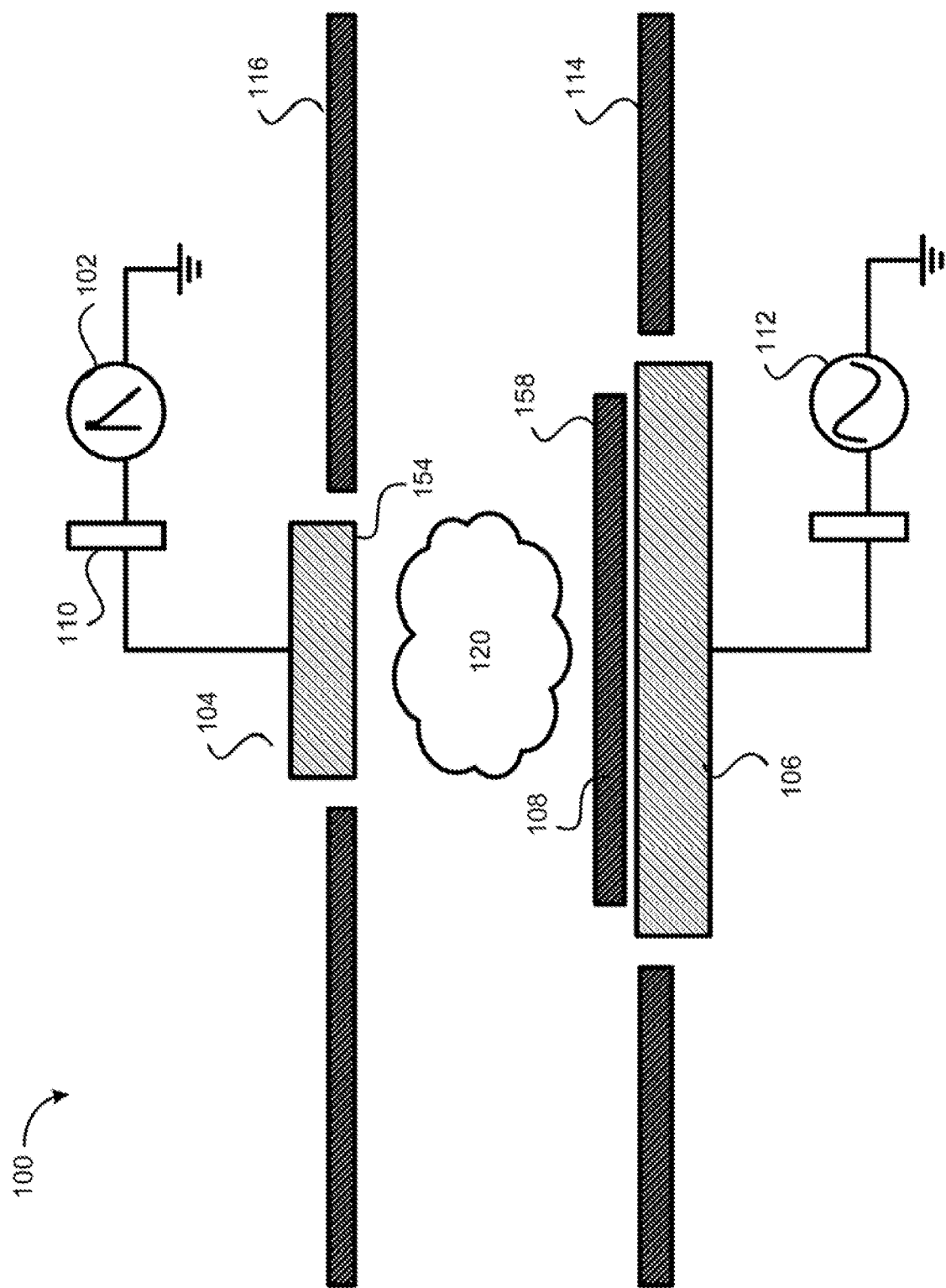


FIG. 1

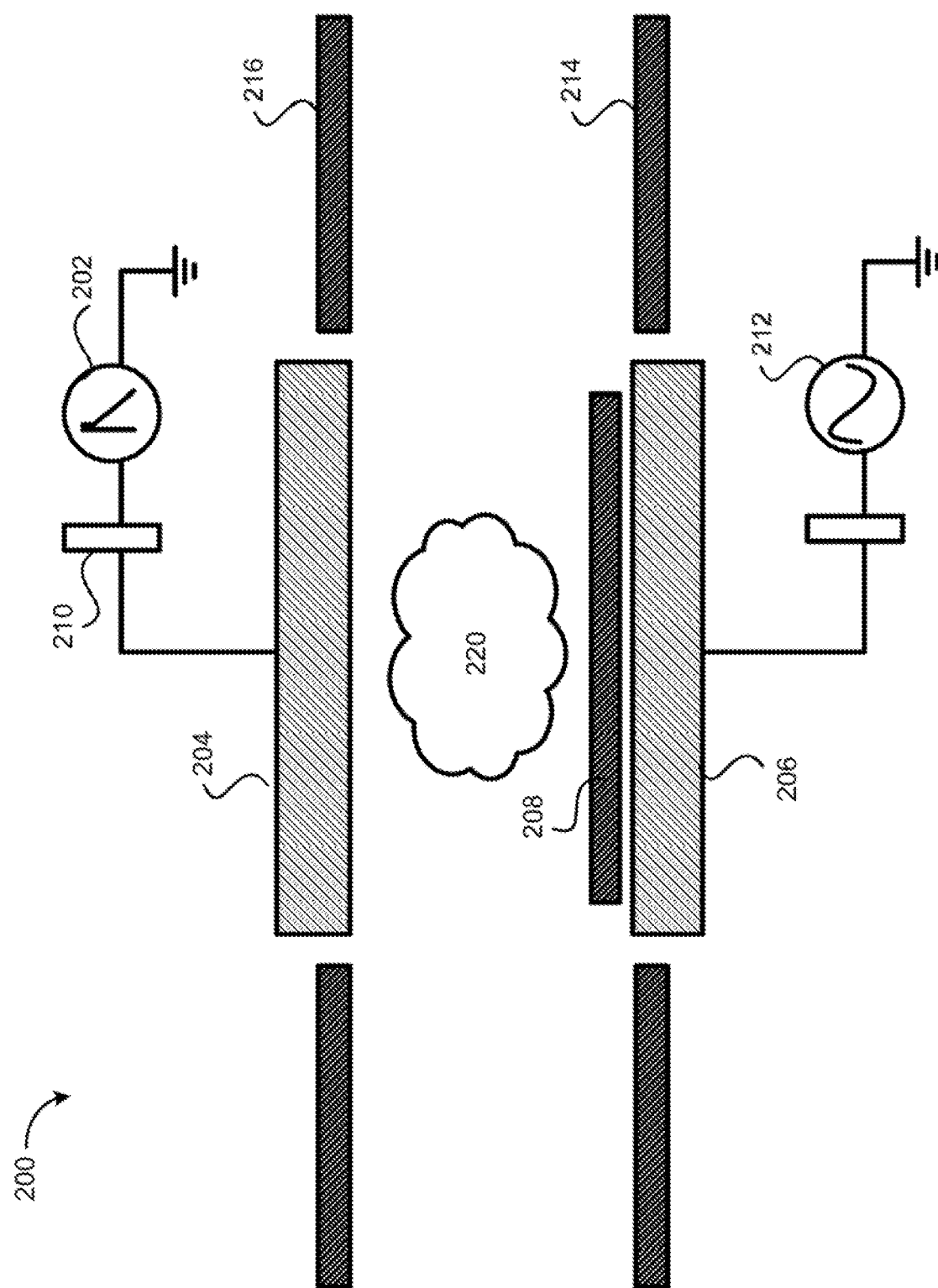


FIG. 2

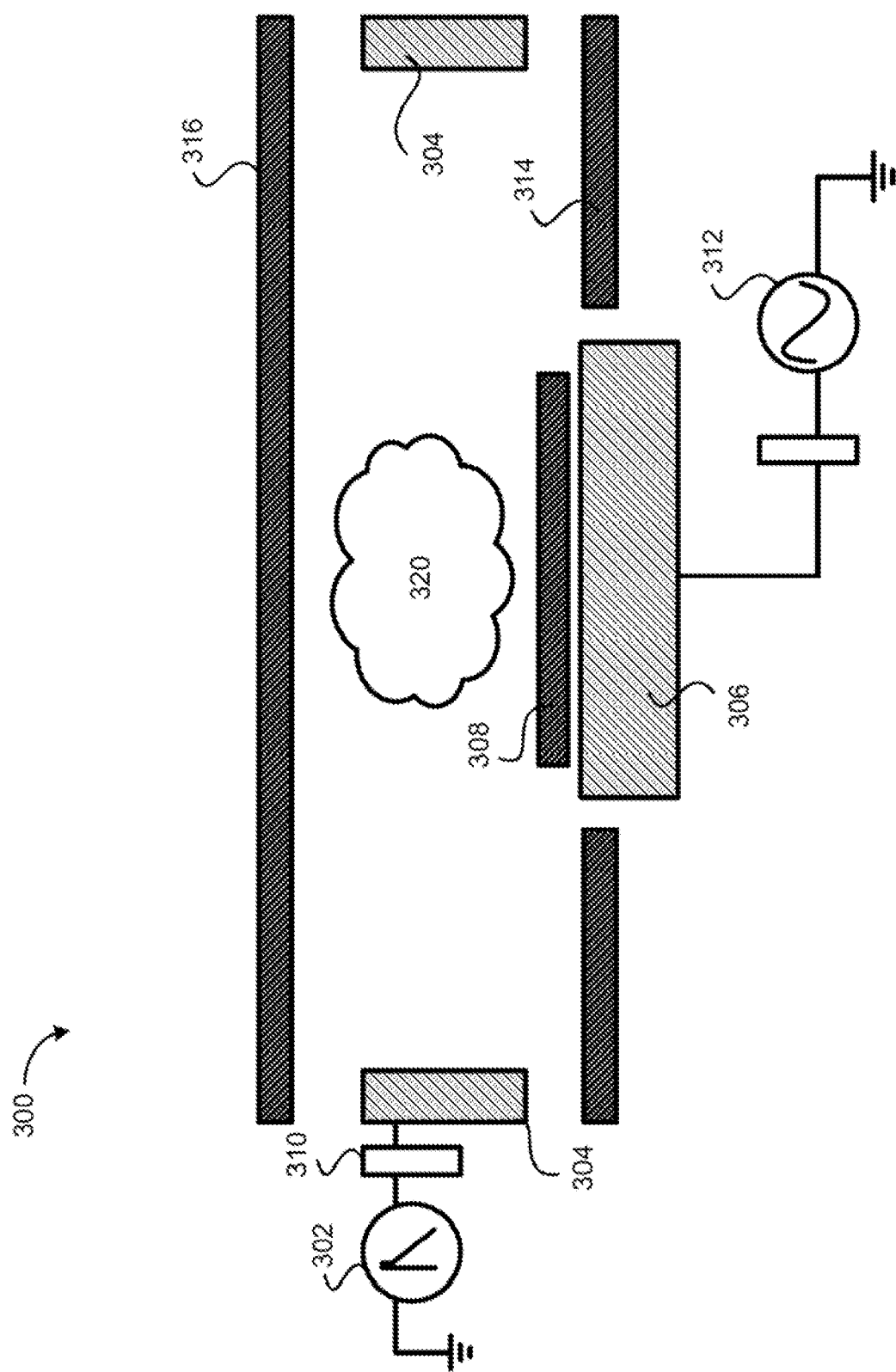


FIG. 3

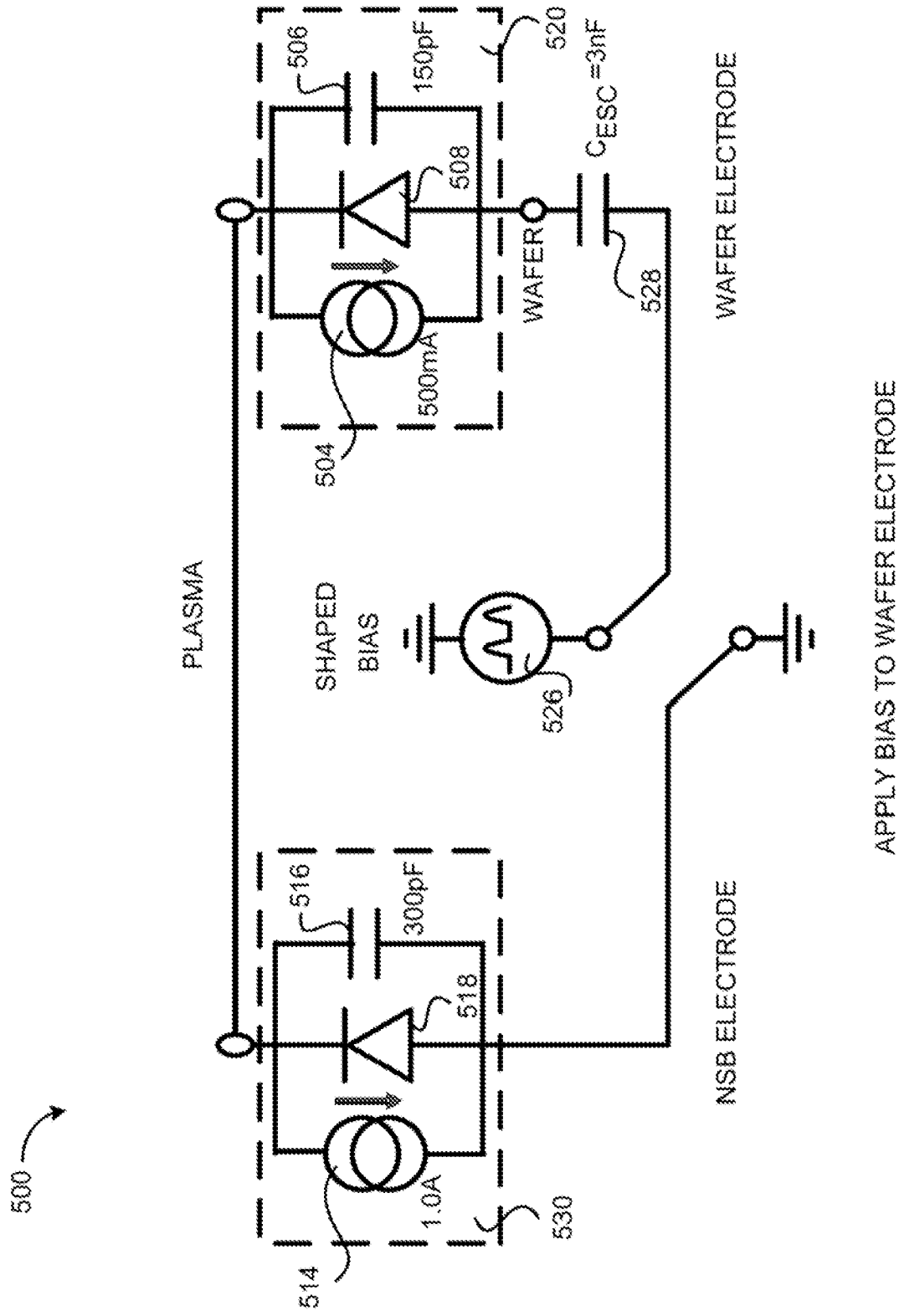


FIG. 4

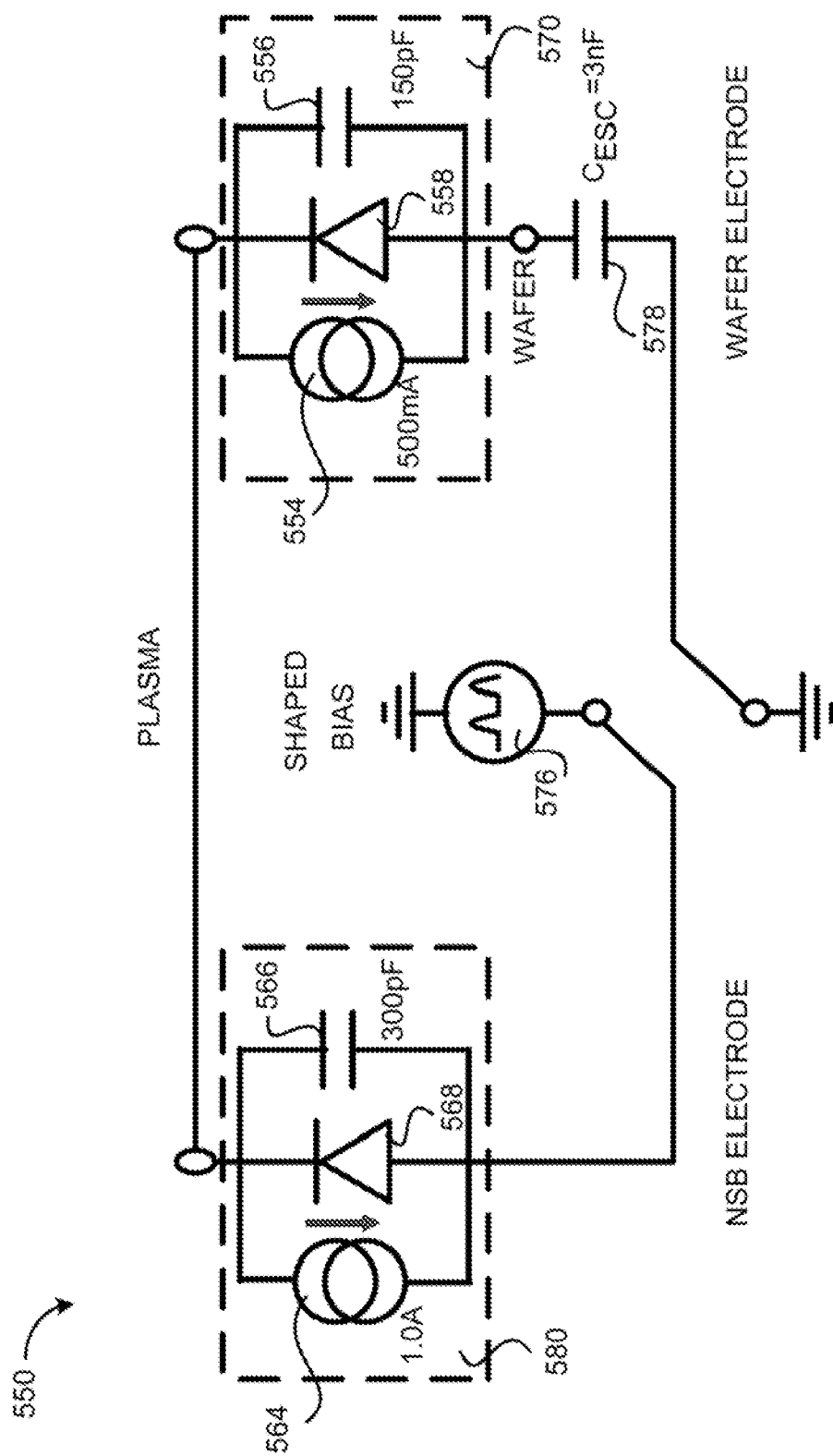


FIG. 5

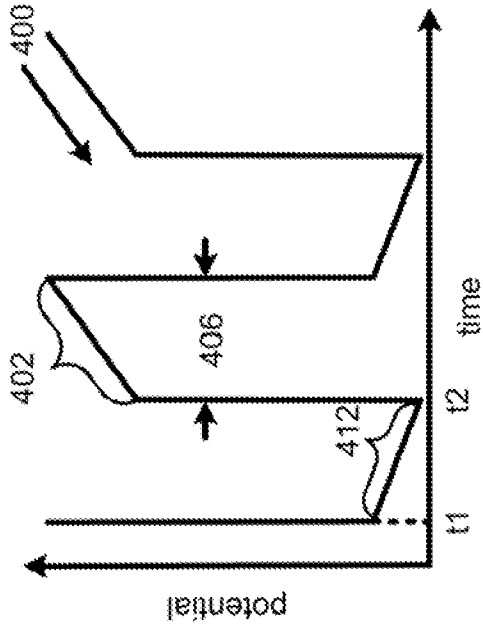


FIG. 6A

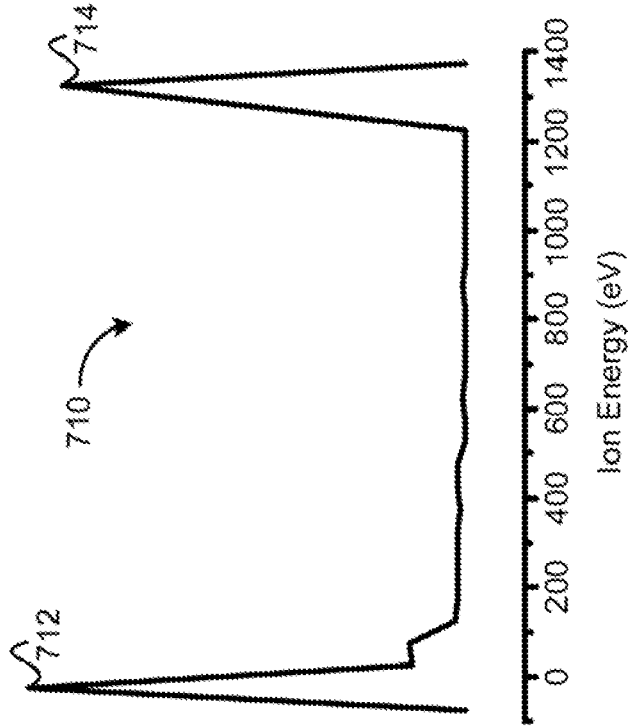


FIG. 6B

APPLY BIAS TO WAFER ELECTRODE

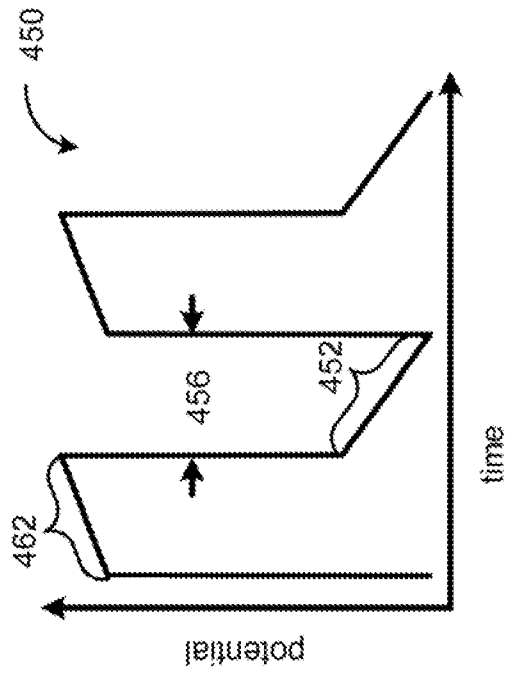


FIG. 7A

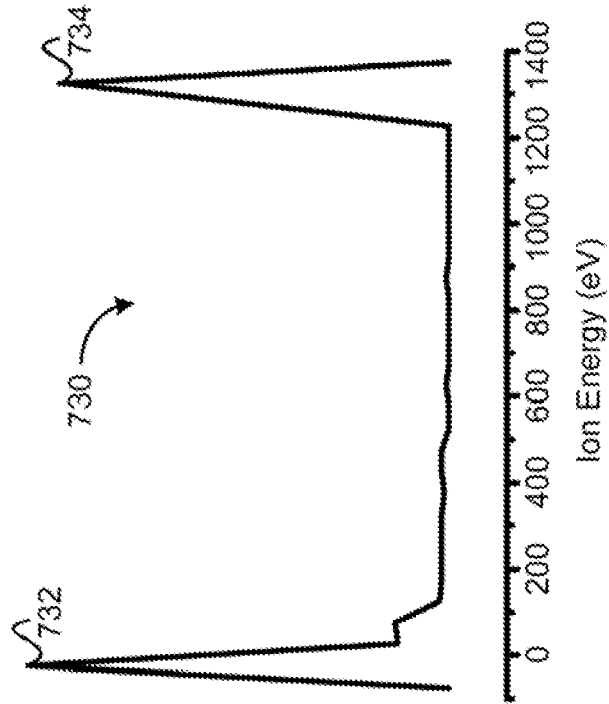


FIG. 7B

APPLY BIAS TO NSB ELECTRODE

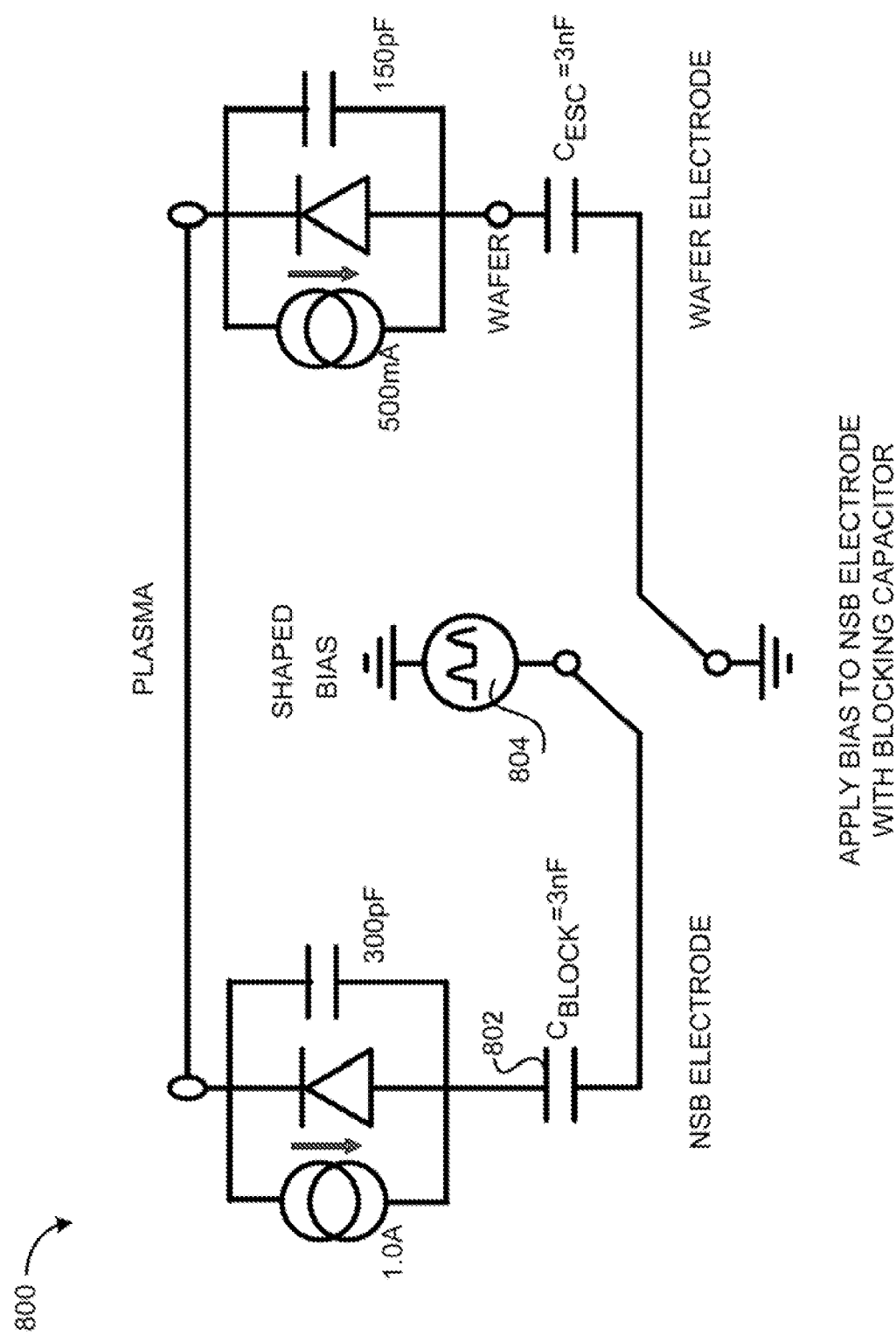


FIG. 8

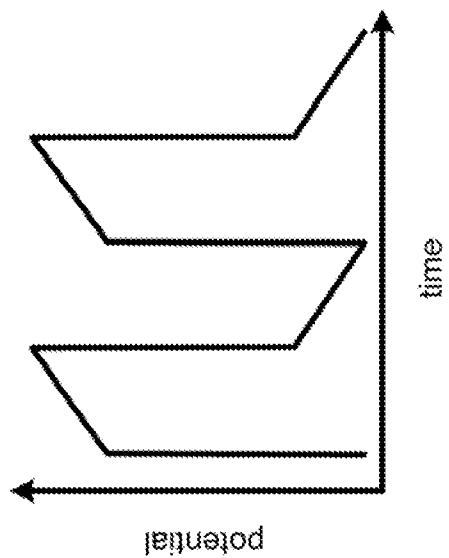


FIG. 9A

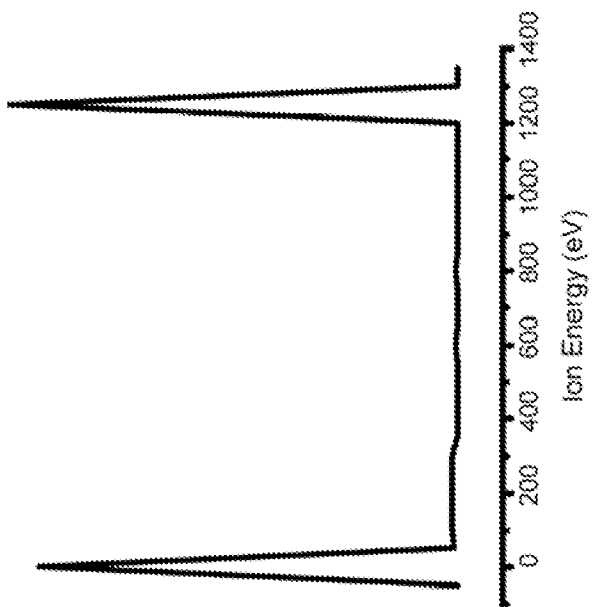


FIG. 9B

APPLY BIAS TO NSB ELECTRODE WITH
BLOCKING CAPACITOR