

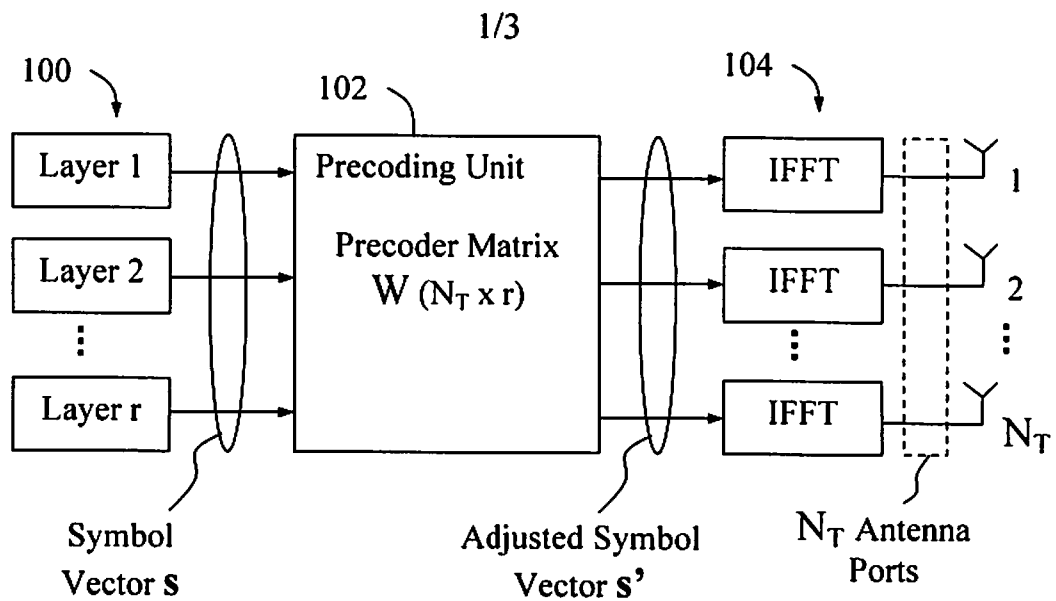


Fig. 1

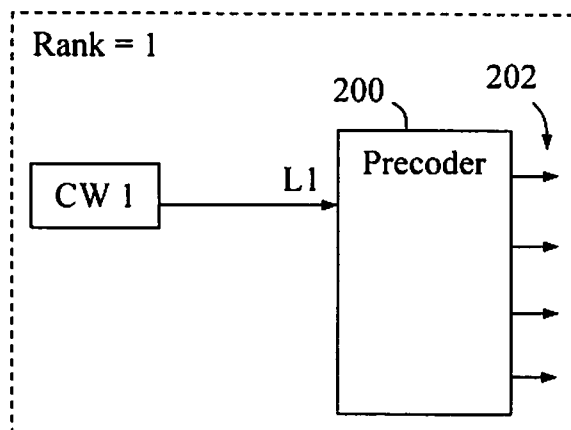


Fig. 2a

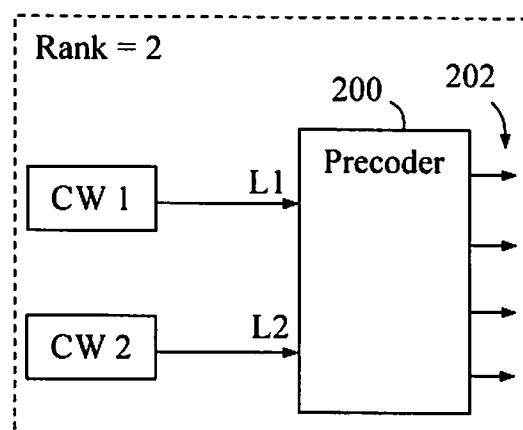


Fig. 2b

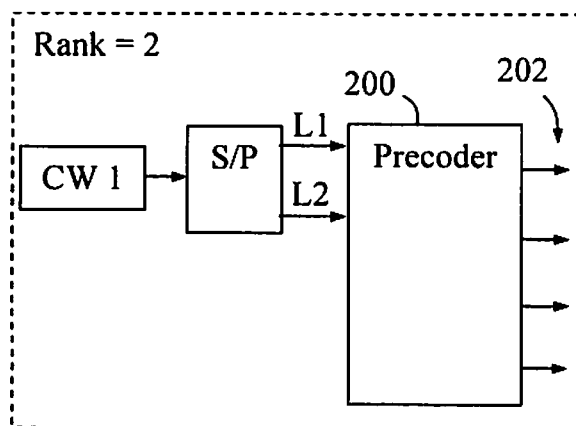


Fig. 2c

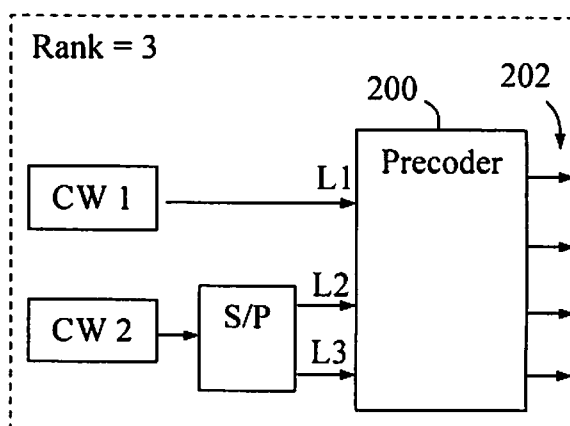


Fig. 2d

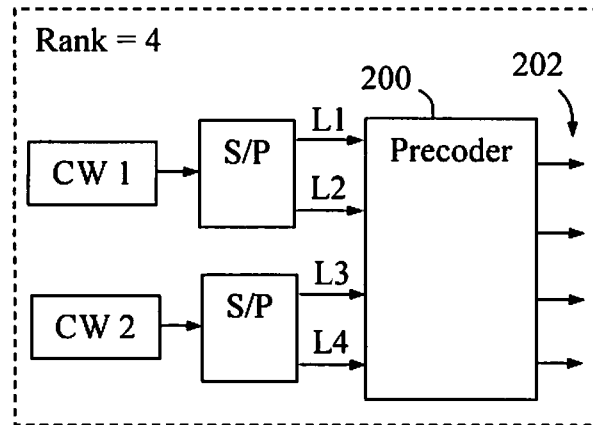


Fig. 2e

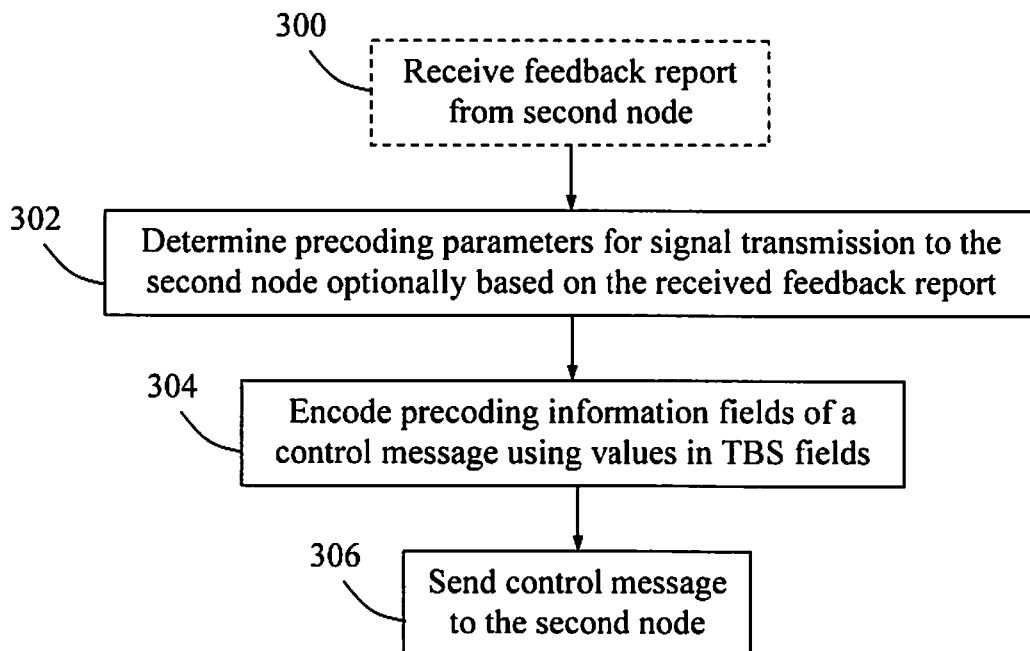


Fig. 3

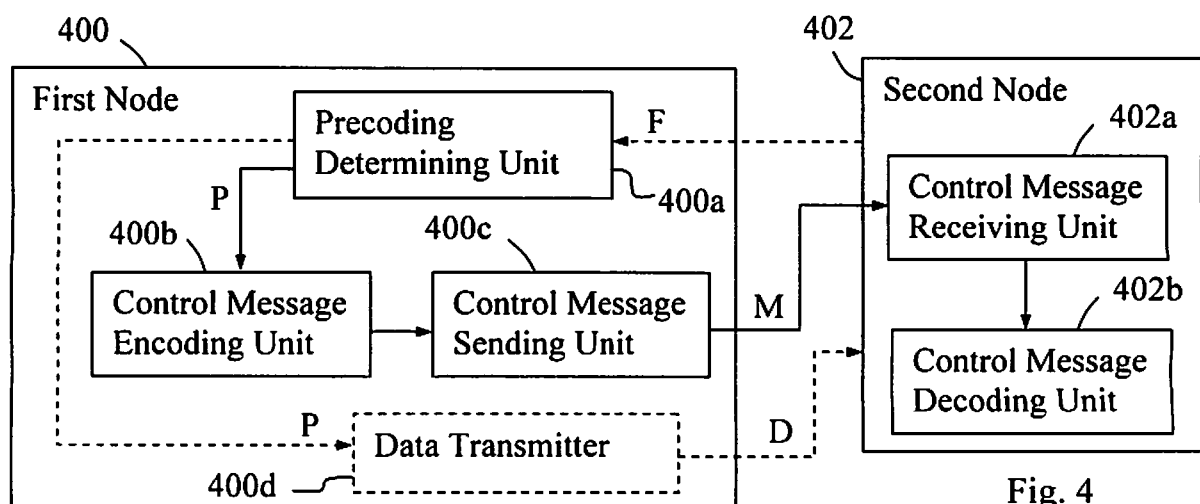


Fig. 4

	PDCCH field	# bits
1	Resource allocation	x
2	Tx power control	2
3	TBS 1	5
4'	TBS 2	5
5	NDI/RV 1	3
6'	NDI/RV 2	3
7	HARQ process ID	3
8''	PMI	4
9''	Precoder confirmation	1
10''	RI	2
11''	HARQ swap flag	1

Fig. 5