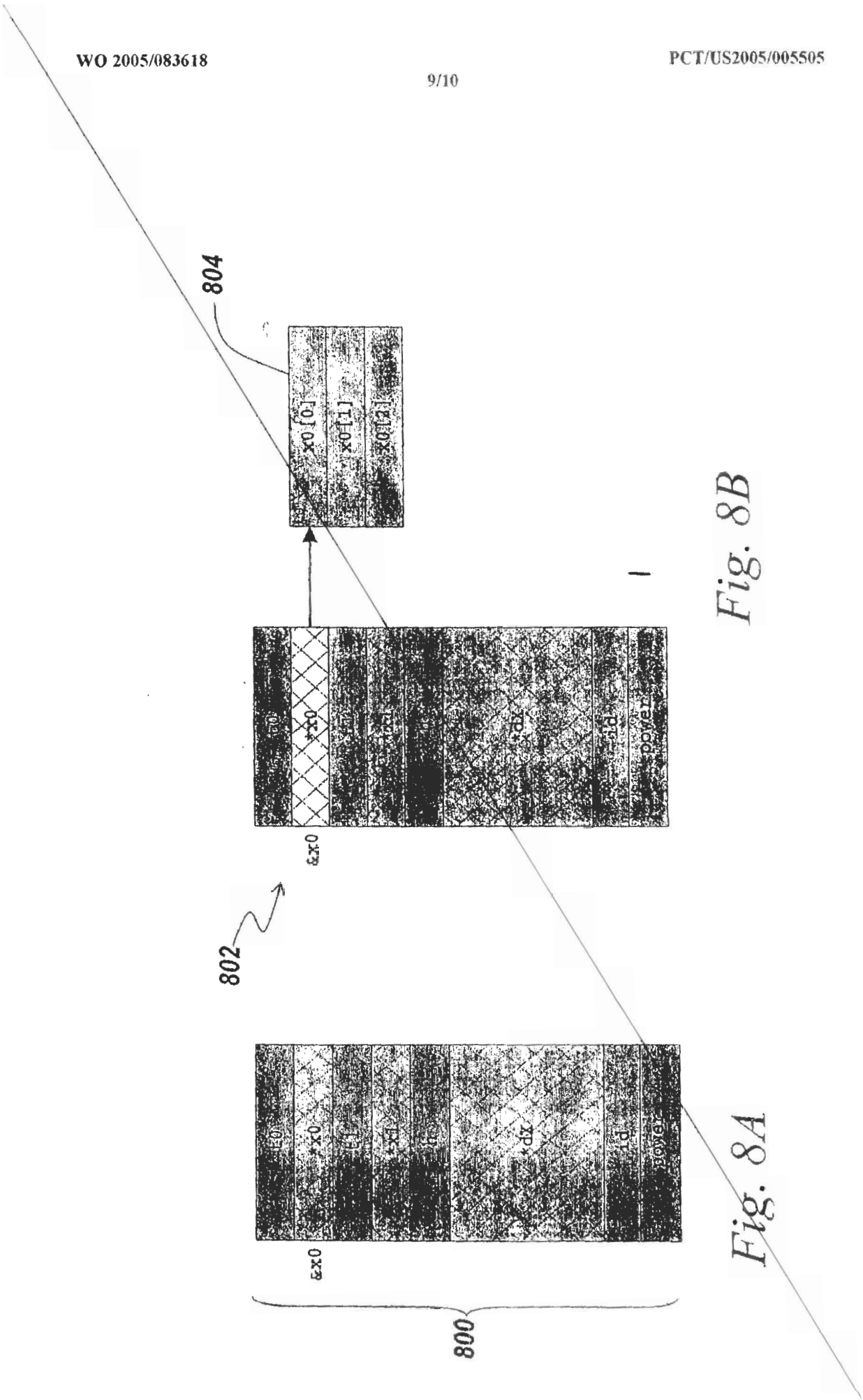


Fig. 8A

Fig. 8B

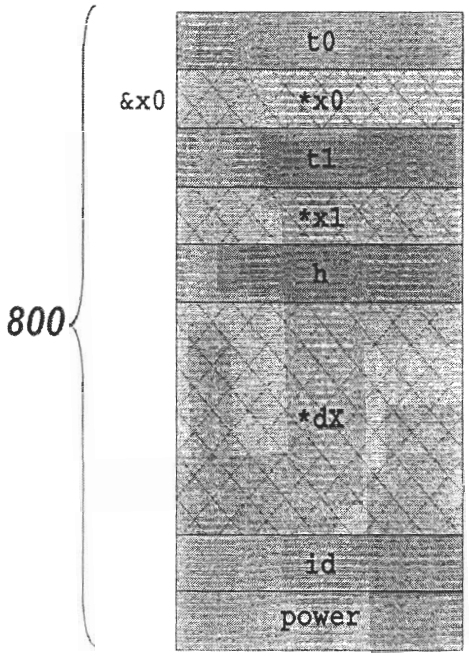

800
t0
&x0
*x0
t1
*x1
h
*dx
id
power

Fig. 8A

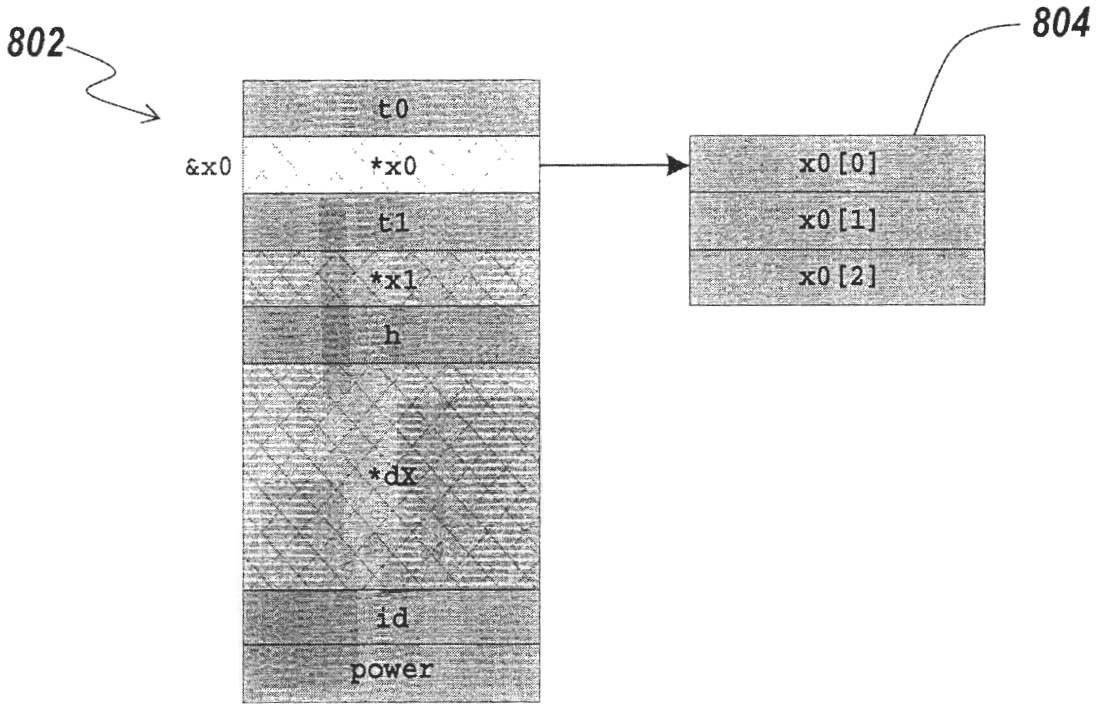

802
804
t0
&x0
*x0
t1
*x1
h
*dx
id
power
x0[0]
x0[1]
x0[2]

Fig. 8B