

ABSTRACT

SLEW RATE CONTROL OF OUTPUT DRIVERS USING FETS WITH DIFFERENT THRESHOLD VOLTAGES

A novel method and apparatus is presented for reducing the slew rate of transition edges of a digital signal (PAD) on a node (150, 250) of an integrated circuit by connecting transistors (N_{LV} , P_{LV} , N_{HV} , P_{HV}) with differing
5 threshold voltages between the node and a voltage source (V_{DD} , ground) and driving the gates of these transistors with the same driving signal.

FIGURE 2