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ABSTRACT

There is provided a display including pixels (2) arranged in a matrix, each pixel (2) including a voltage signal input terminal (E) to which a voltage signal is supplied, a drive control element (Tr) including a first terminal connected to a first power supply terminal (11), a control terminal, and a second terminal that outputs a current corresponding to a voltage between the first and second terminals, a capacitor (C1) connected between the voltage signal input terminal (E) and the control terminal, a current signal input terminal to which a current signal is supplied, a first switch (Sw5b) connected between the current signal input terminal and the control terminal, a second switch (Sw5a) connected between the current signal input terminal and the second terminal, an output control switch (Sw2) whose input terminal is connected to the second terminal, and a display element (20) connected between a second power supply terminal and an output terminal of the output control switch (Sw2).