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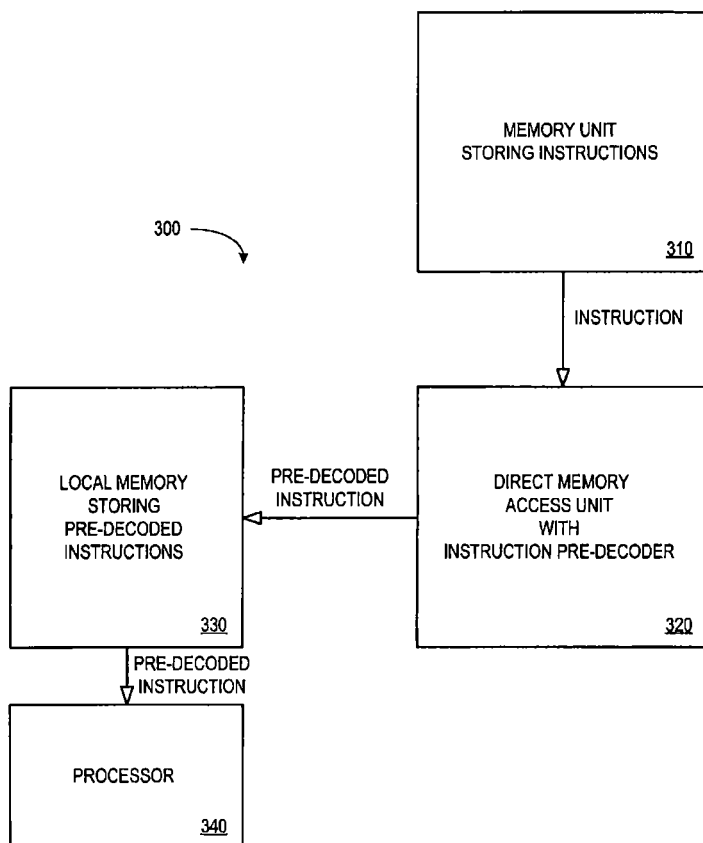
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(54) Title: DIRECT MEMORY ACCESS UNIT WITH INSTRUCTION PRE-DECODER

(57) Abstract: According to some embodiments, an instruction is pre-decoded at a direct memory access unit.



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ACTION

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