What is claimed is:

1. A solid state drive, comprising:

first and second memories of different types than each other; and

a memory controller which is configured to control the first and second memories,

wherein the memory controller is configured to receive a metadata access request from a host, and includes a condition checker which is configured to determine conditions of the first and second memories in response to the metadata access request and to select at least one of the first and second memories based on at least one of the conditions, and

wherein the memory controller is configured to access the at least one of the first and second memories selected by the condition checker,

wherein the metadata of the first and second memories are mirror data having same contents.

2. The solid state drive of claim 1, wherein the condition checker is configured to determine conditions of the first and second memories in consideration of at least one of a temperature, a number of program/erase (P/E) cycles, a program retention time and an erase retention time.

3. The solid state drive of claim 1 or claim 2, wherein each of the first and second memories is one of a NAND flash memory, a PRAM, an RRAM and a MRAM.

4. The solid state drive of any one of claims 1 to 3, wherein the metadata

access request is a metadata read request,

wherein the condition checker is configured to select one of the first and second memories in response to metadata read request, and

wherein the memory controller is configured to read the metadata in the memory selected by the condition checker among the first and second memories.

5. The solid state drive of any one of claims 1 to 3, wherein the metadata access request is a metadata program request, and

the memory controller programs metadata in the memory among the first and second memories selected by the condition checker.

6. The solid state drive of claim 5, wherein the memory controller is configured to first program the metadata in the memory selected by the condition checker among the first and second memories, and

wherein the metadata is programmed in a memory which is not selected by the condition checker when the condition of the memory which is not selected by the condition checker among the first and second memories becomes better.

7. The solid state drive of any one of claims 1 to 6, wherein the second memory includes first and second channel memories connected to the memory controller via different channels.

8. The solid state drive of claim 7, wherein the condition checker is configured to determine the conditions of each of the first memory, the first channel memory and the second channel memory in response to the metadata access request,

and to select one of the first memory, the first channel memory and the second channel memory.

9. The solid state drive of any one of claims 1 to 8, further comprising:

a third memory of a different type than the first and second memories,

wherein the condition checker is configured to select at least one of the first and second memories and third memory in response to the metadata access request, and

the memory controller is configured to access the memory selected by the condition checker.

10. The solid state drive of claim 9, wherein the metadata access request is a metadata program request,

the condition checker is configured to select remaining memories other than the selected at least one of the first and second memories and third memory, and

the memory controller is configured to program the metadata in the memory selected by the condition checker.

11. The solid state drive of claim 10, wherein the memory controller is configured to first program the metadata in the memory selected by the condition checker among the first and second memories and third memory, and

when the condition of a memory which is not selected by the condition checker among the first and second memories and third memory becomes better, the memory controller programs the metadata in the memory which is not selected by the condition checker.

12. The solid state drive of any one of claims 1 to 11, wherein the metadata includes at least one of security metadata, firmware, metadata, and debug metadata.

13. A solid state drive, comprising:

a first memory which is configured to store first metadata;

a second memory which is configured to store second metadata identical to the first metadata, wherein the second memory is of a different type than the first memory; and

a memory controller which is configured to control the first memory and the second memory, and includes a condition checker configured to determine conditions of the first memory and the second memory and to select a one of the first memory and the second memory which has a better condition,

wherein the memory controller is configured to receive a metadata read request from a host, and to read one of the first metadata and the second metadata stored in the memory selected by the condition checker in response to the metadata read request.

14. The solid state drive of claim 13, wherein the condition checker is configured to receive current status information of the first memory and the second memory, to determine conditions of the first memory and the second memory based on the current status information, and

wherein the current status information includes at least one of a temperature, a number of program/erase (P/E) cycles, a program retention time and an erase retention time of the first memory and the second memory.

15. The solid state drive of claim 14, wherein the condition checker includes

a characteristic table which is configured to store characteristic values corresponding to the current status information for each of the first memory and the second memory.

16. The solid state drive of claim 14 or claim 15, wherein the condition checker includes characteristic parameters corresponding to the current status information for each of the first memory and the second memory.

17. A method, comprising:

a solid state drive (SSD) receiving a metadata read request from a host, wherein the SSD includes at least first and second memories of different types than each other;

determining conditions of the first and second memories to select one of the first and second memories; and

reading metadata of a selected memory among the first and second memories,

wherein the metadata of the first memory is a mirror data of the metadata of the second memory.

18. The method of claim 17, further comprising:

receiving a metadata program request from the host;

determining the conditions of each of the first and second memories to select at least one of the first and second memories; and

programming the metadata in a selected memory among the first and second memories.

19. The method of claim 18, further comprising:

after programming the metadata in the selected memory among the first and

second memories, when the condition of a non-selected memory among the first and second memories becomes better, programming the metadata in the non-selected memory among the first and second memories.

20. The method of claim 18 or claim 19, wherein the second memory includes first and second channel memories having different channels, and

wherein determination of the conditions of the first and second memories and selection of one of them includes:

determining the conditions of each of the first memory and the first and second channel memories and selecting one of the first memory and the first and second channel memories.