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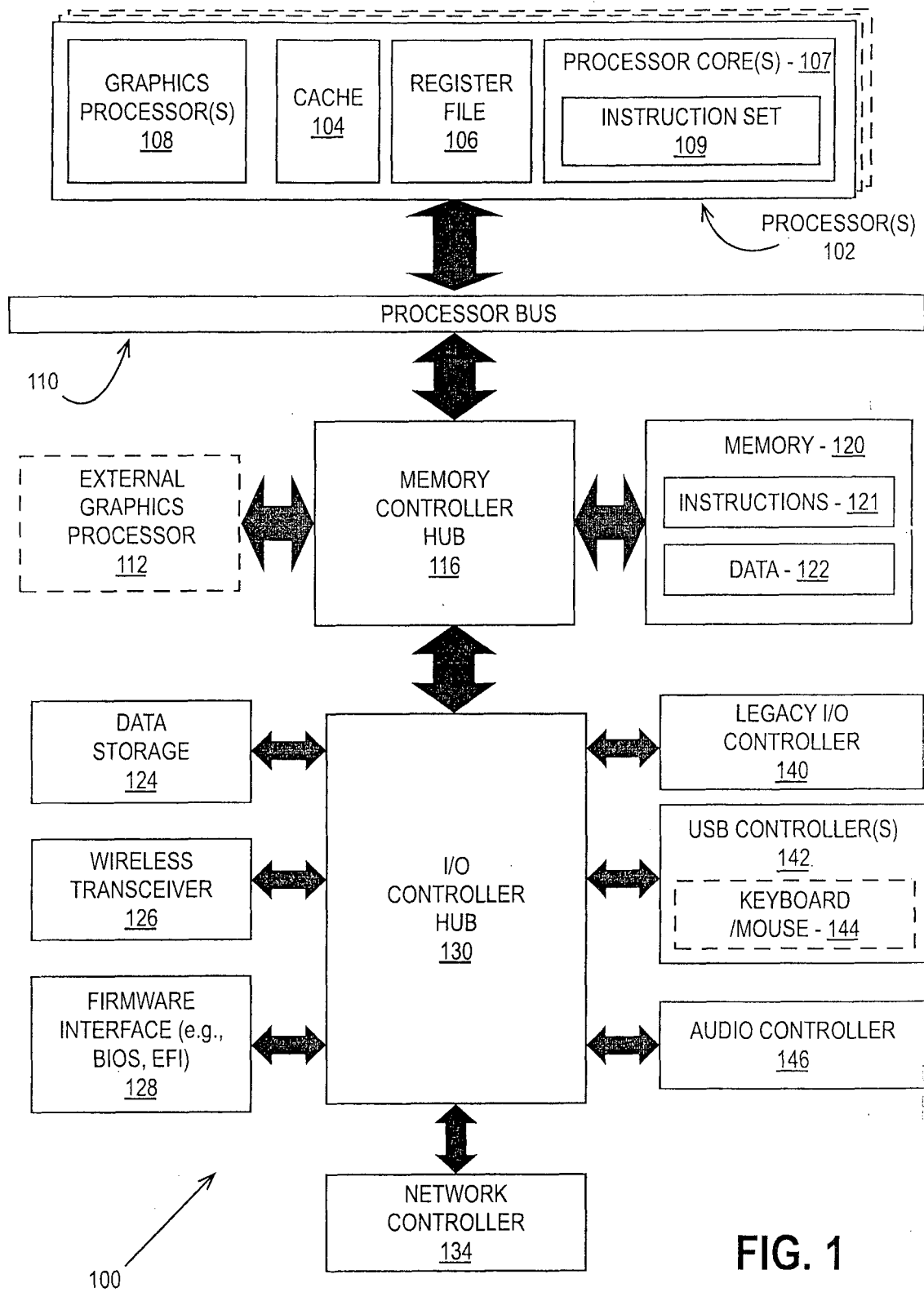


FIG. 1

PROCESSOR 200

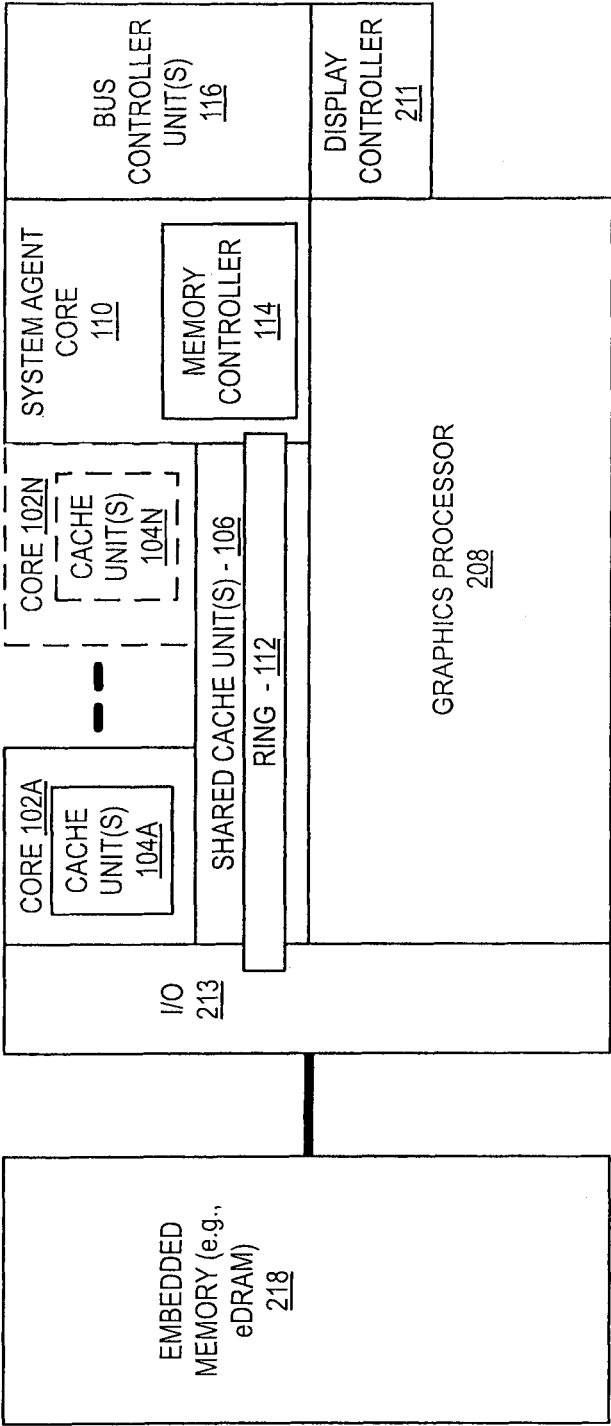


FIG. 2

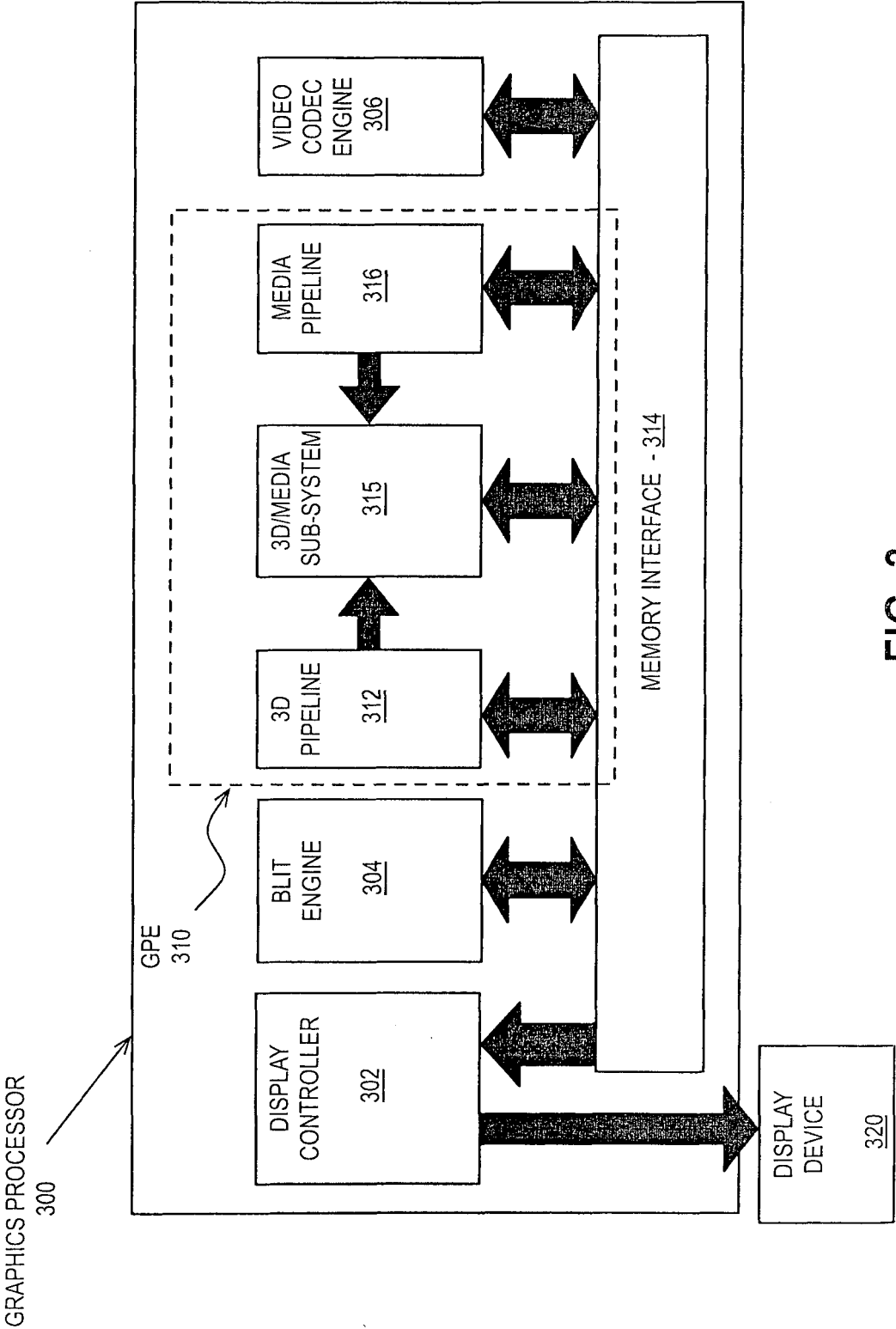


FIG. 3

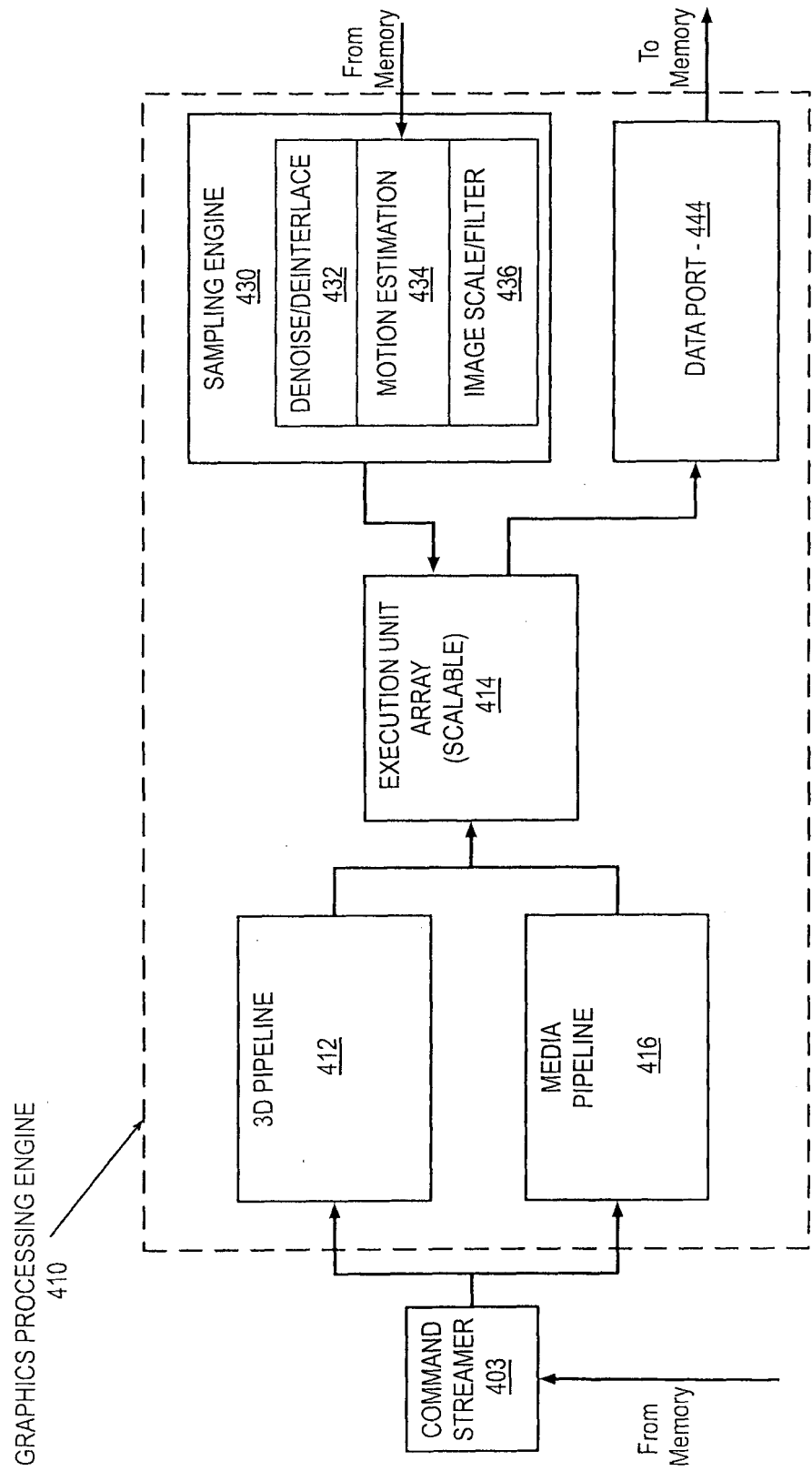


FIG. 4

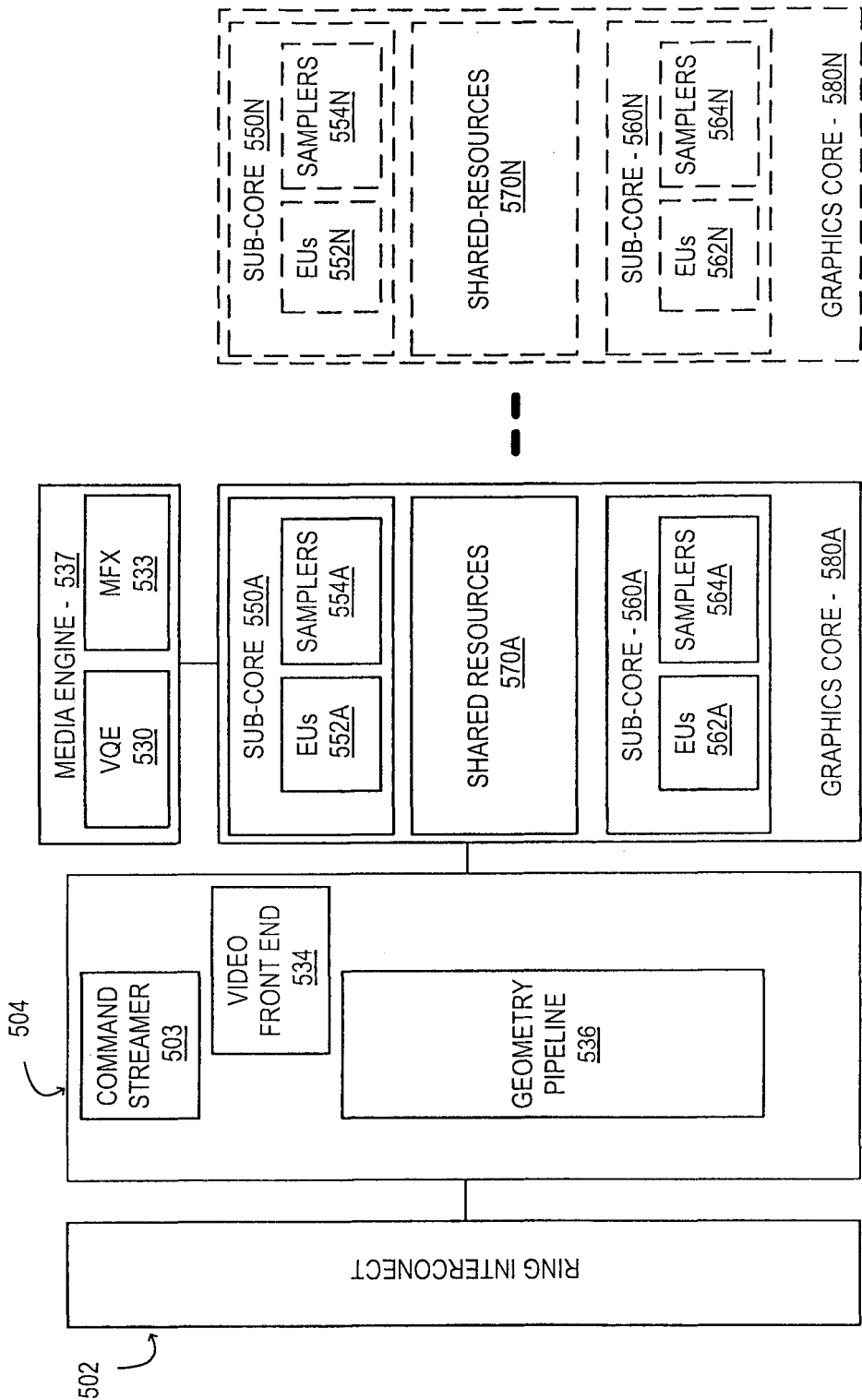


FIG. 5

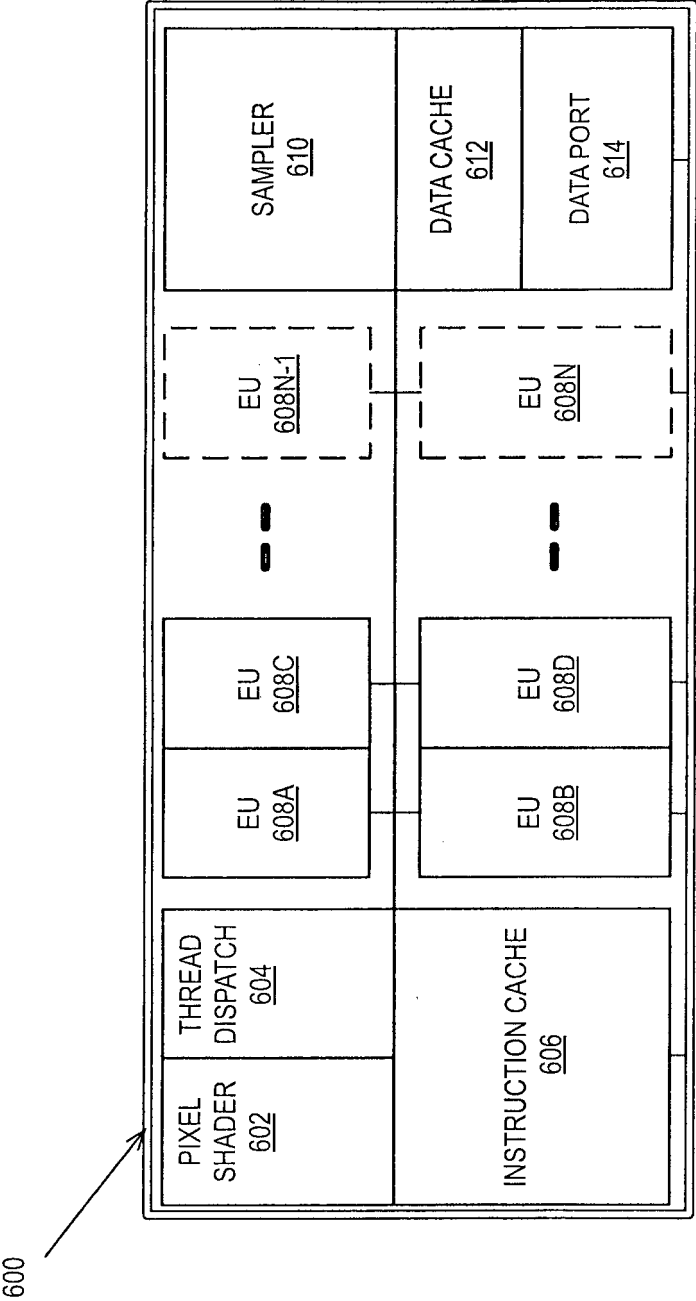
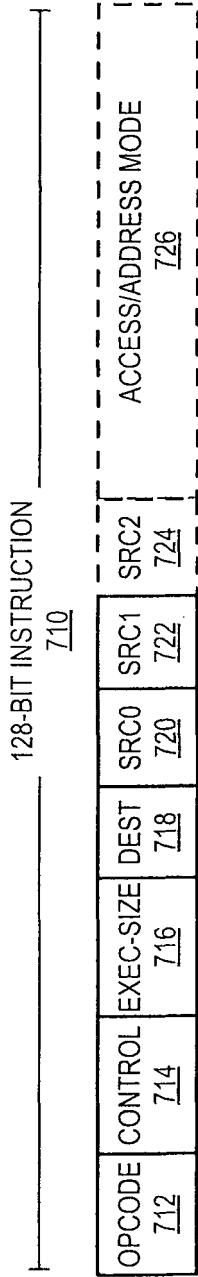


FIG. 6

GRAPHICS CORE INSTRUCTION FORMATS
700



64-BIT COMPACT INSTRUCTION 730



OPCODE DECODE 740

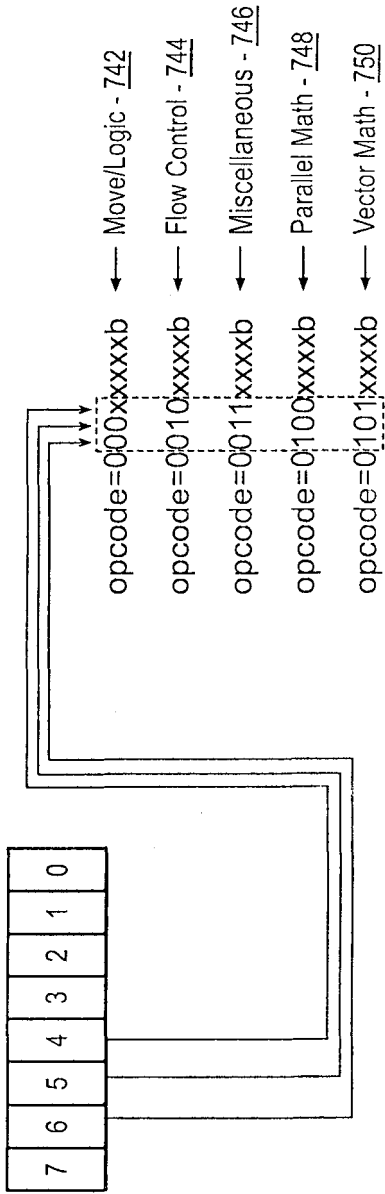


FIG. 7

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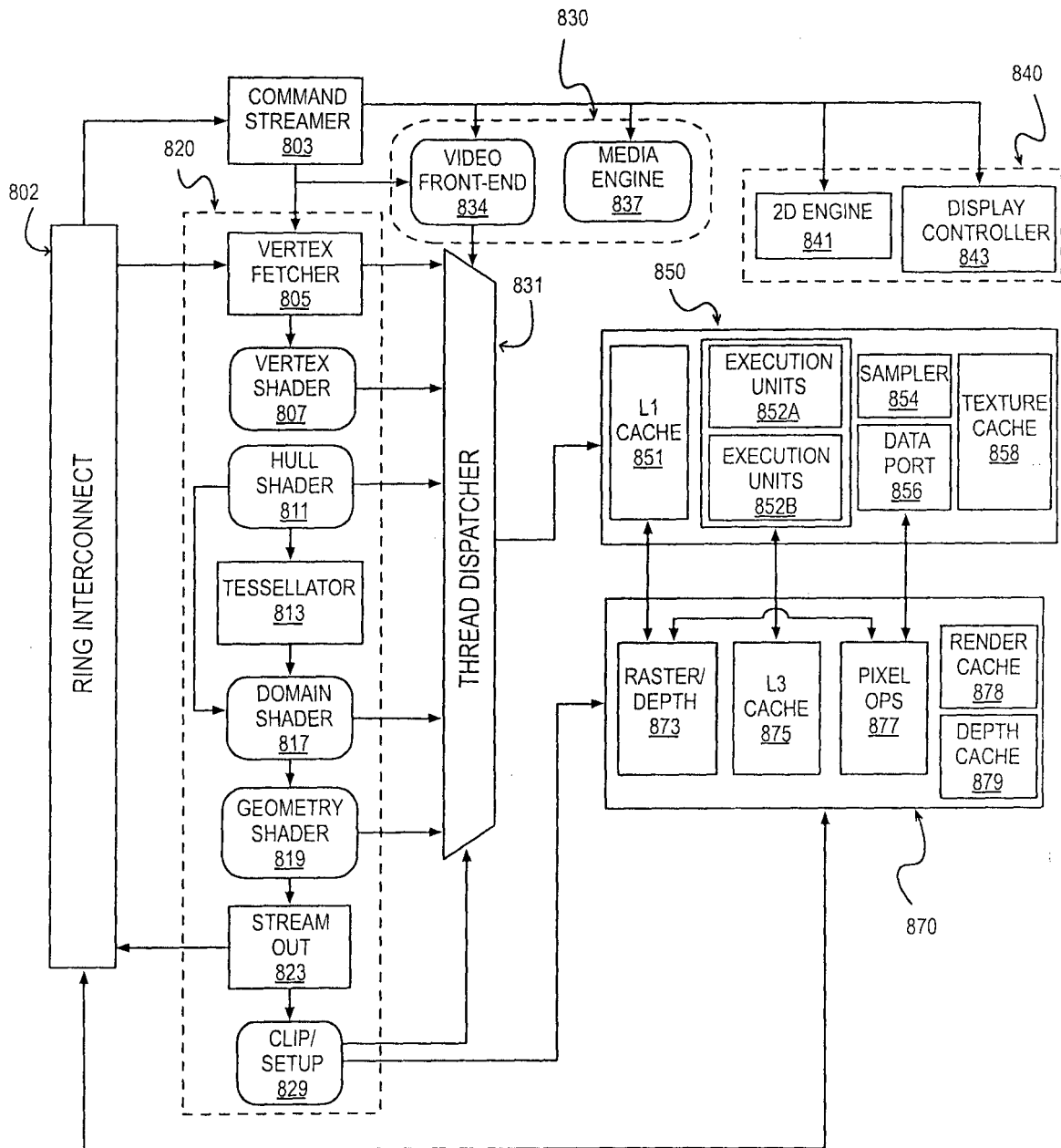


FIG. 8

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FIG. 9A

SAMPLE COMMAND FORMAT

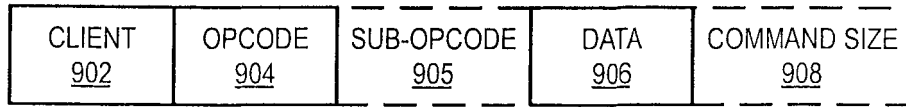
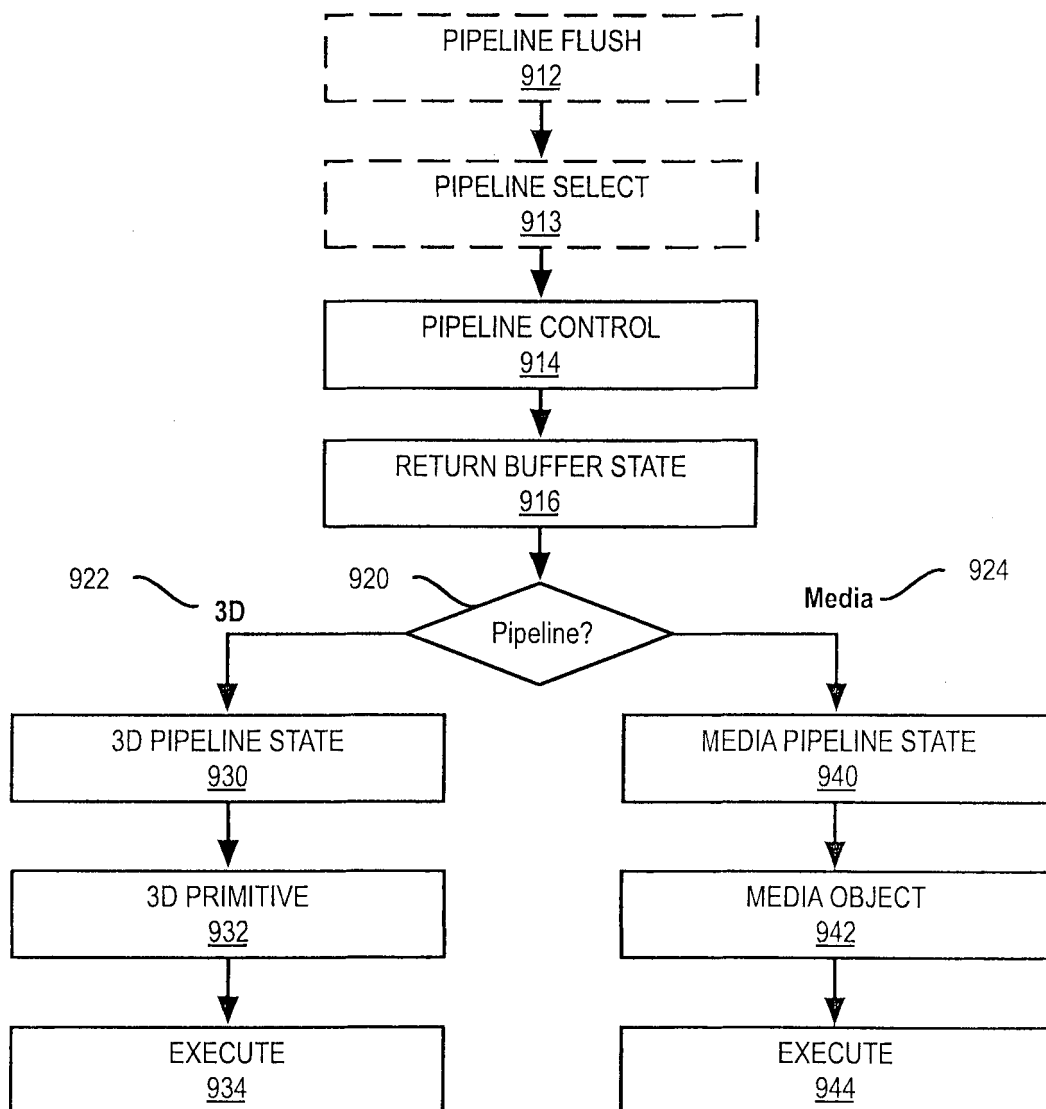
900

FIG. 9B

SAMPLE COMMAND SEQUENCE

910

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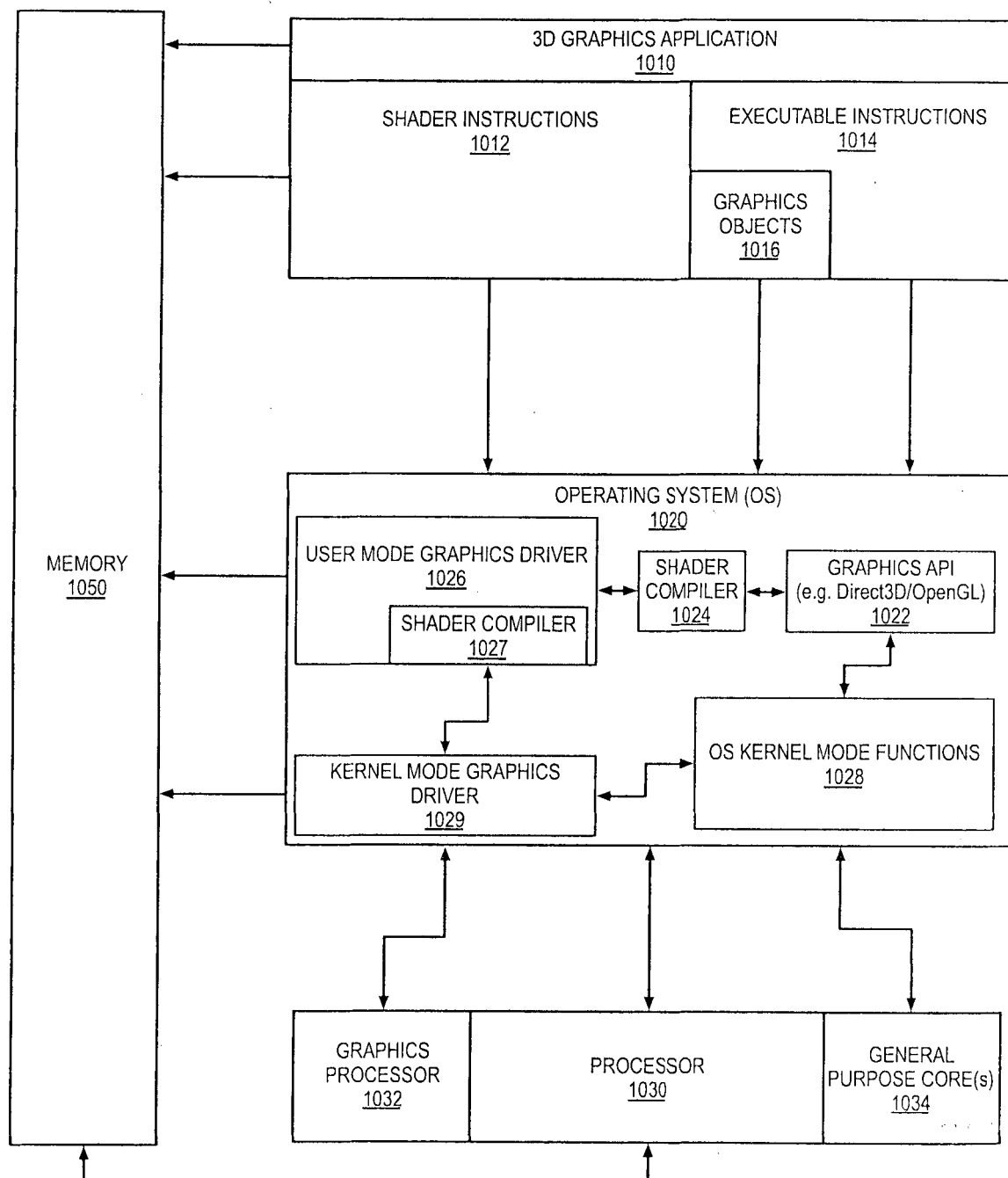


FIG. 10

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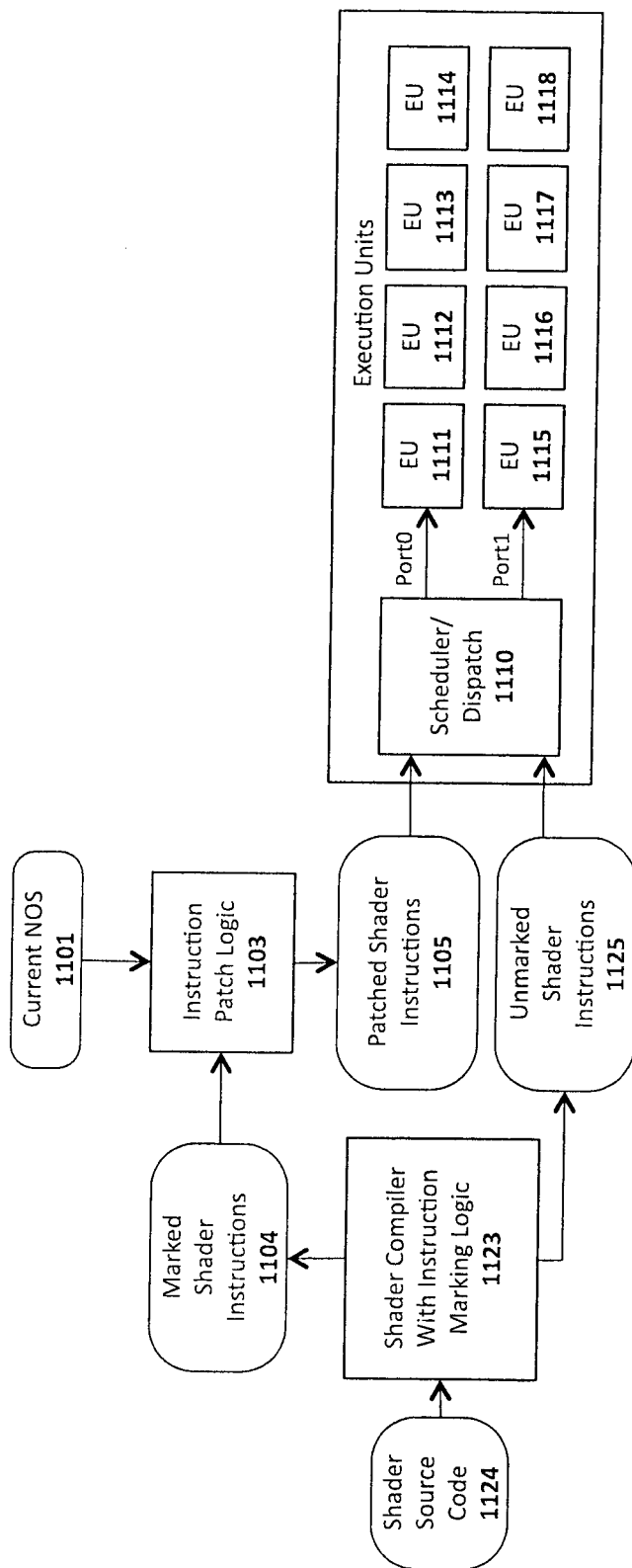


FIG. 11

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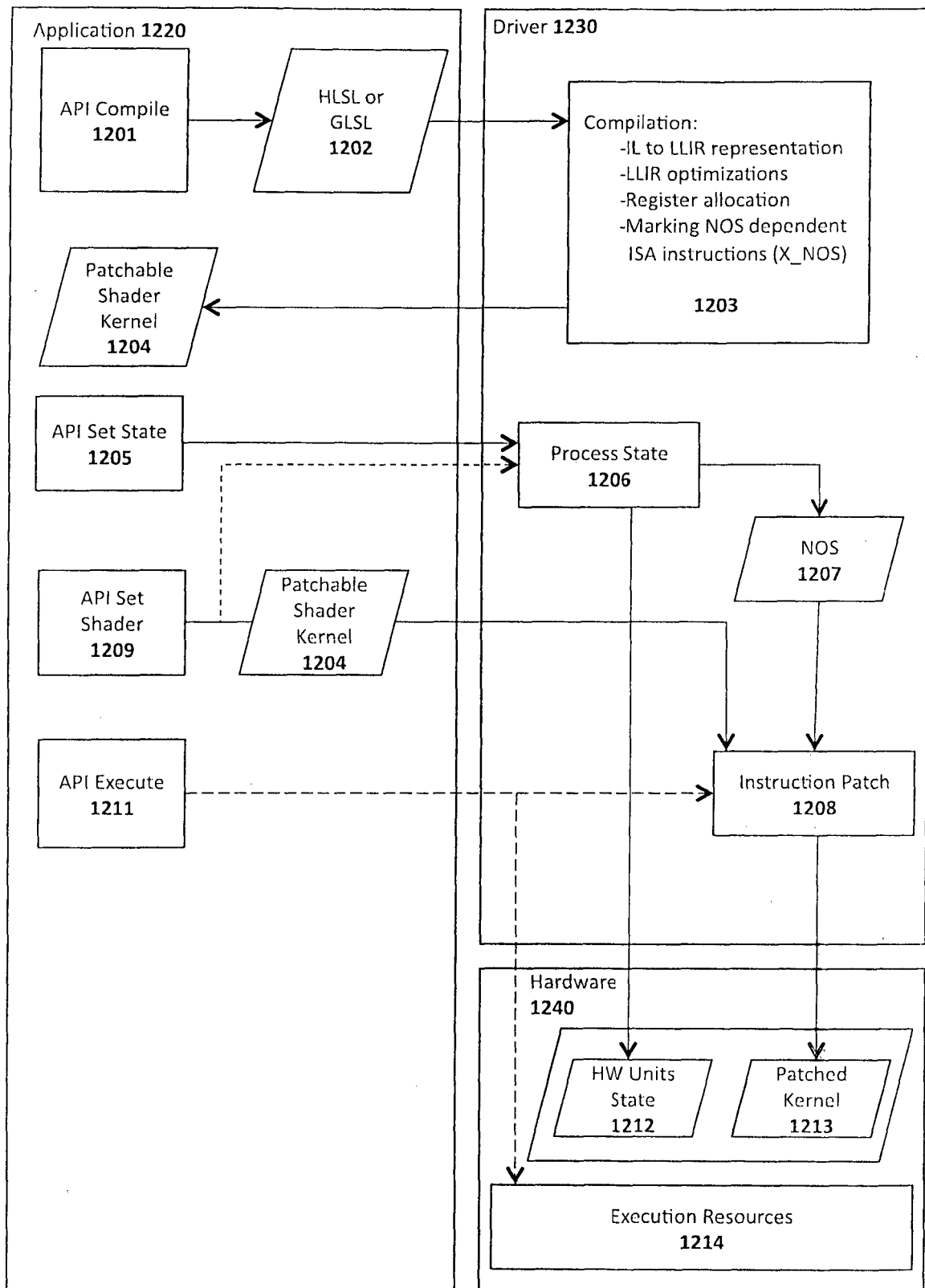


FIG. 12

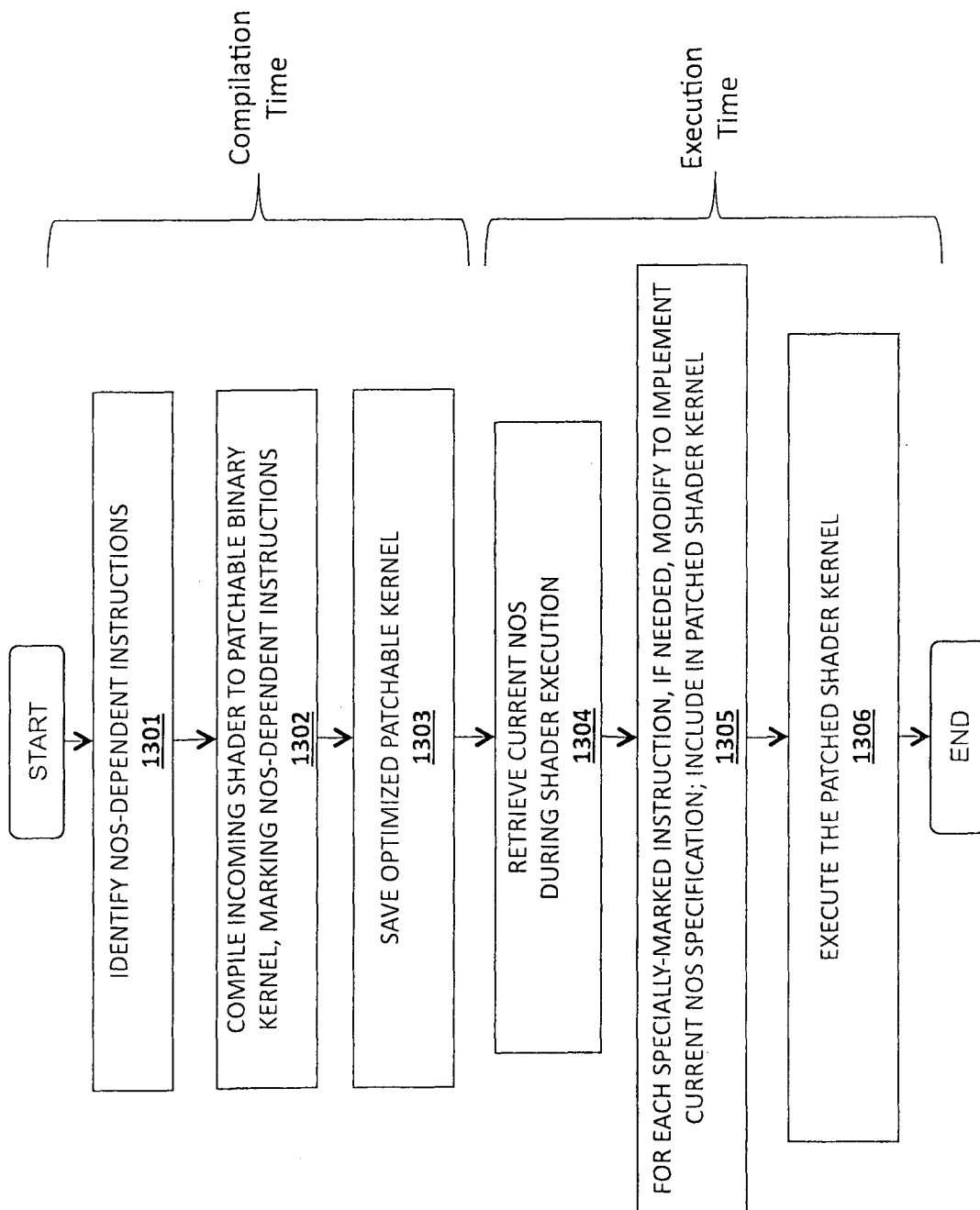


FIG. 13