

FIG. 1

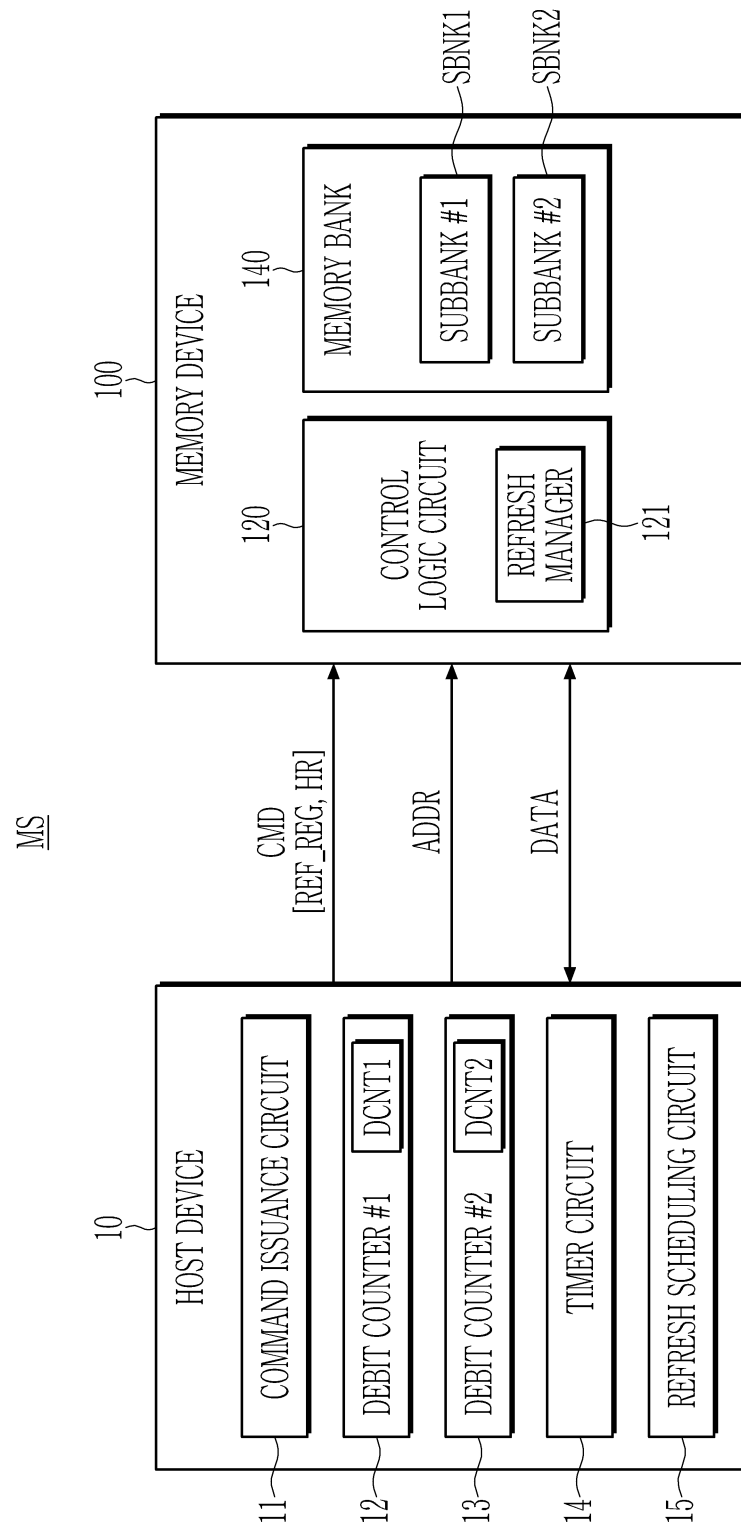


FIG. 2

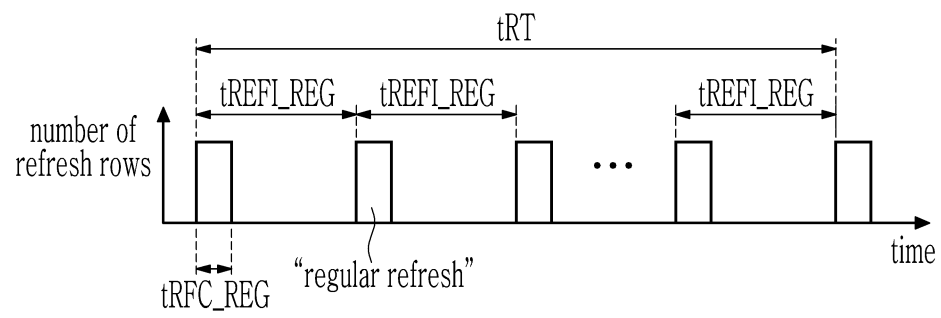


FIG. 3

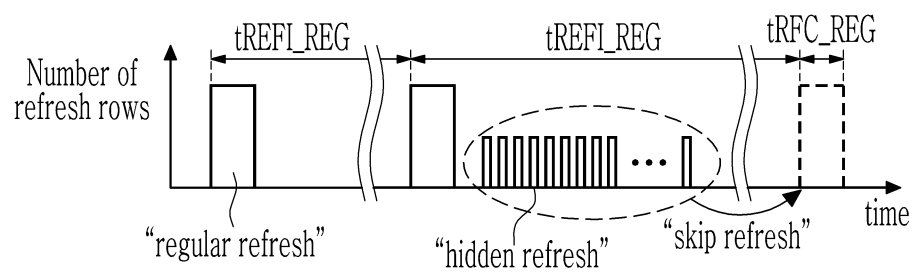


FIG. 4

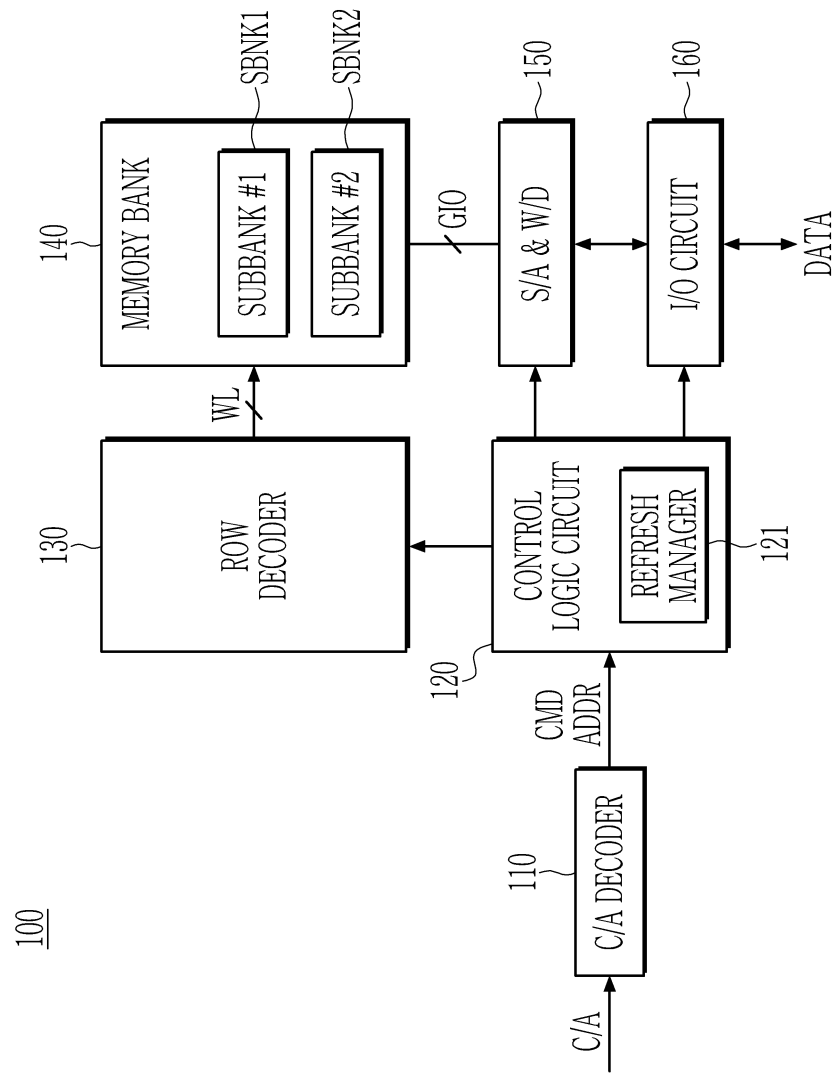


FIG. 5

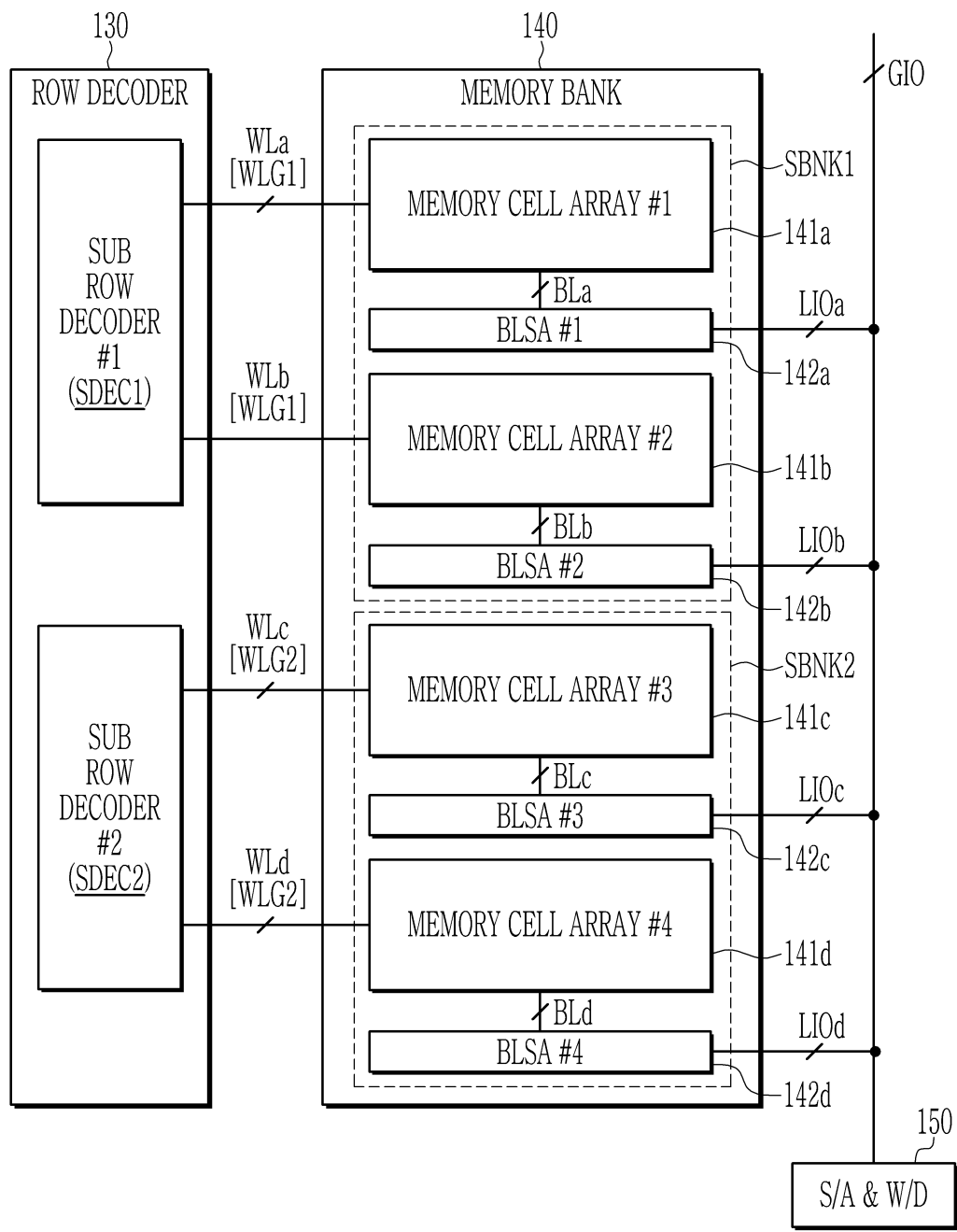


FIG. 6

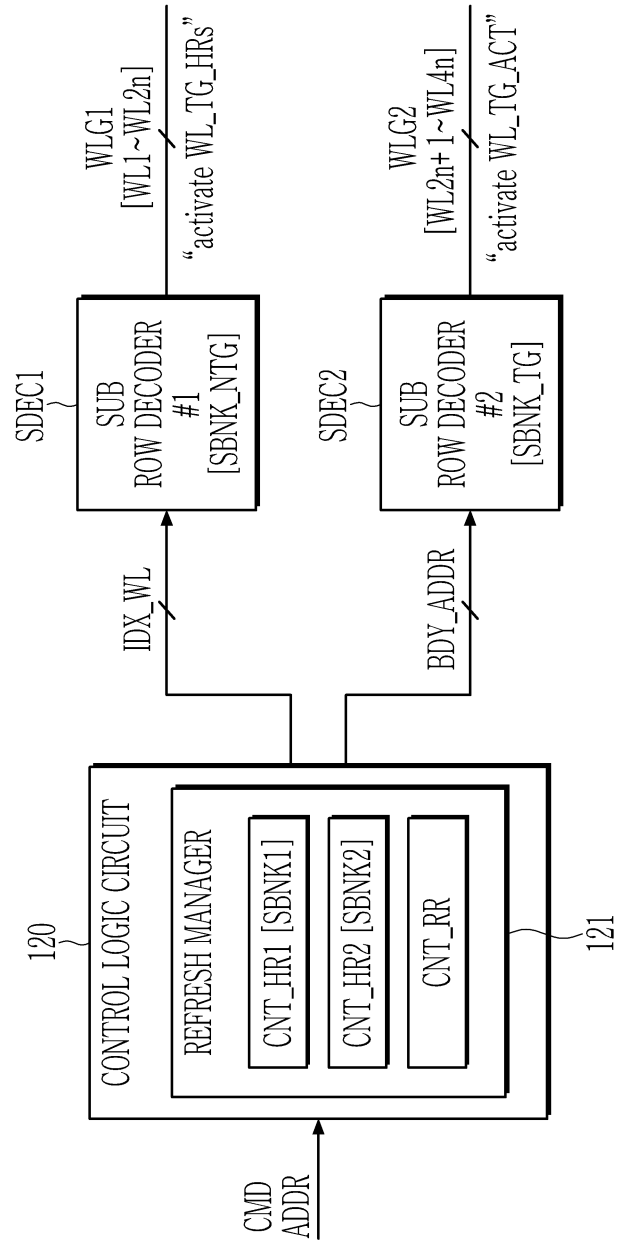


FIG. 7

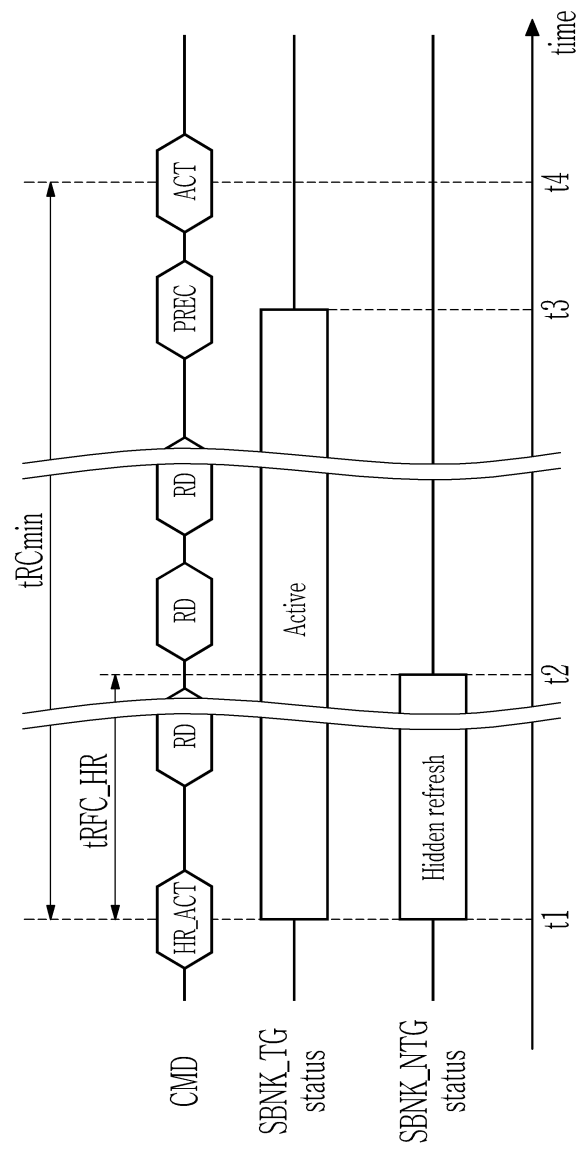


FIG. 8

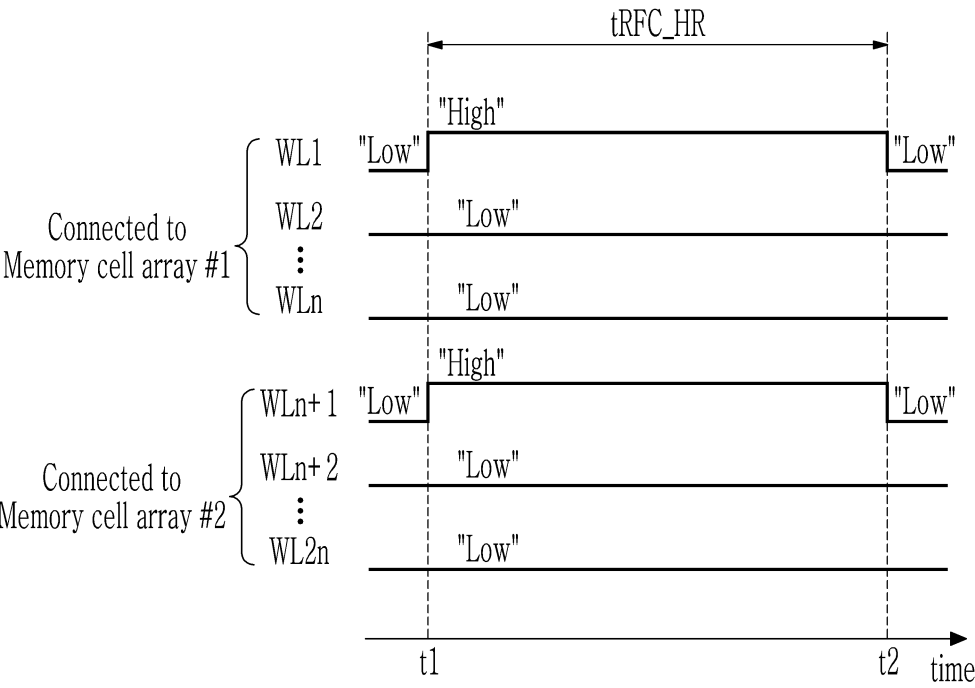


FIG. 9

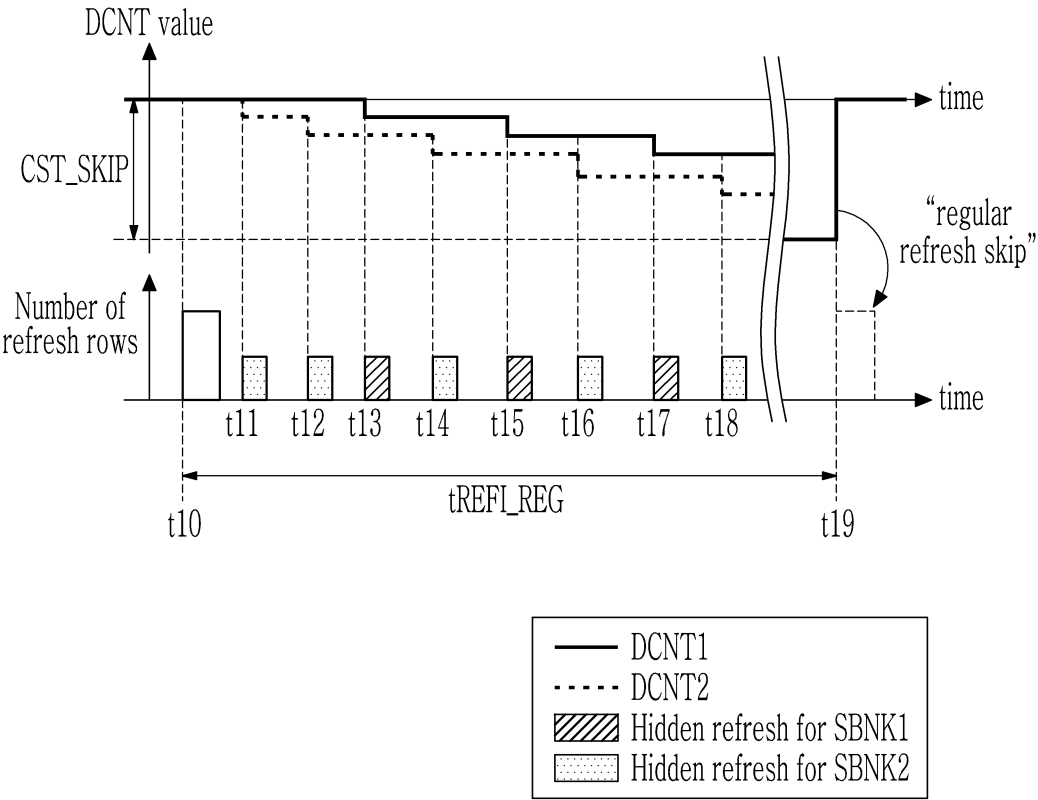


FIG. 10

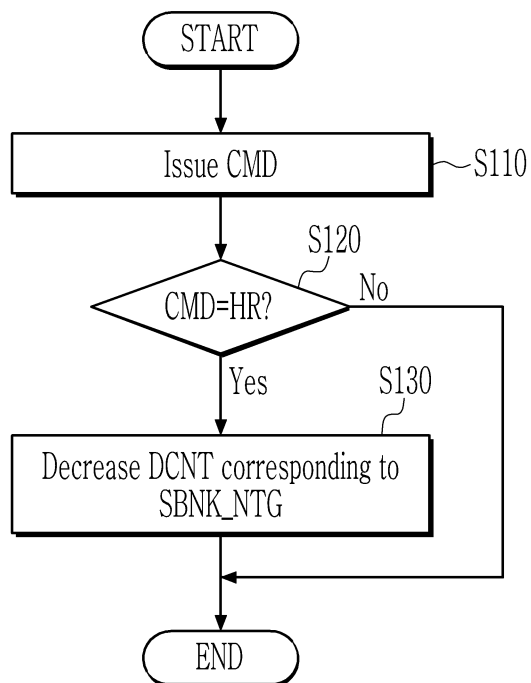


FIG. 11

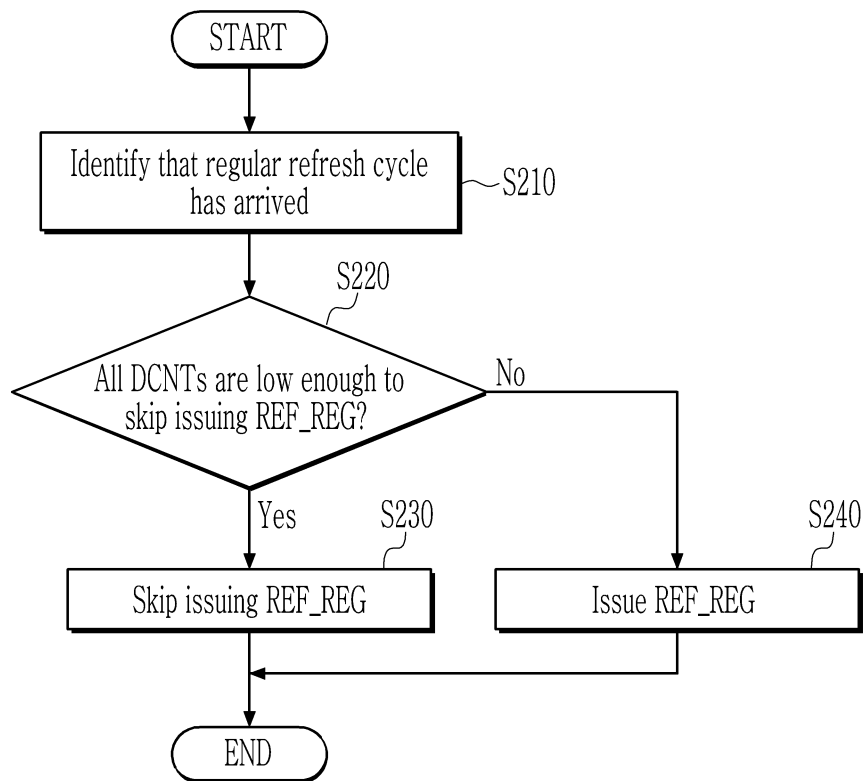


FIG. 12

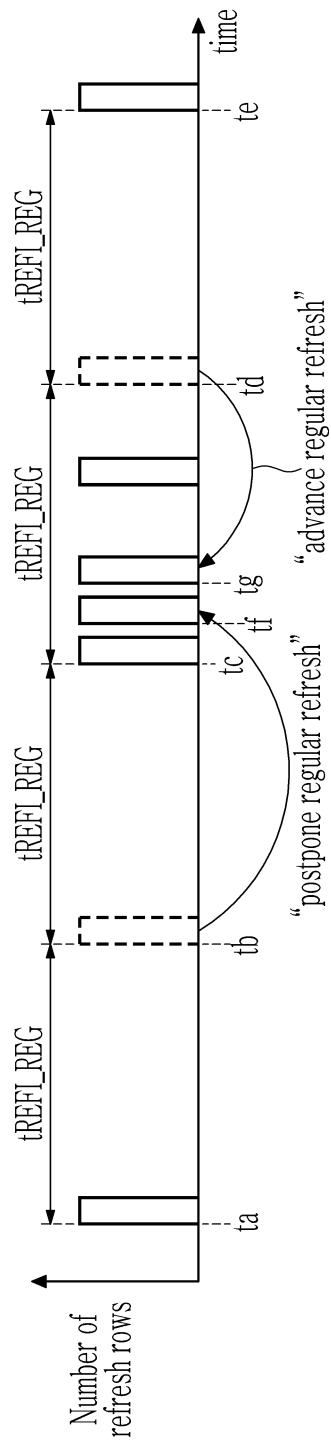


FIG. 13

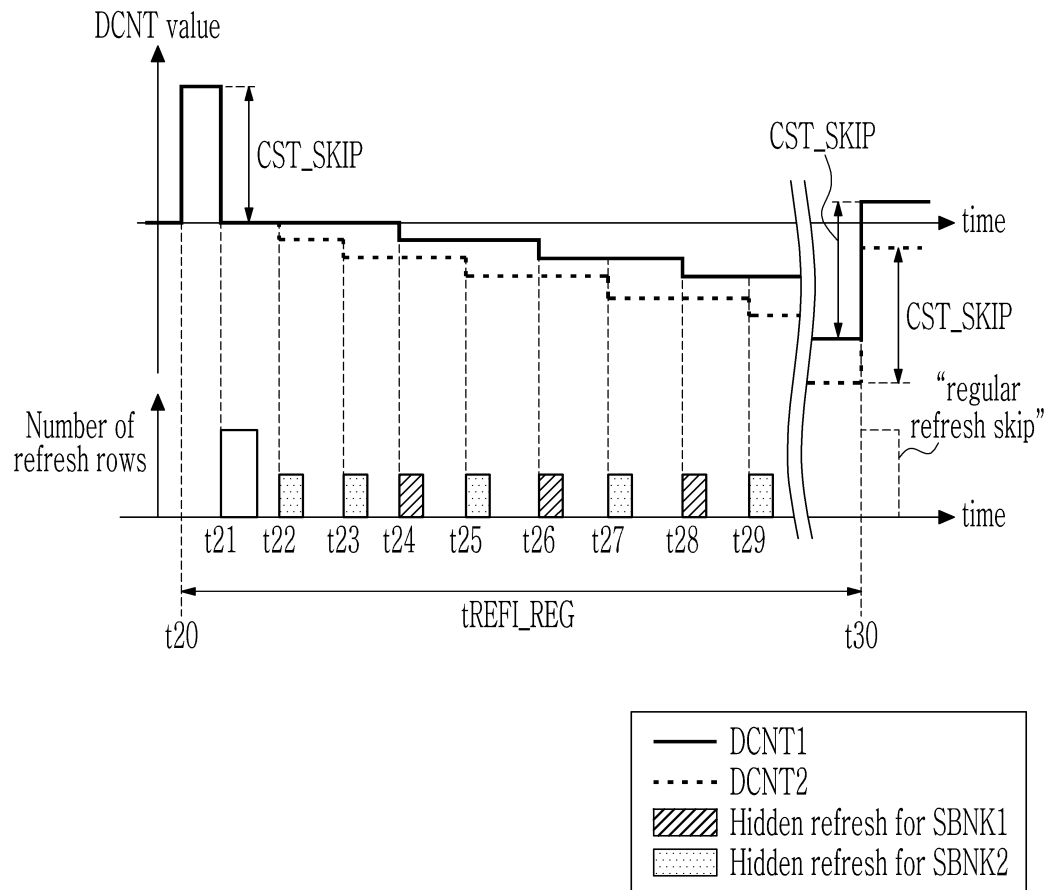


FIG. 14

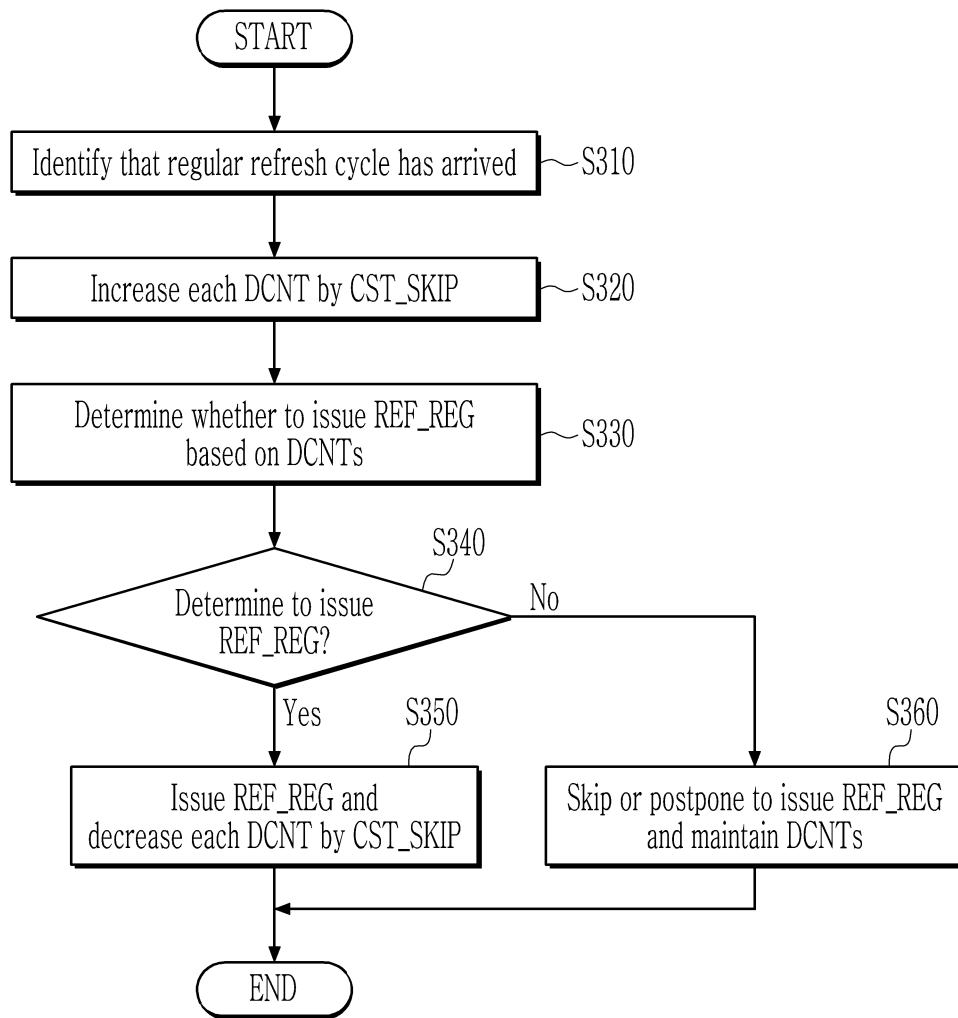


FIG. 15

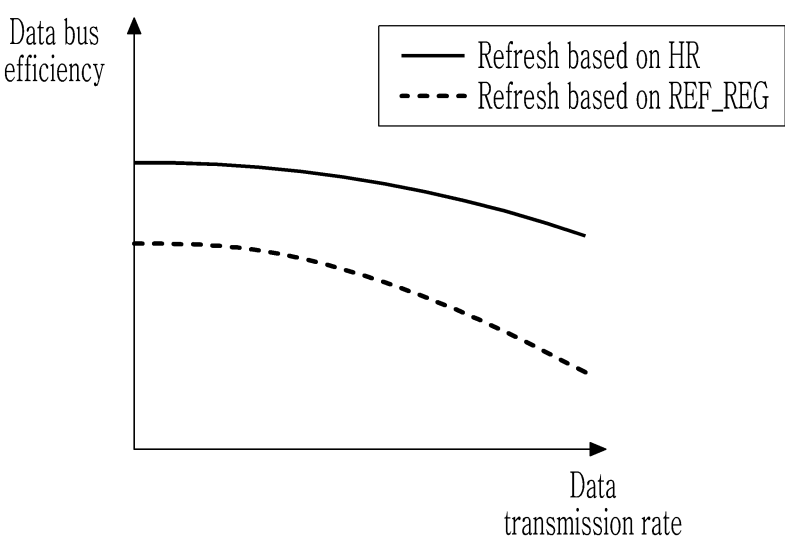


FIG. 16

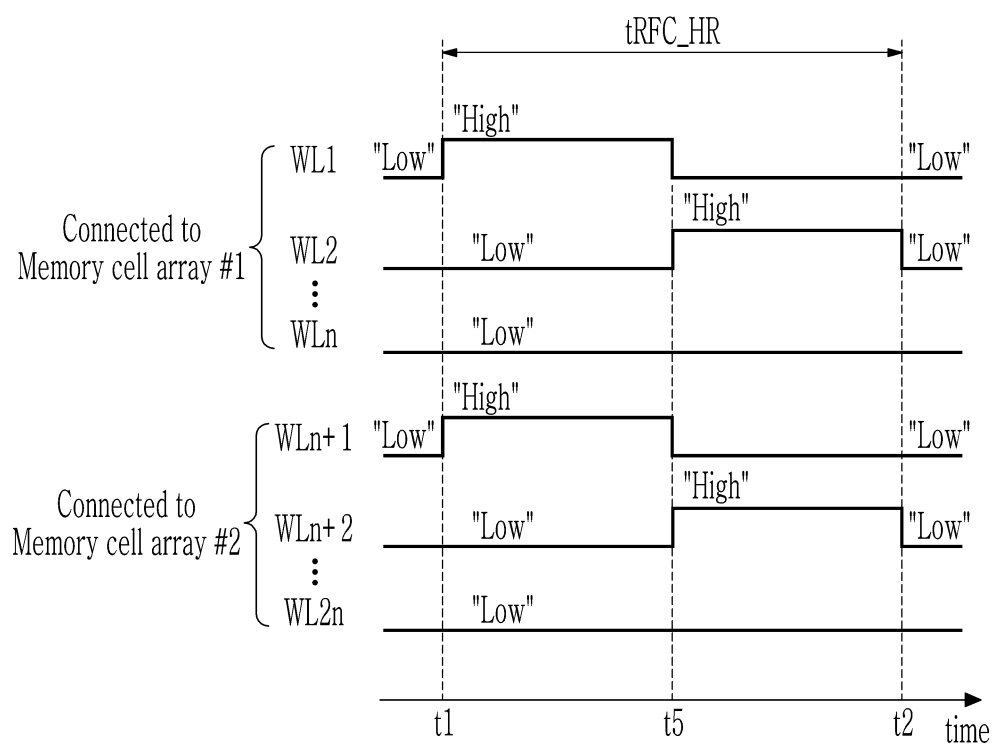


FIG. 17

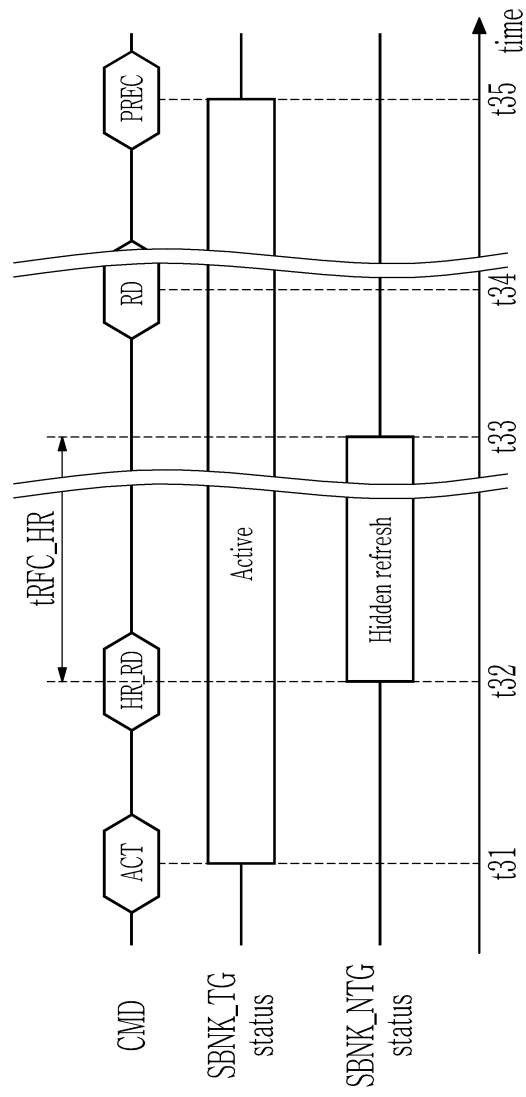


FIG. 18

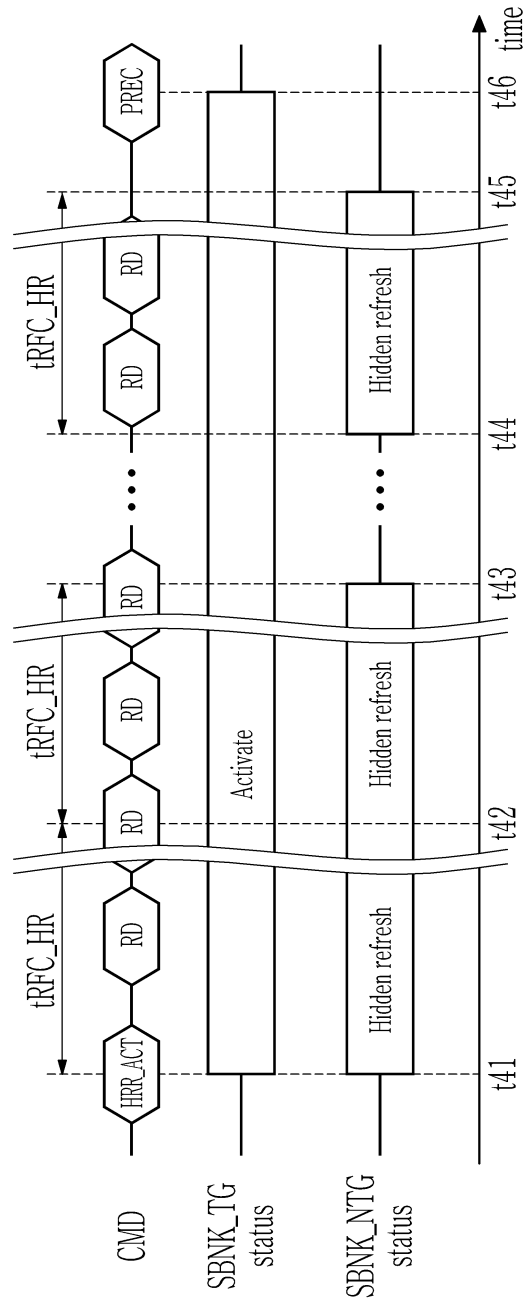


FIG. 19

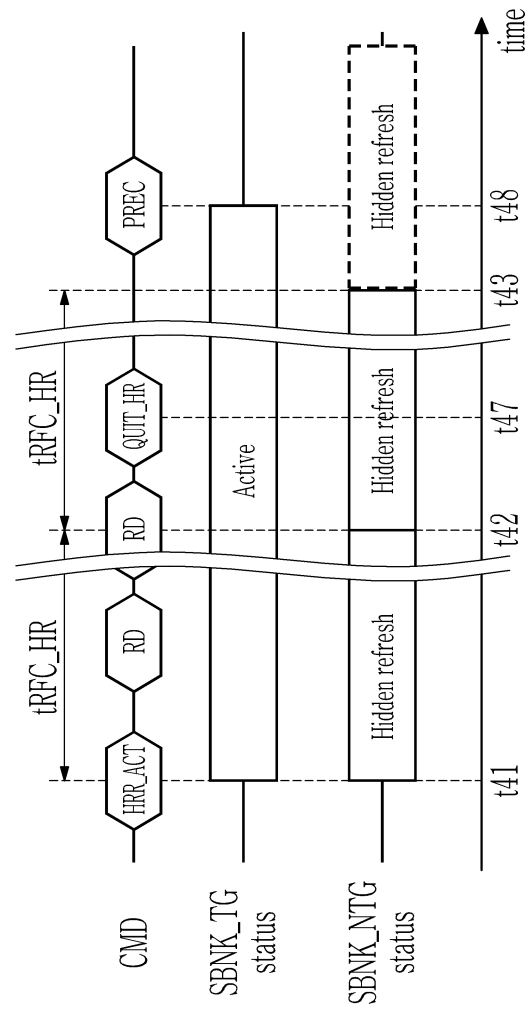


FIG. 20

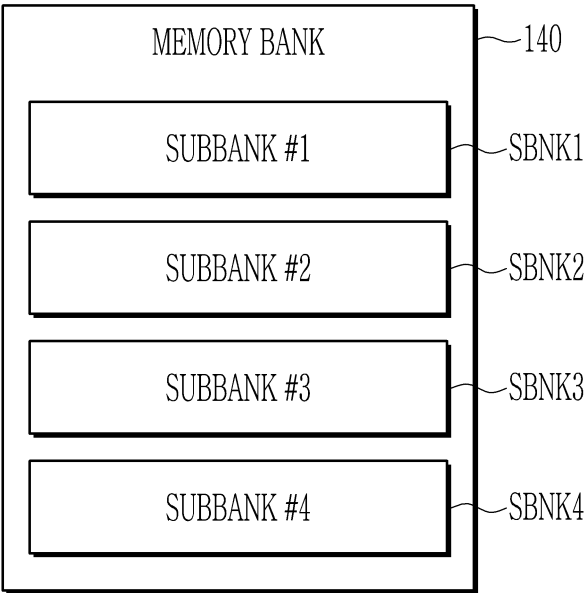


FIG. 21

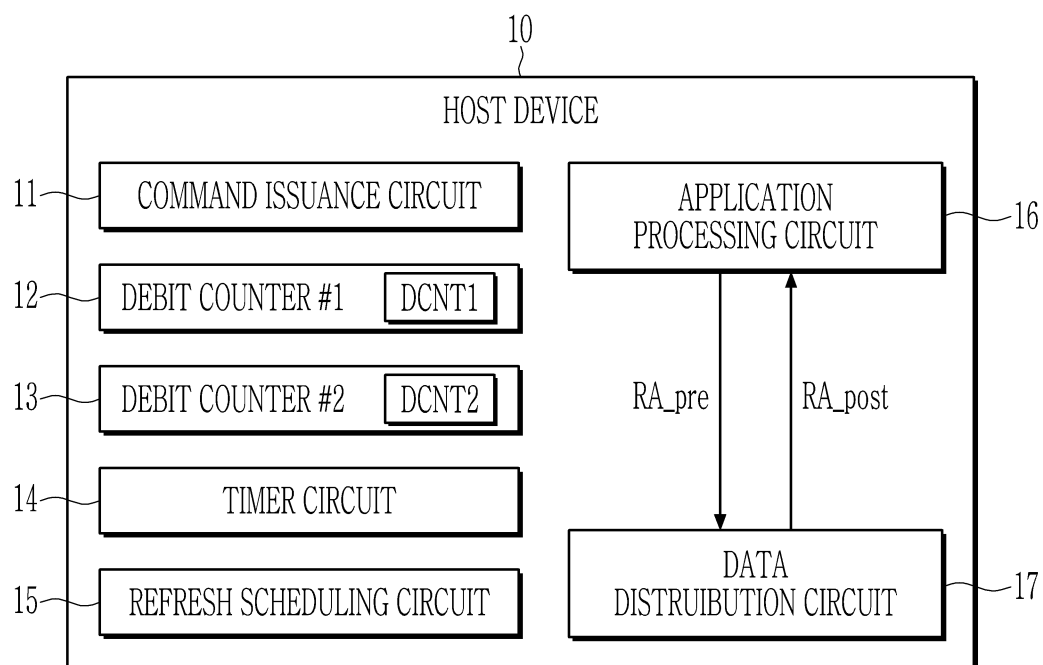


FIG. 22

Data	RA_pre	RA_post
D1	0b00000001 [SB1]	0b00110001 [SB1]
D2	0b00000010 [SB1]	0b10001010 [SB2]
D3	0b00000011 [SB1]	0b00010011 [SB1]
D4	0b00000100 [SB1]	0b10100110 [SB2]
⋮	⋮	⋮

FIG. 23

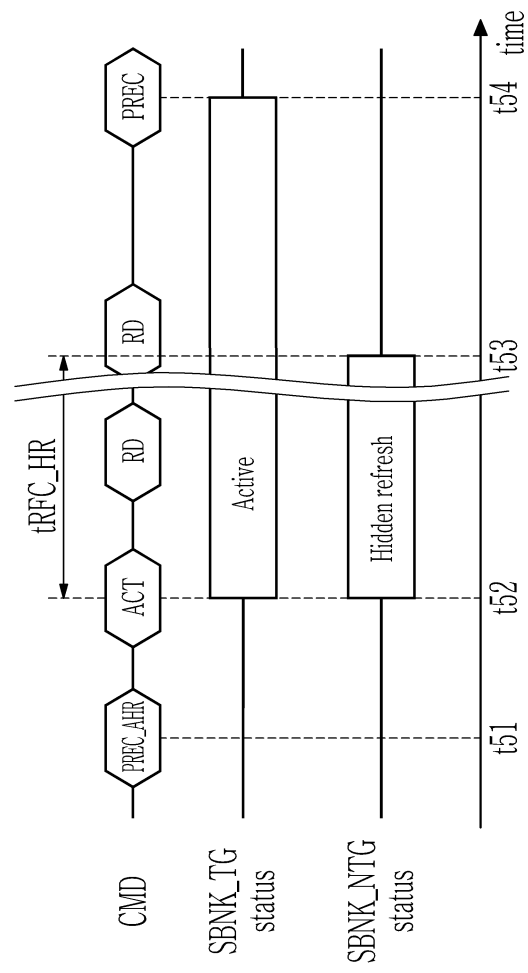


FIG. 24

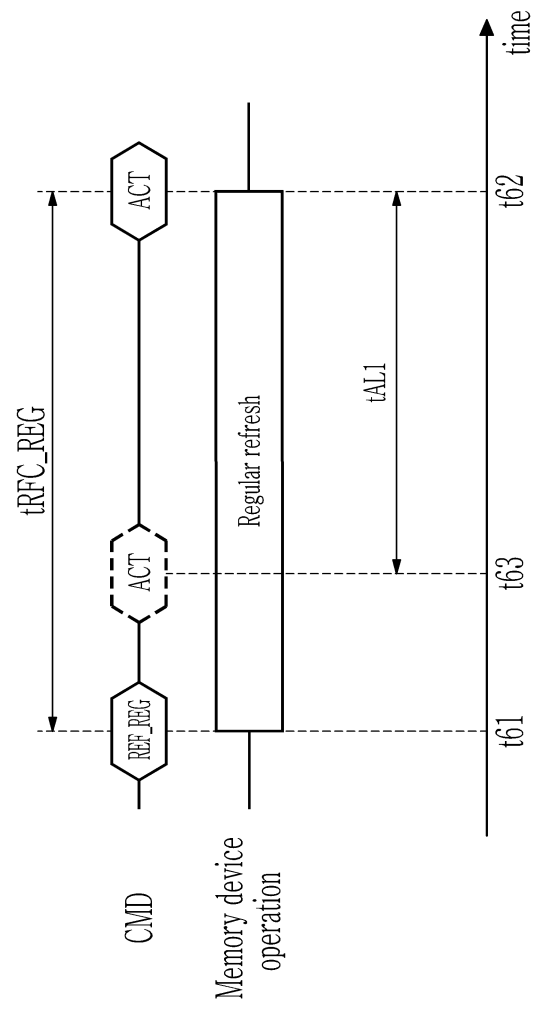


FIG. 25

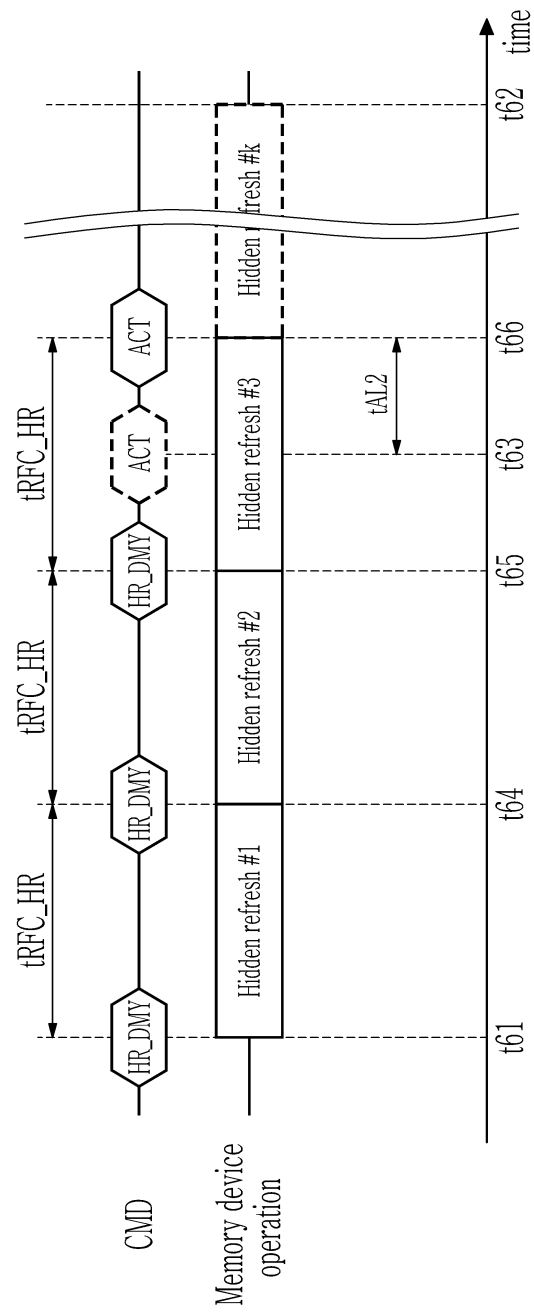


FIG. 26

