ABSTRACT



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Adopted is a system configuration in which a screen module, which displays multicolor images on a screen to which a multitude of first-color LEDs, second-color LEDs and third-color LEDs are orderly arrayed, and a data-sending module, which gives a control signal and image data to be displayed on the screen module, are connected by a data-sending means. On the screen module, for each pixel on the screen, there are installed first-color gradation-control circuits, second-color gradation-control circuits and third-color gradation-control circuits for pulse-lighting the LEDs. The data-sending module comprises: a frame memory for temporarily storing image data to be displayed on the screen module; an image-data-transfer-control means for reading out the image data from the frame memory, and for outputting, to the data-sending means, the image data along with a predetermined data-transfer clock in a predetermined pixel order; first-color high-speed pulse-train generating means, second-color high-speed pulse-train generating means, and third-color high-speed pulse-train generating means for generating high-speed pulse trains to be given to the respective first-color gradation-control circuit, second-color gradation-control circuit and third-color gradation-control circuit; and a high-speed pulse-train outputting means for outputting, to the data-sending means, the respective high-speed pulse trains for the respective first color, second color and third color. The high-speed



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pulse-train generating means for each color repetitively generate, with a constant period, high-speed pulse trains of (2^n) pieces or a number closely therebelow, of which pulse intervals vary with time according to a varying characteristic having been set.