

CAPACITOR MOUNTING STRUCTURE FOR PRINTED
CIRCUIT BOARDS

ABSTRACT

5 A capacitor mounting structure for printed circuit
boards wherein the capacitor includes first and second
terminals which are connected to first and second
conductor planes in the printed circuit board. Three
10 vias are mounted in the printed circuit board in a
position to be aligned with the middle of the
capacitor. A first conductor pad is mounted underneath
one end of the capacitor and includes spaced apart
extension portions which electrically attach to the
15 first and third via. A second conductor pad is mounted
under the other end of the capacitor and includes a
central extension portion which attaches to the second
or middle via. In this manner, the region available
for generation of parasitic inductance is minimized
thereby increasing the operating efficiency of the
capacitor.

(Figure 3A)