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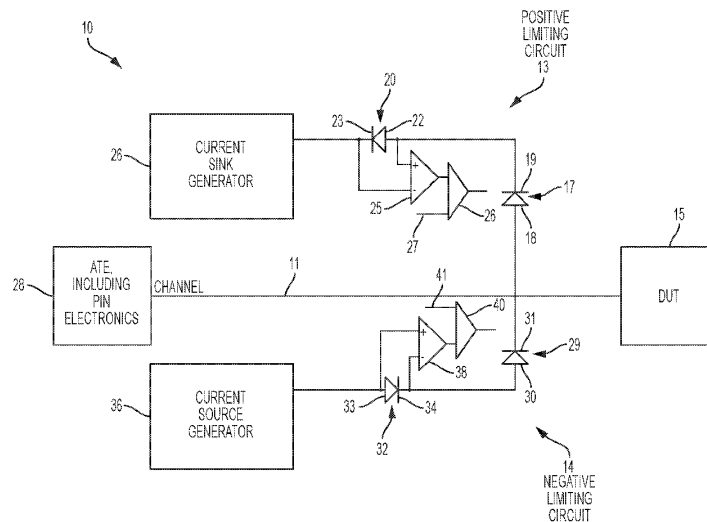


FIG. 1

(57) Abstract: An example system includes a channel over which signals are transmitted between test equipment and a device under test (DUT); and limiting circuitry to limit a voltage on the channel. The limiting circuitry includes a PN-junction device connected to pass current in response to the voltage on the channel exceeding a limit.

[Continued on next page]

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