

## ABSTRACT OF THE DISCLOSURE

**SEMICONDUCTOR MEMORY APPARATUS, SEMICONDUCTOR APPARATUS,  
DATA PROCESSING APPARATUS AND COMPUTER SYSTEM**

A semiconductor memory apparatus is provided with a memory array, a first global bit line connected to a sense amplifier, a second global bit line connected to a write amplifier, and a selection circuit for connecting a plurality of bit lines selectively to the first global bit line and the second global bit line.

(Figure 1)