

FIG. 1

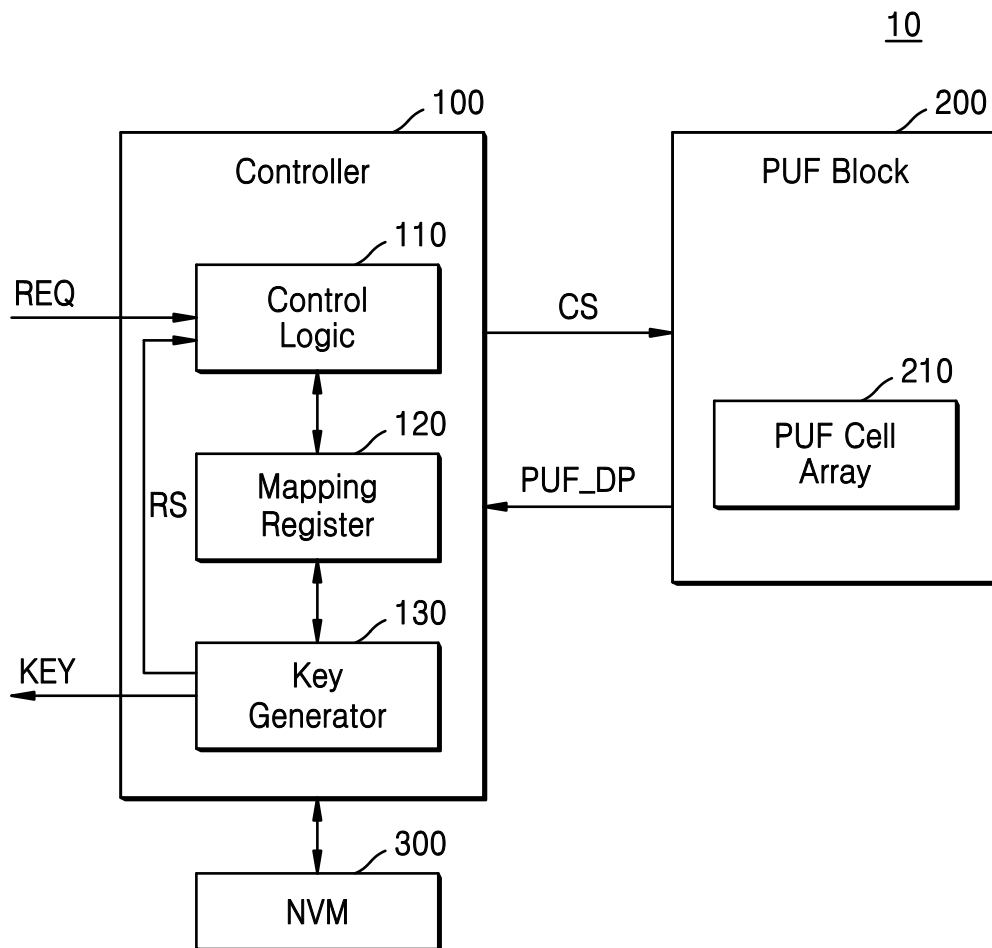


FIG. 2

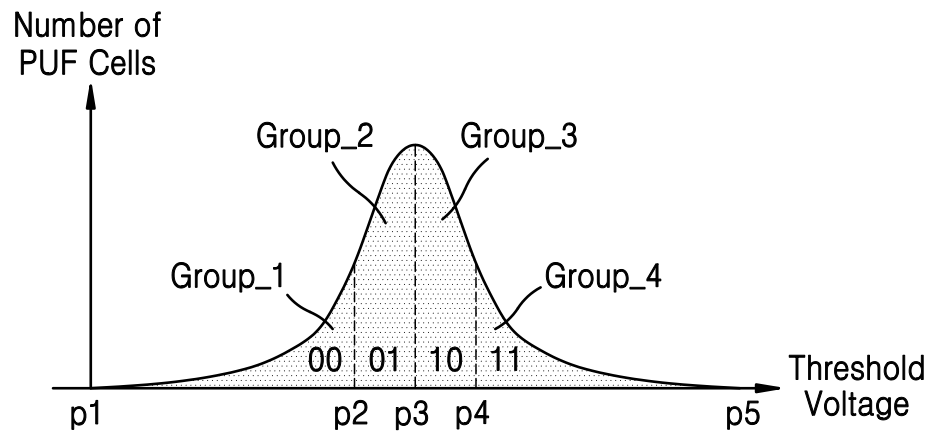


FIG. 3

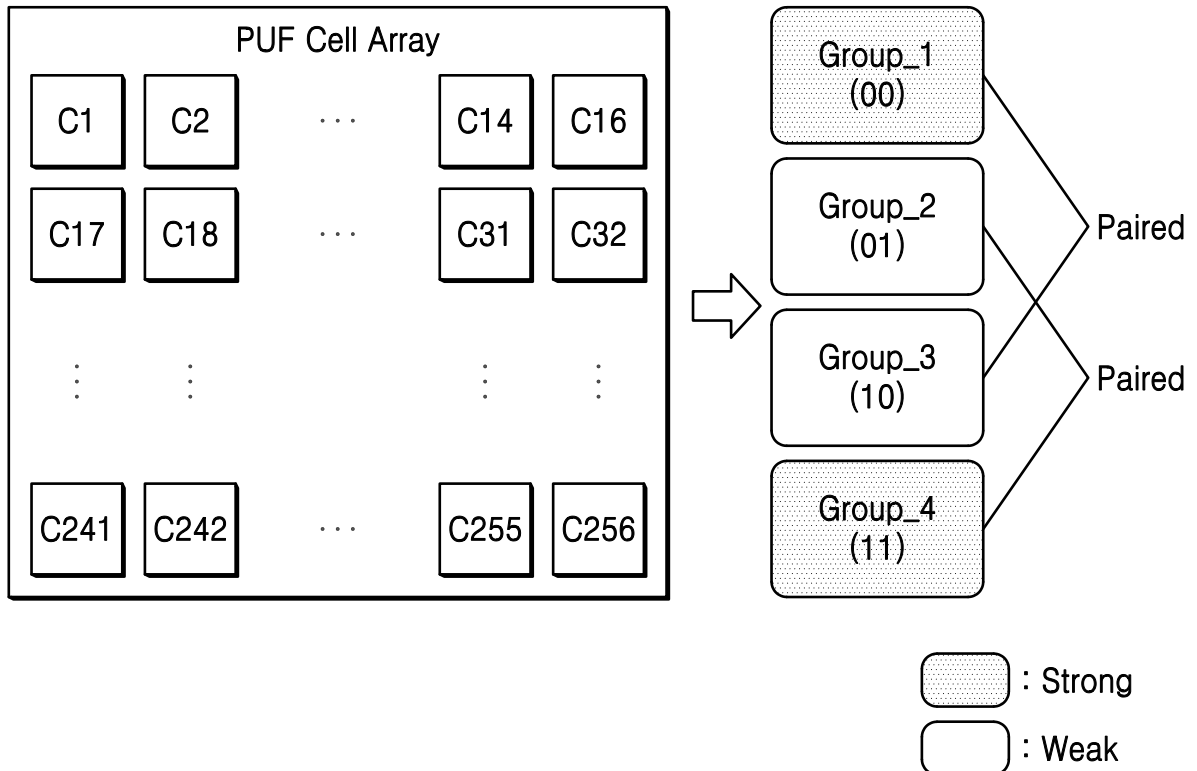


FIG. 4

TB_1

Address	Paired Address
C1_ADD	C79_ADD
⋮	⋮
C256_ADD	C67_ADD

FIG. 5

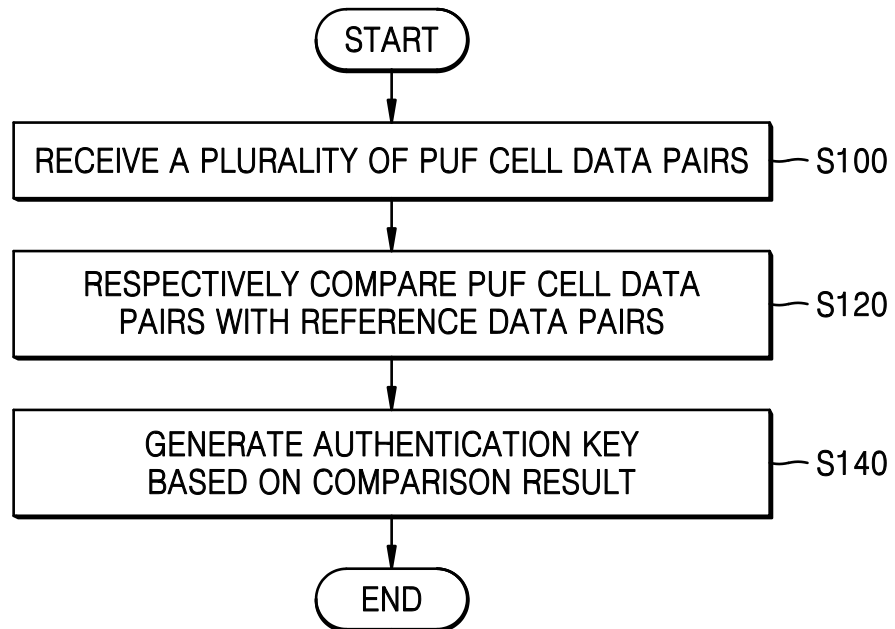


FIG. 6

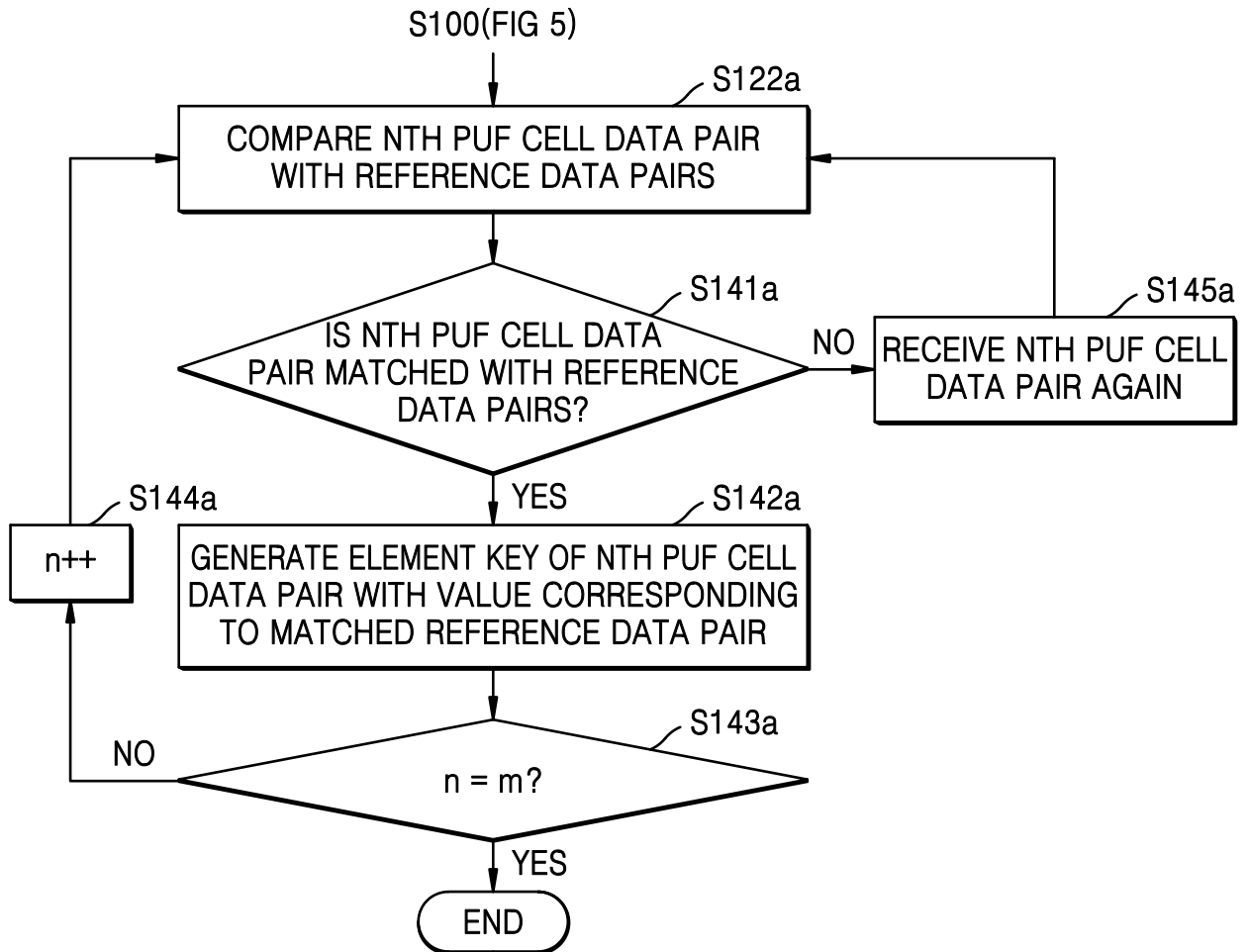


FIG. 7

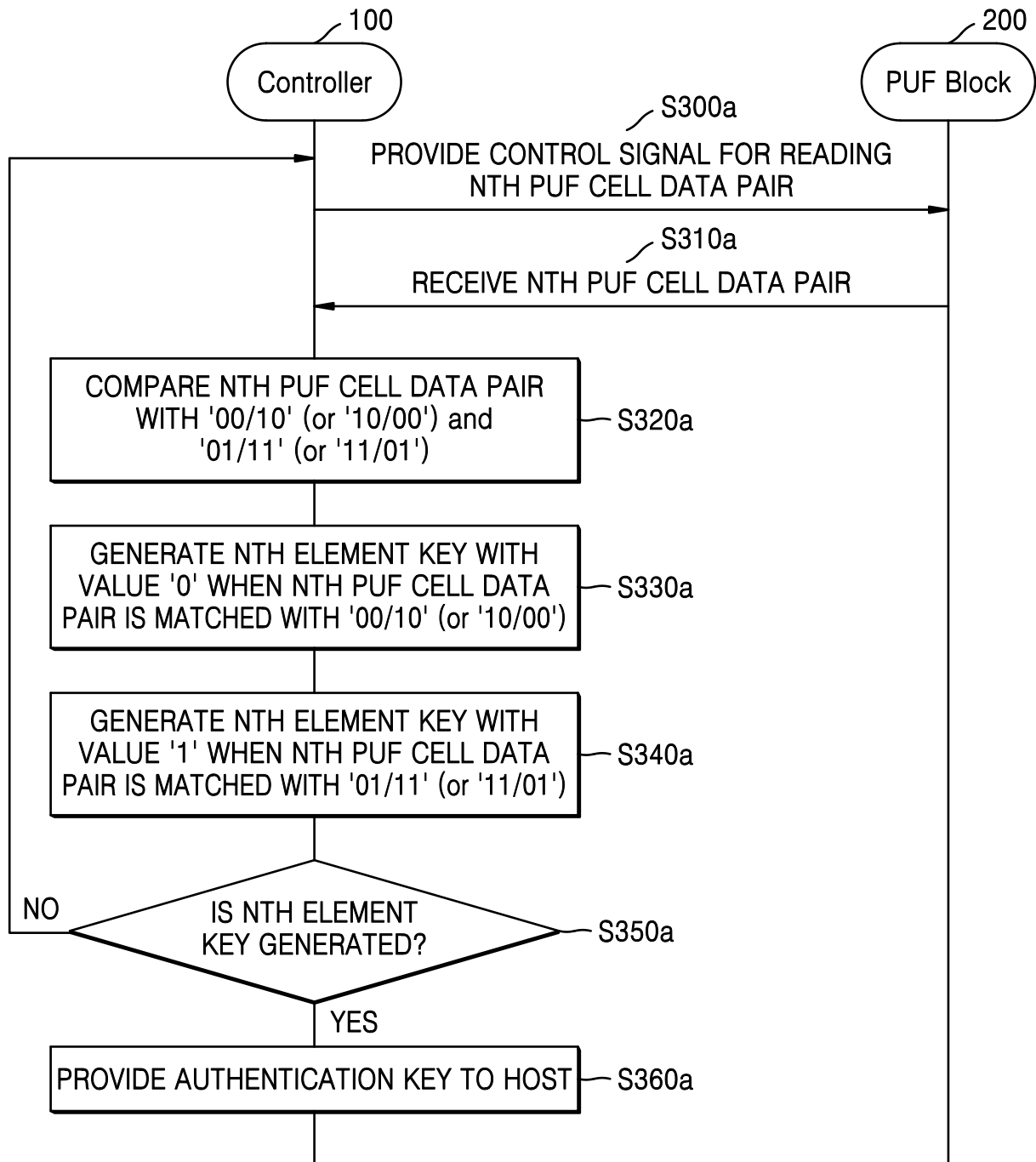


FIG. 8A

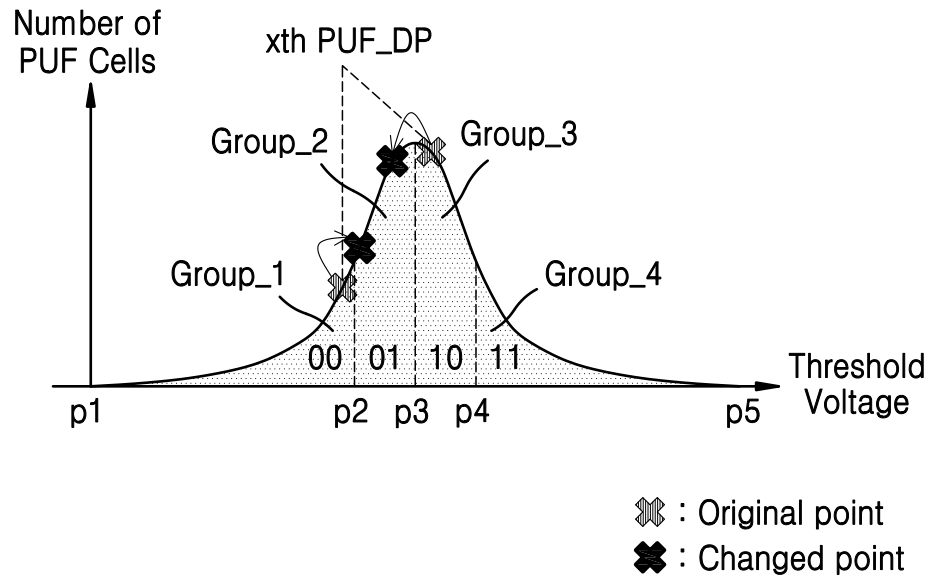


FIG. 8B

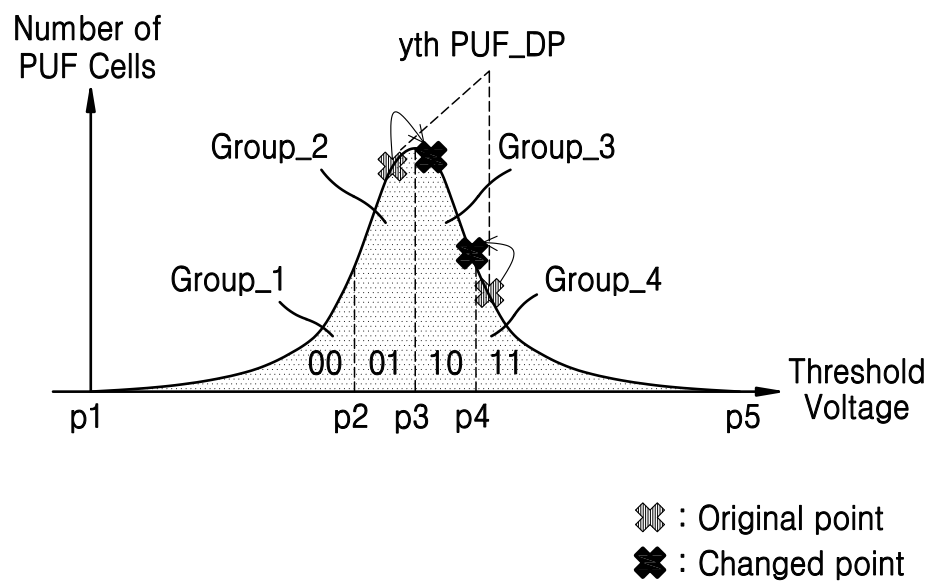


FIG. 9

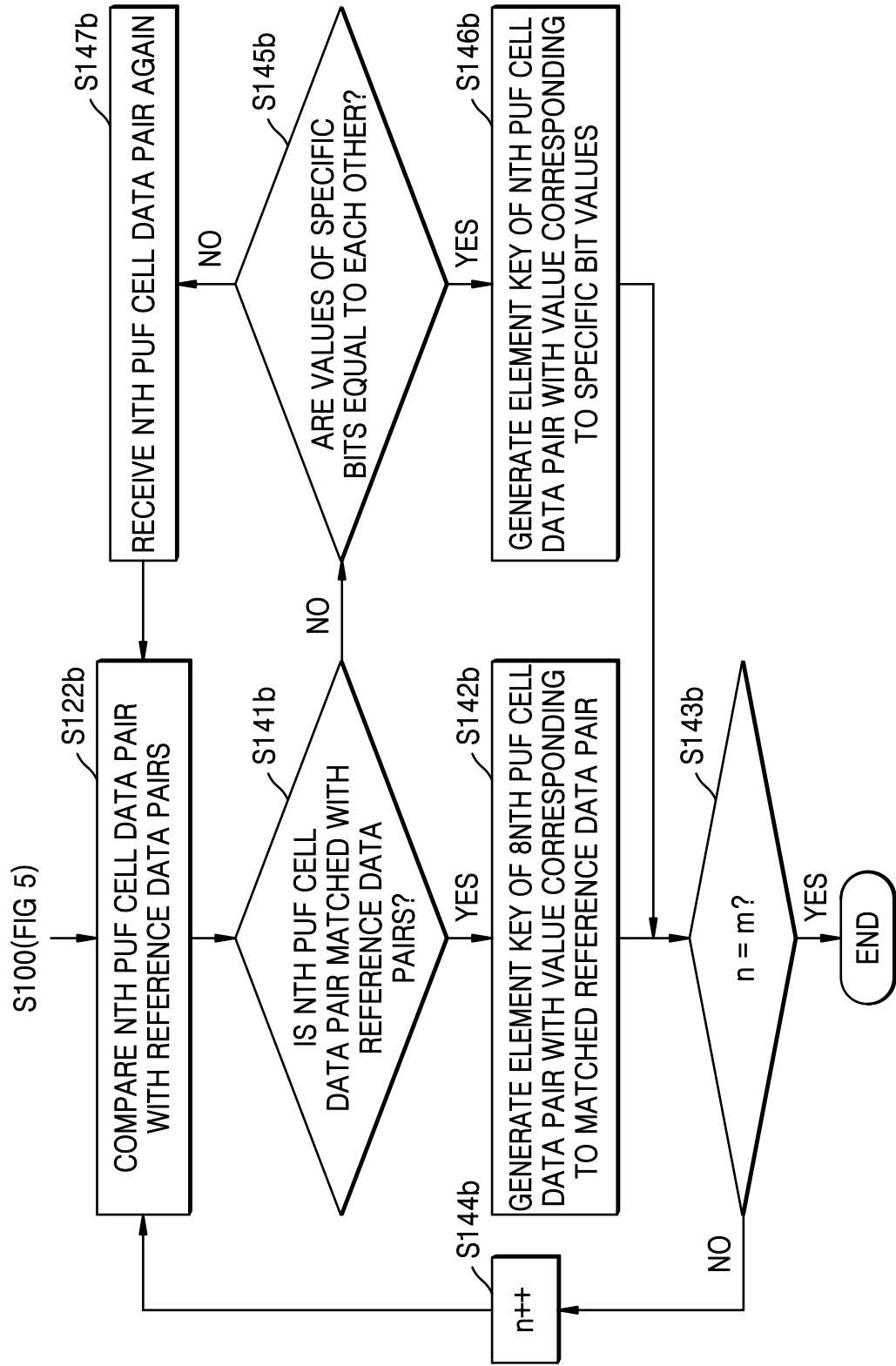


FIG. 10

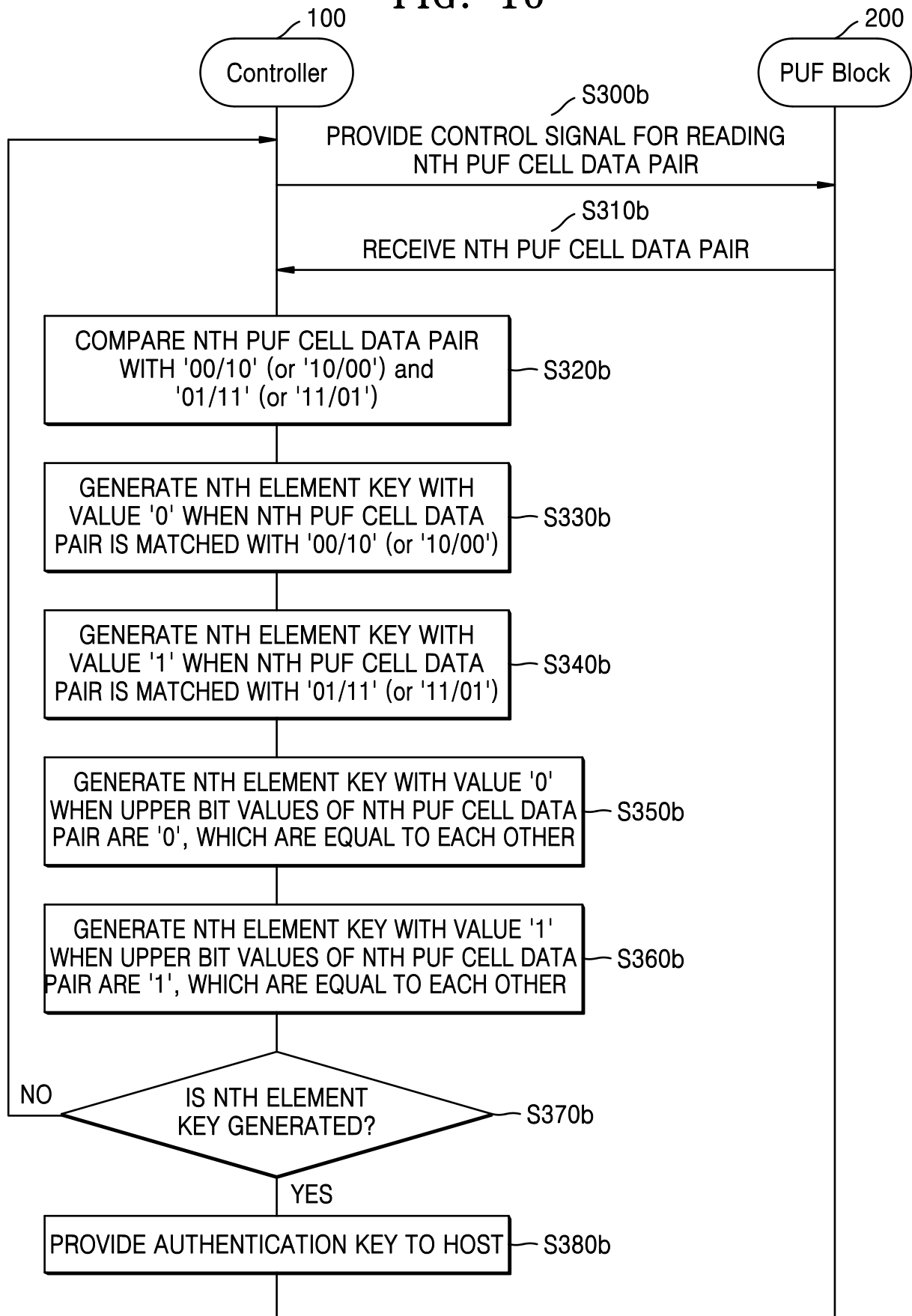


FIG. 11

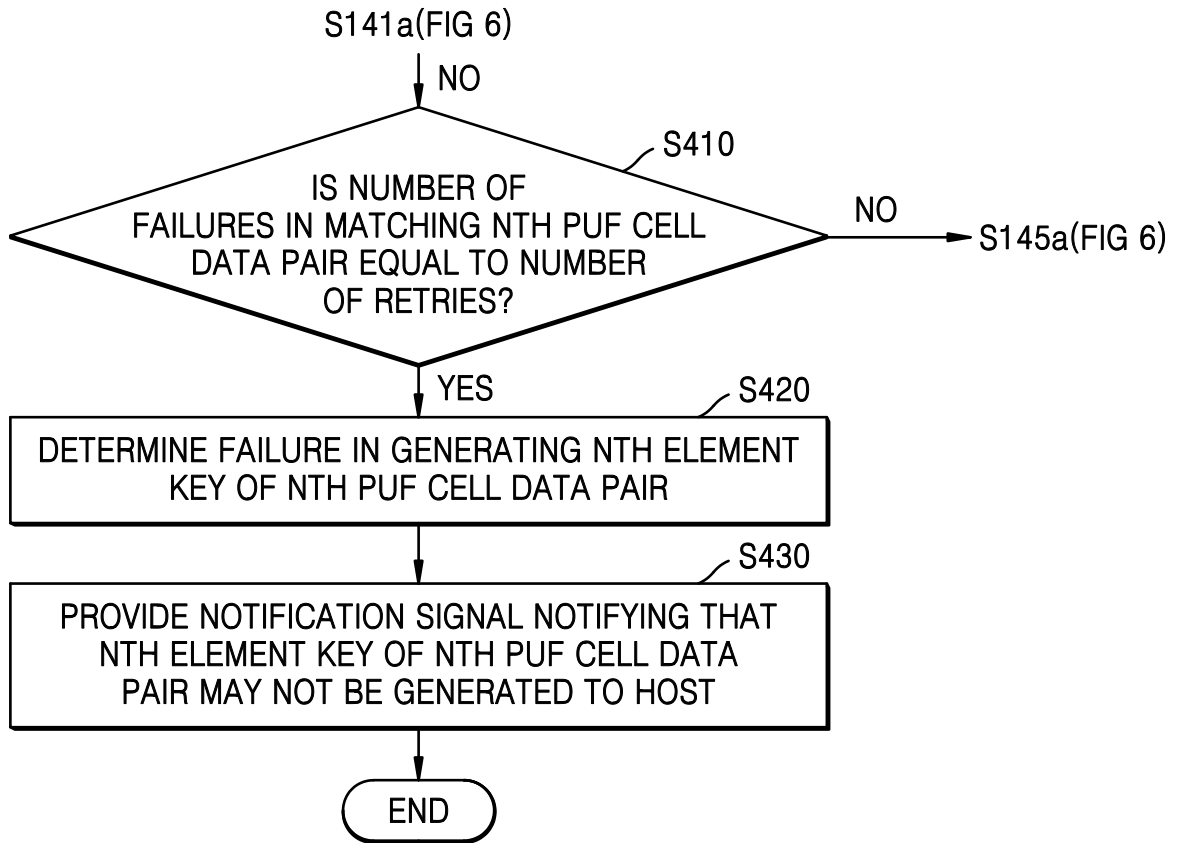


FIG. 12

TB_2

Address	Paired Address	Retry Count
C1_ADD	C79_ADD	RC1
⋮	⋮	⋮
C256_ADD	C67_ADD	RC2

FIG. 13

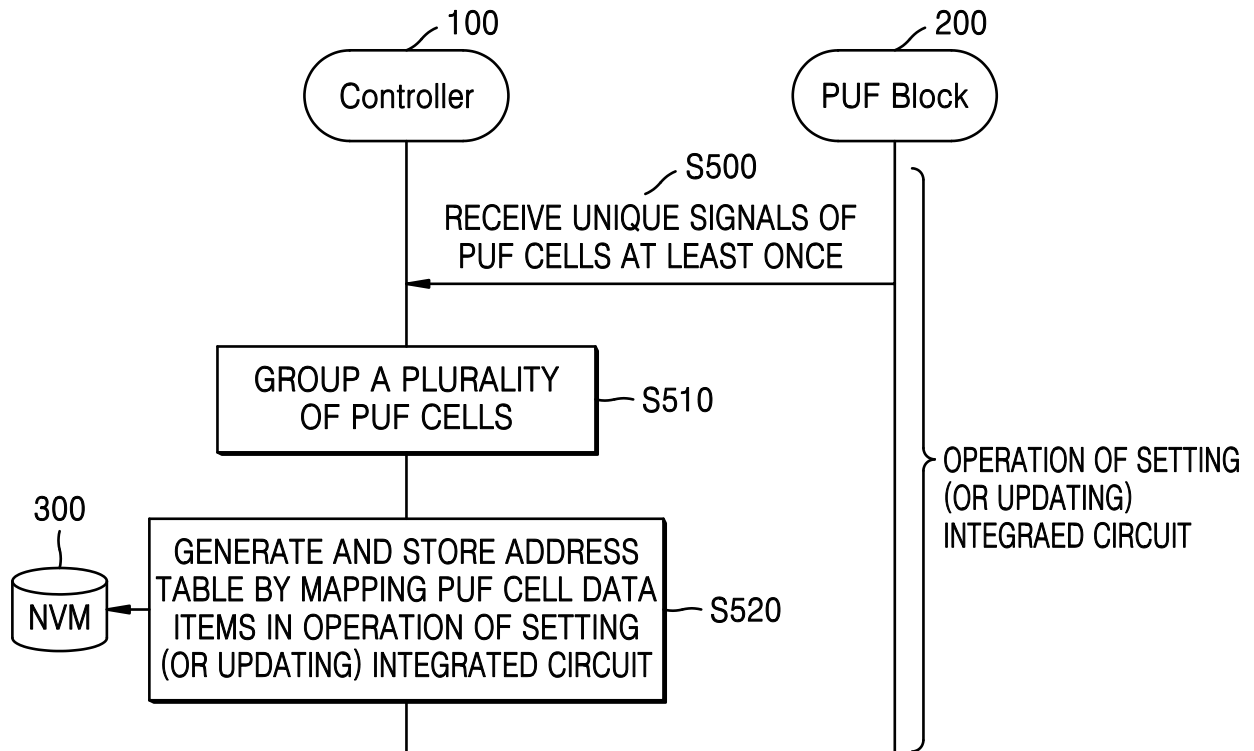


FIG. 14A

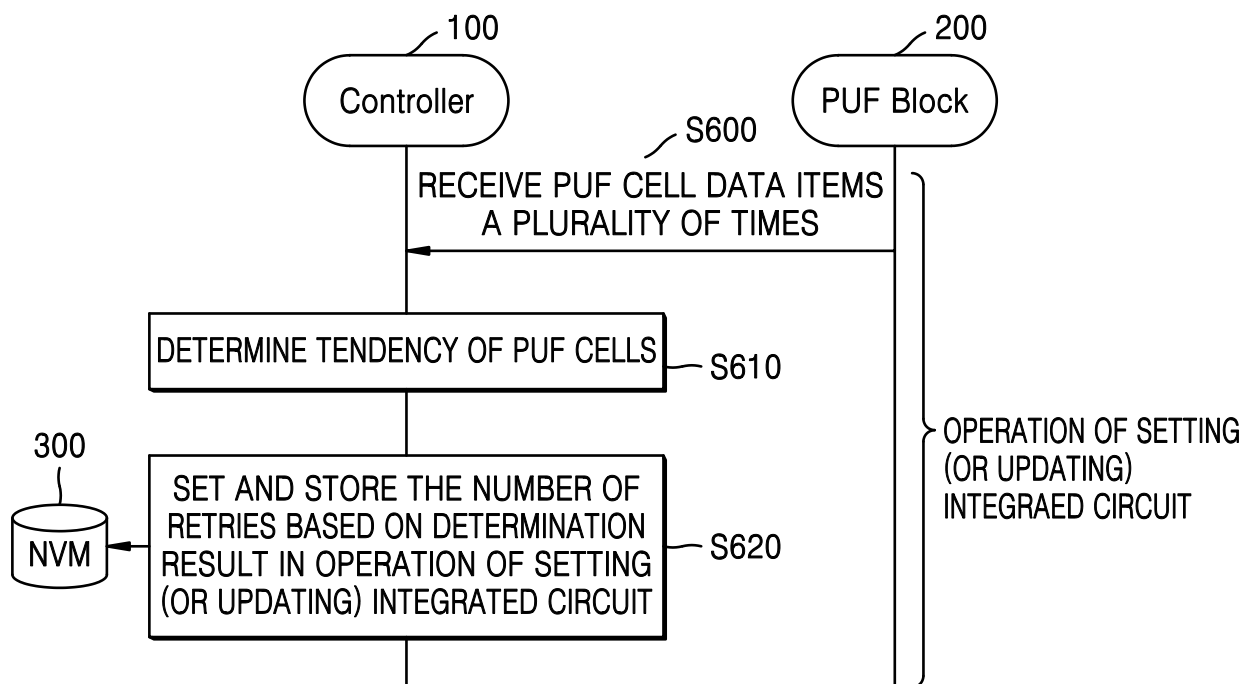


FIG. 14B

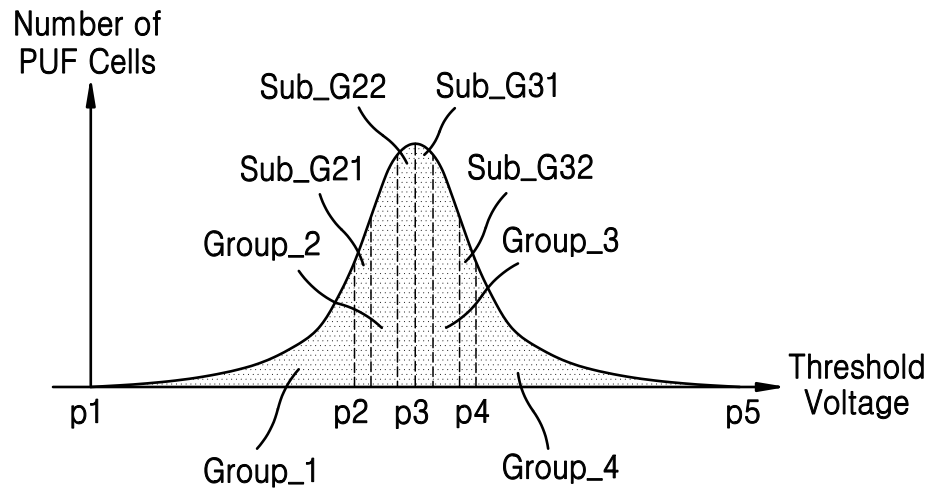


FIG. 15

