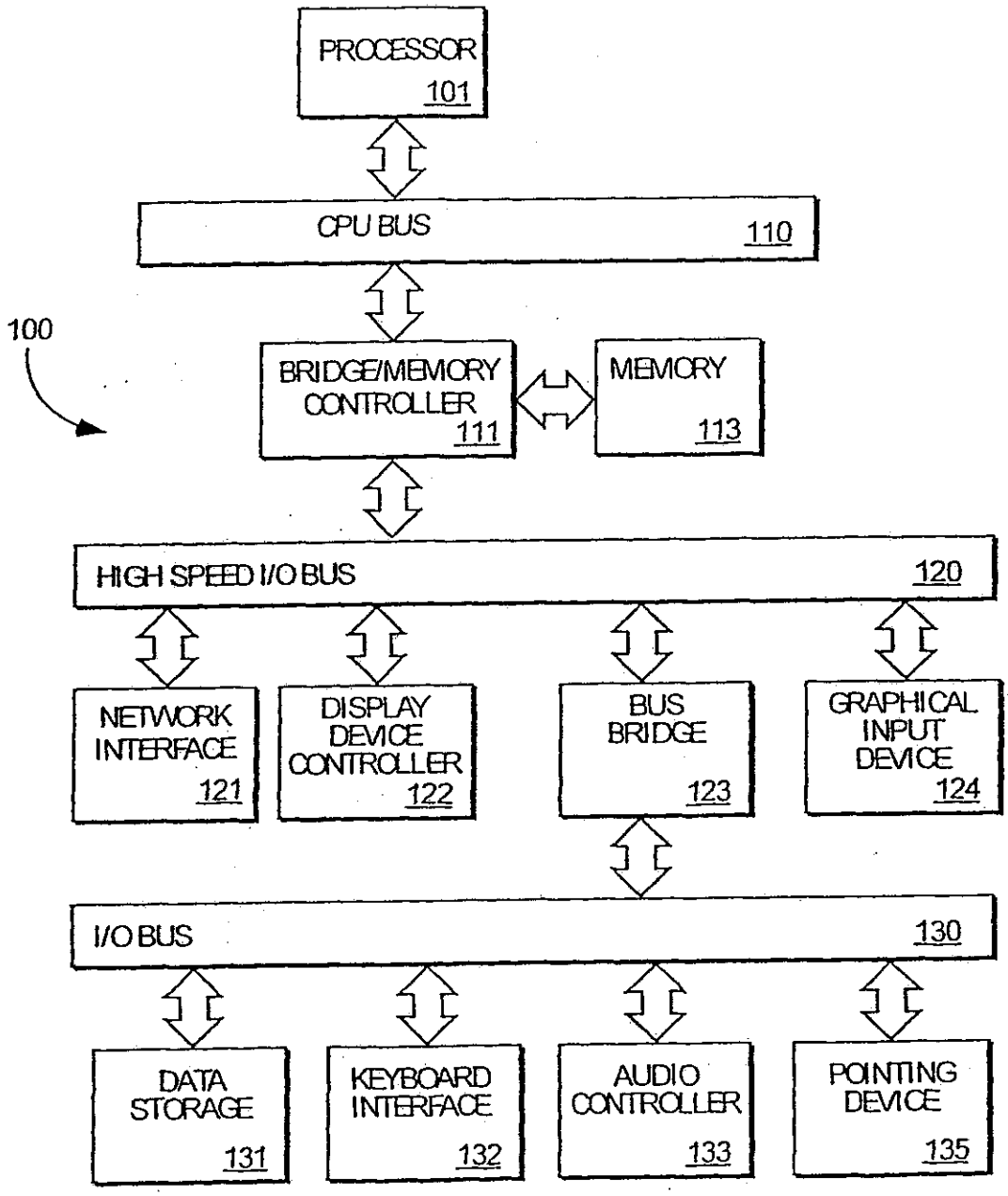

PROCESSOR
101
CPU BUS
110
100
BRIDGE/MEMORY
CONTROLLER
111
MEMORY
113
HIGH SPEED I/O BUS
120
NETWORK
INTERFACE
121
DISPLAY
DEVICE
CONTROLLER
122
BUS
BRIDGE
123
GRAPHICAL
INPUT
DEVICE
124
I/O BUS
130
DATA
STORAGE
131
KEYBOARD
INTERFACE
132
AUDIO
CONTROLLER
133
POINTING
DEVICE
135

FIG. 1

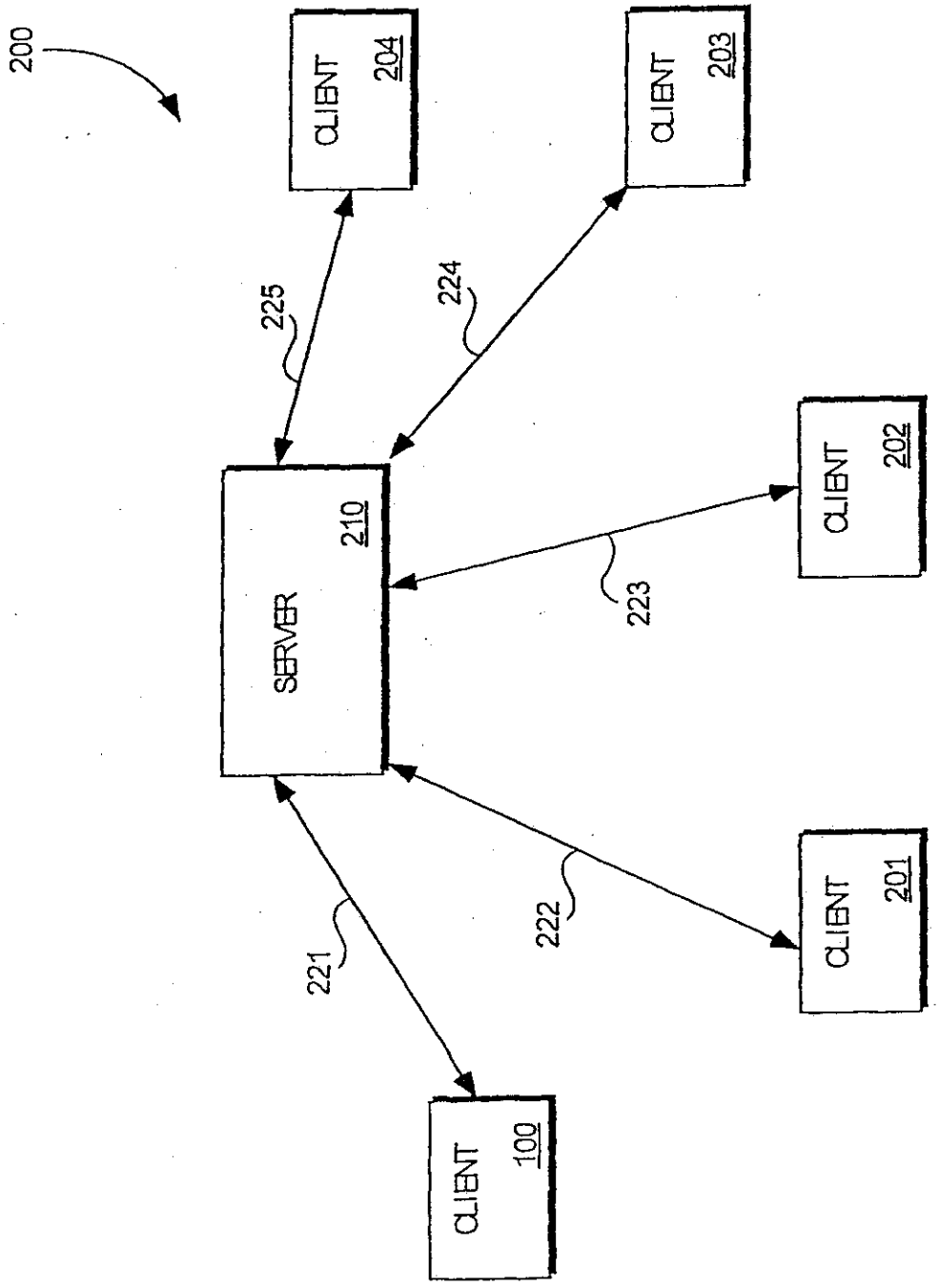

200
CLIENT 204
CLIENT 203
225
224
SERVER 210
CLIENT 202
223
221
222
CLIENT 201
CLIENT 100

FIG 2

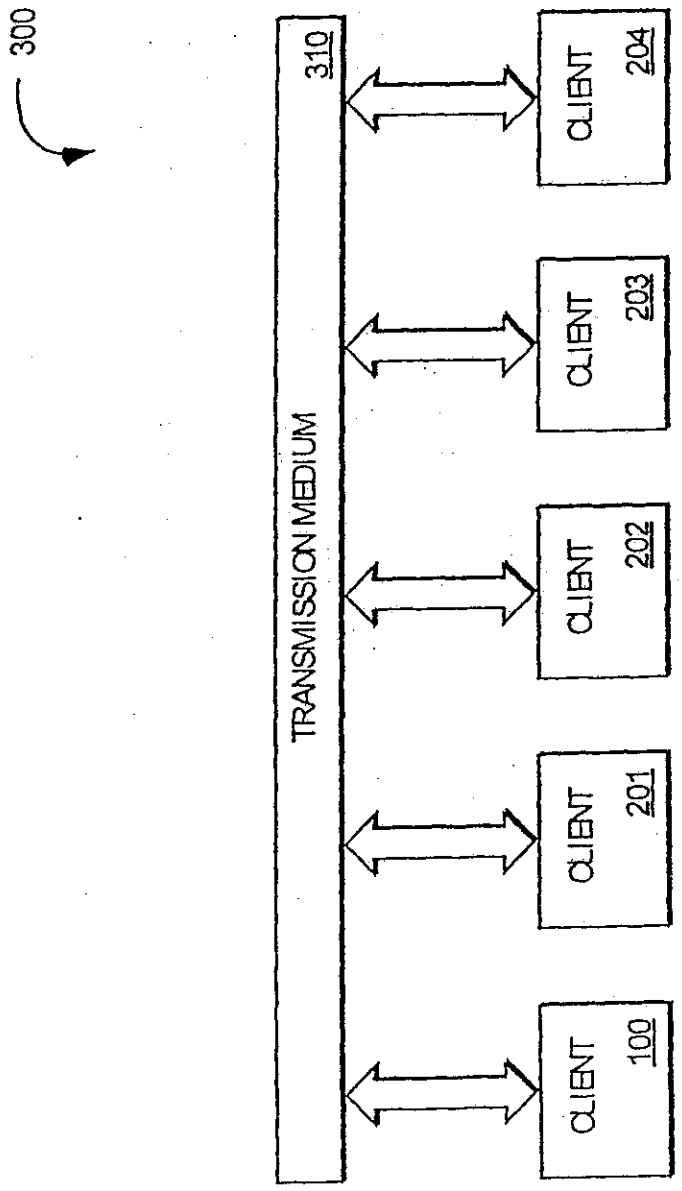

300
310
TRANSMISSION MEDIUM
CLIENT 100
CLIENT 201
CLIENT 202
CLIENT 203
CLIENT 204

FIG. 3

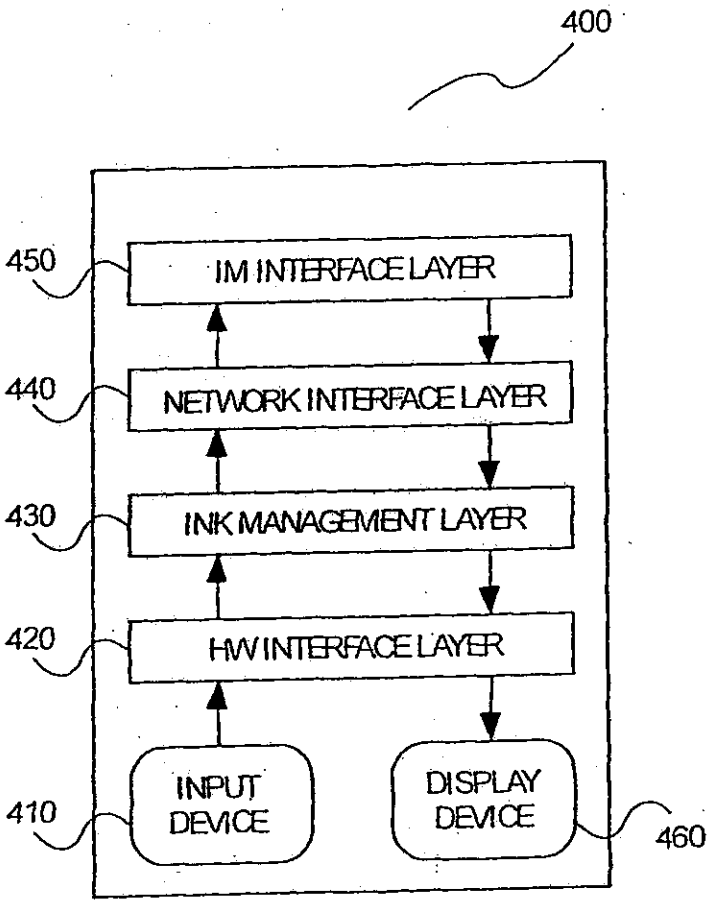

400
450
IM INTERFACE LAYER
440
NETWORK INTERFACE LAYER
430
INK MANAGEMENT LAYER
420
HW INTERFACE LAYER
410
INPUT DEVICE
DISPLAY DEVICE
460

FIG. 4