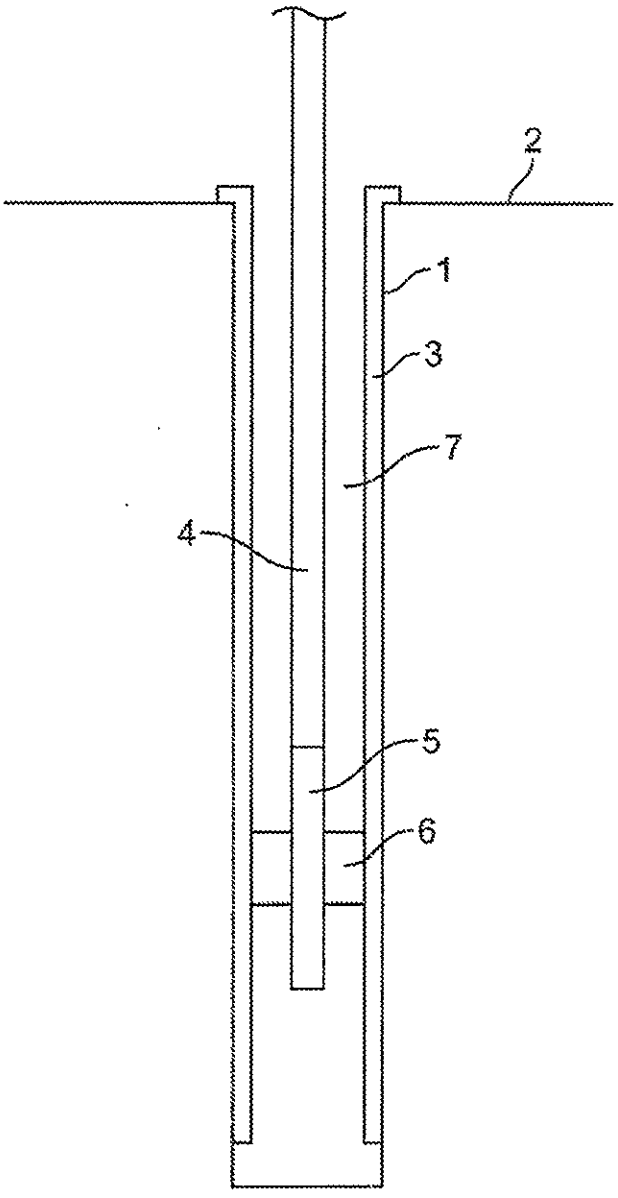


FIG. 1

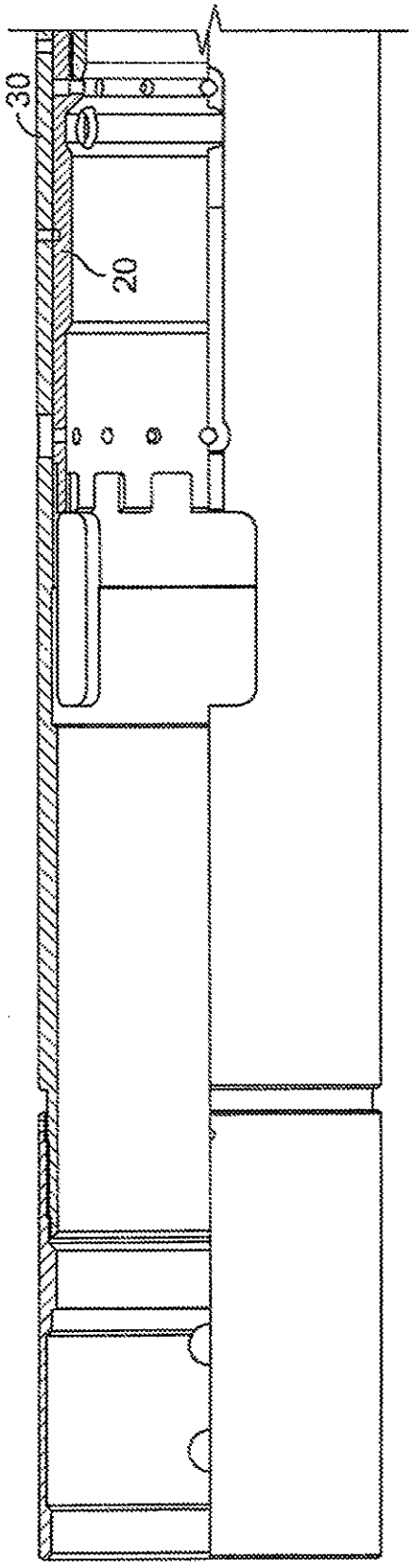

30
20

FIG. 2A

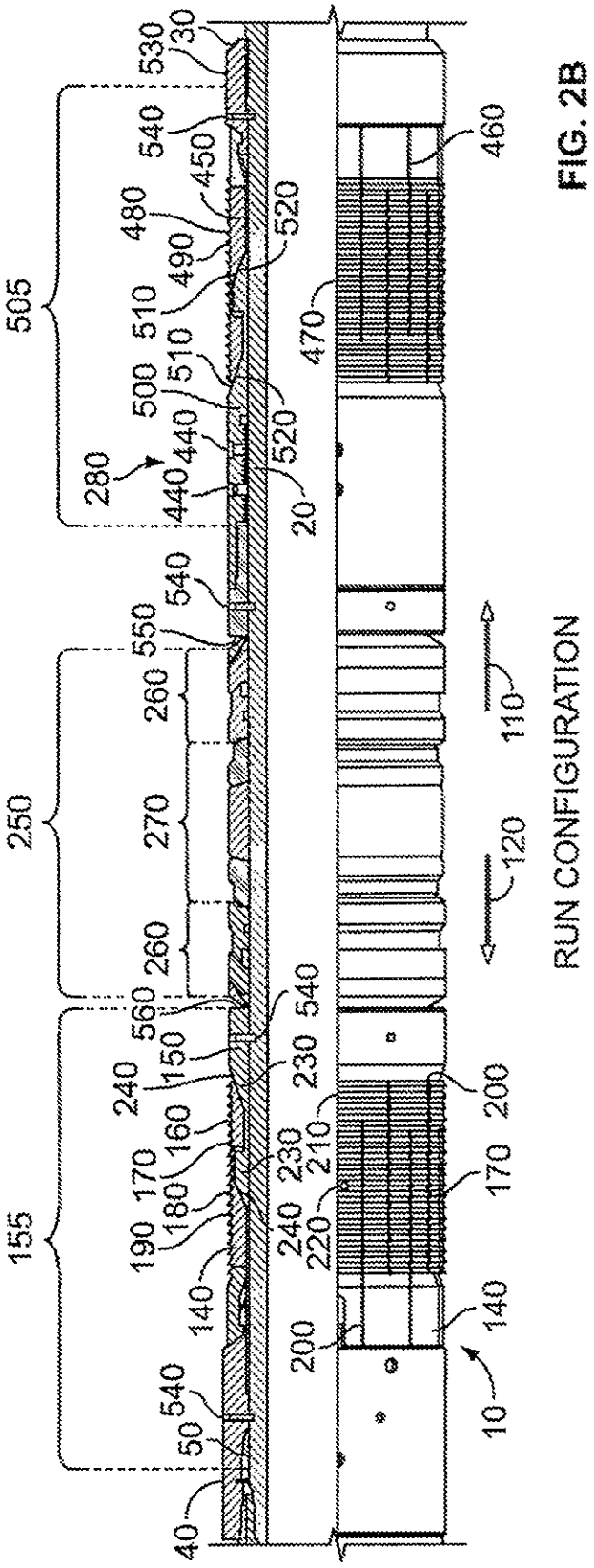

505
155
250
280
530
540
450
480
490
510
500
440
520
460
470
20
550
260
270
560
150
240
230
160
170
180
190
210
220
200
140
50
40
10
110
120
RUN CONFIGURATION

FIG. 2B

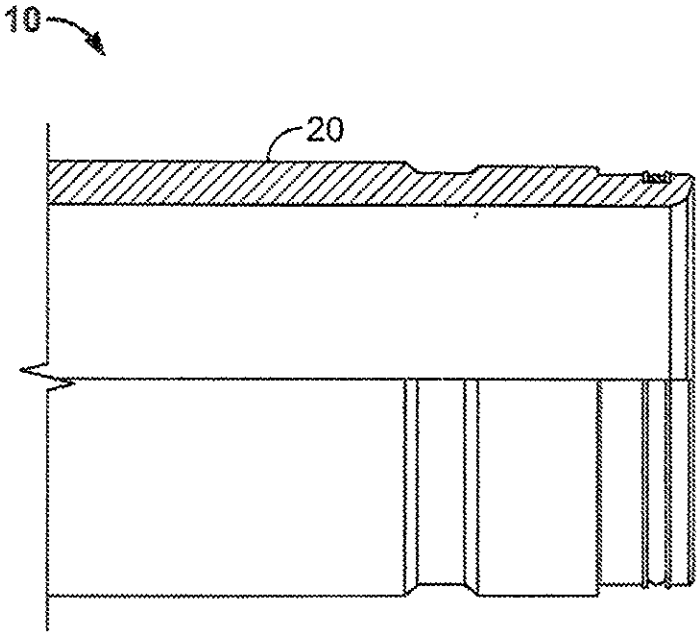


FIG. 2C

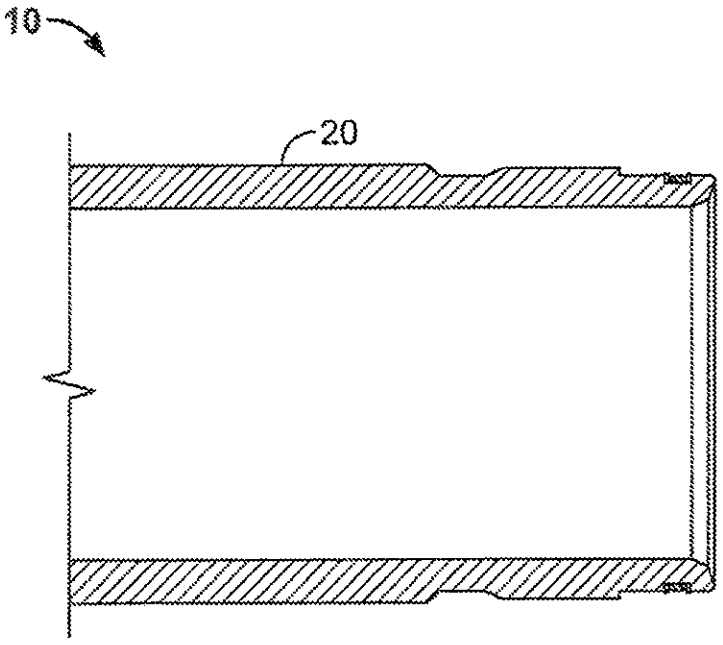


FIG. 3C

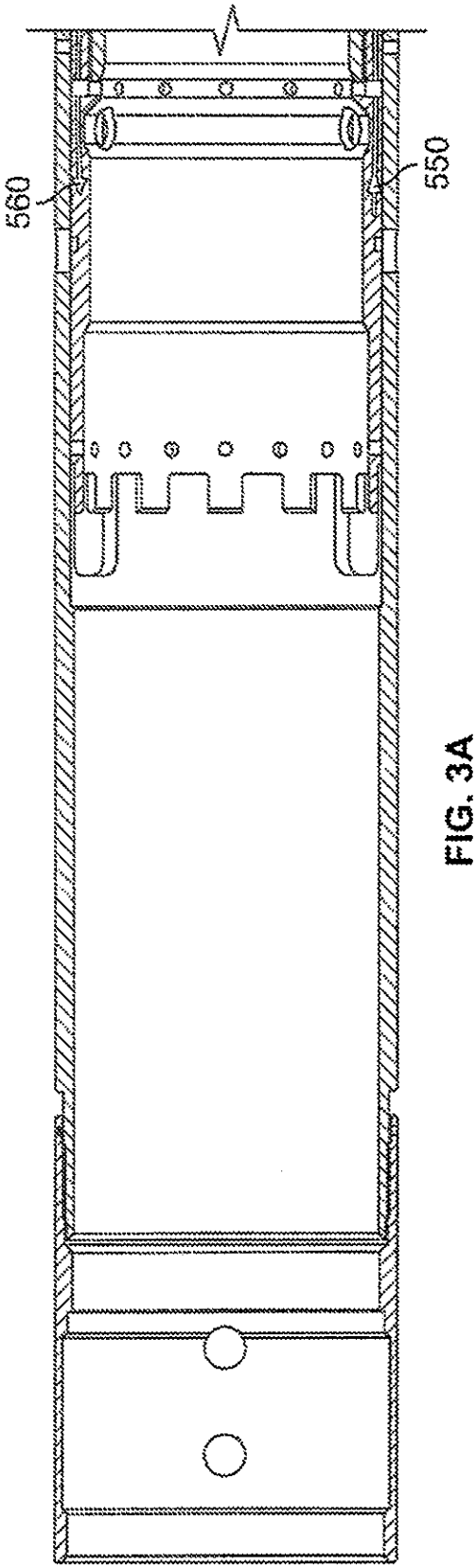

560
550

FIG. 3A

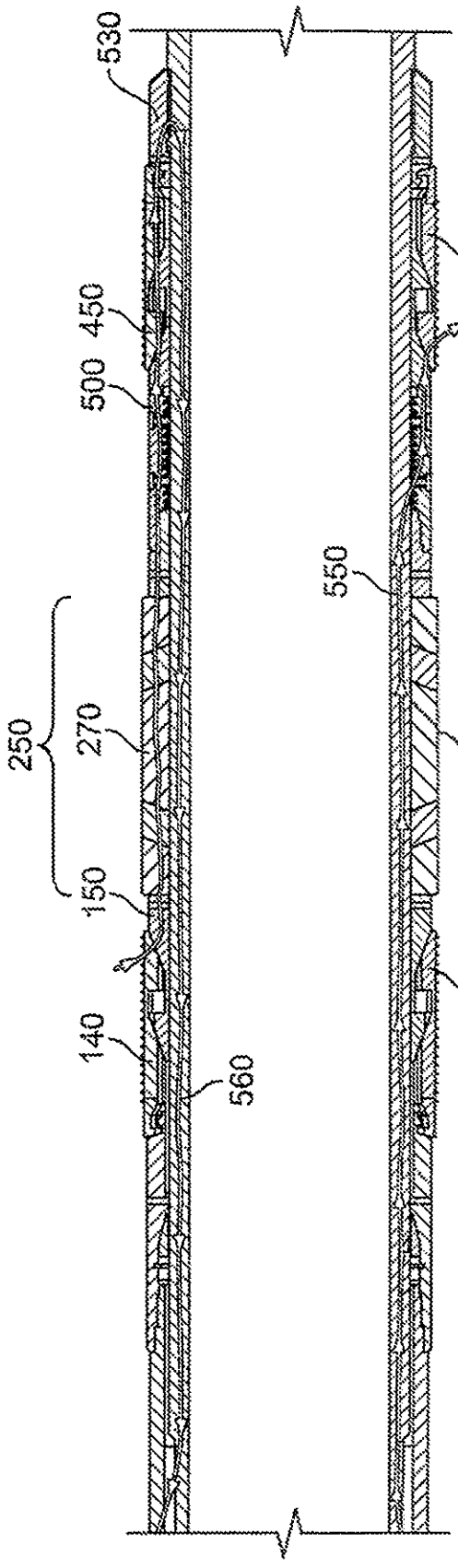

530
450
500
450
550
270
250
150
140
560
140
270
SET CONFIGURATION

FIG. 3B

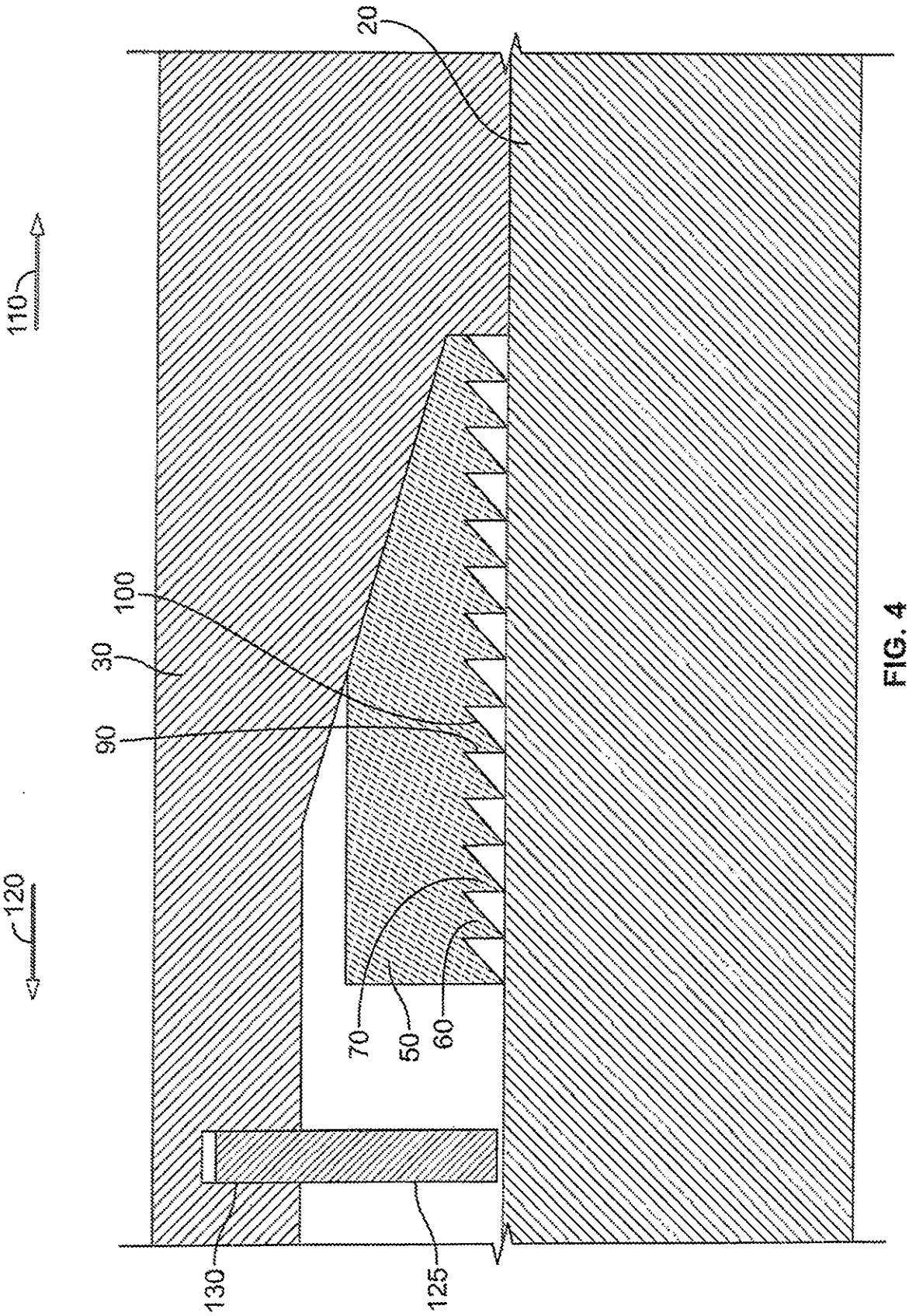


FIG. 4

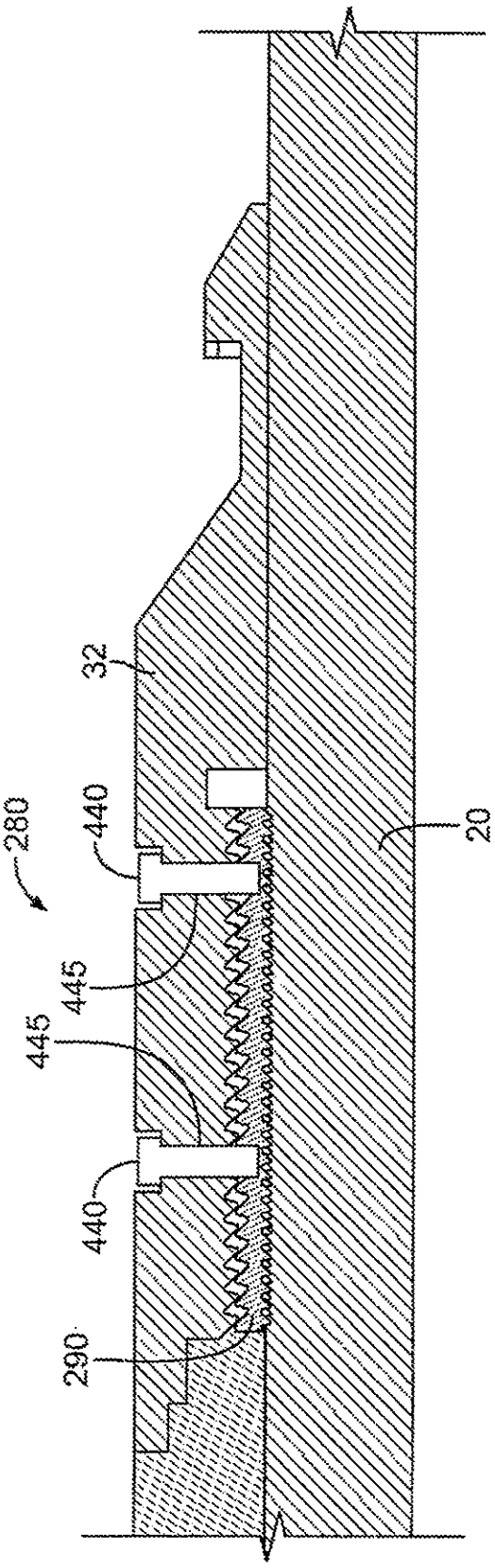


FIG. 5

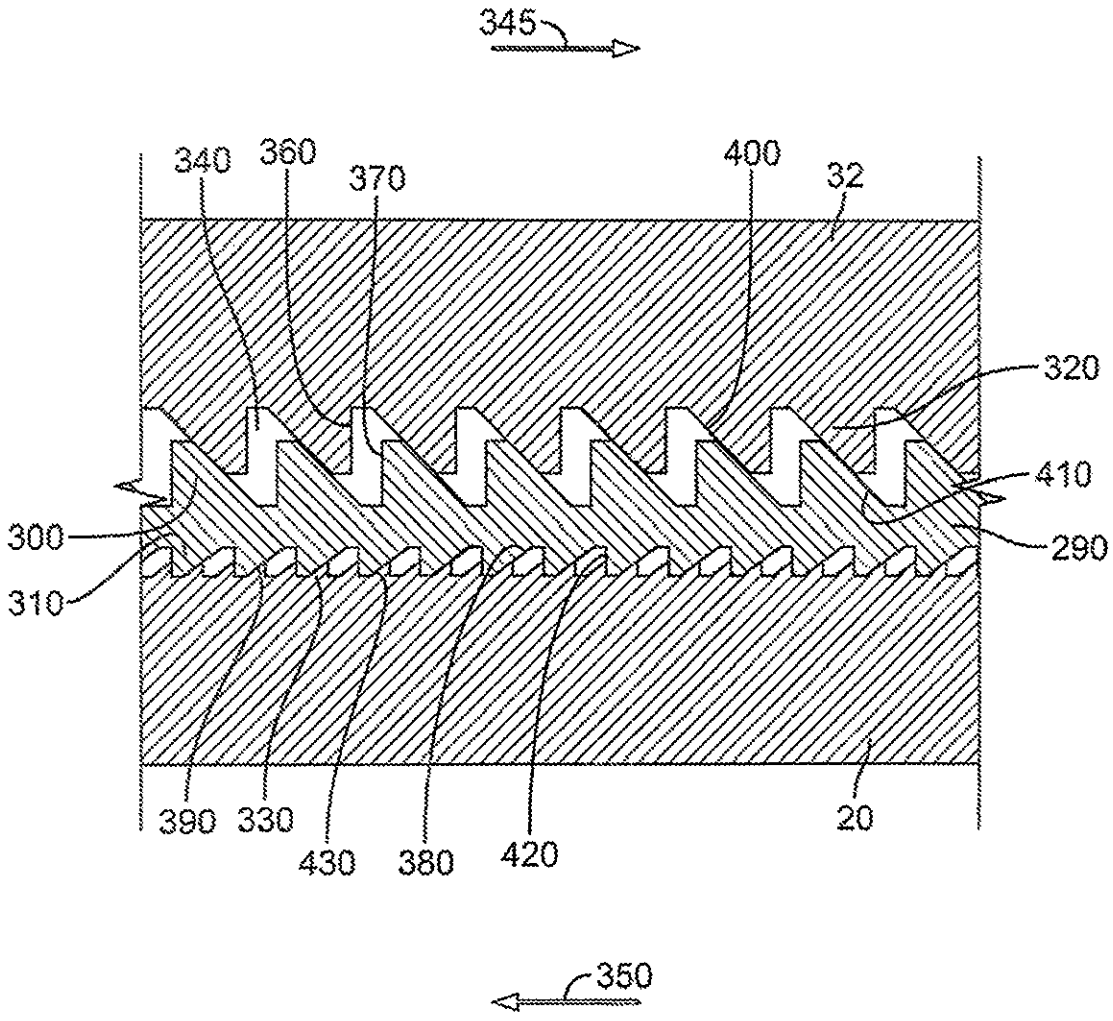

345
340
360
370
400
32
320
410
290
300
310
390
330
430
380
420
20
350

FIG. 6

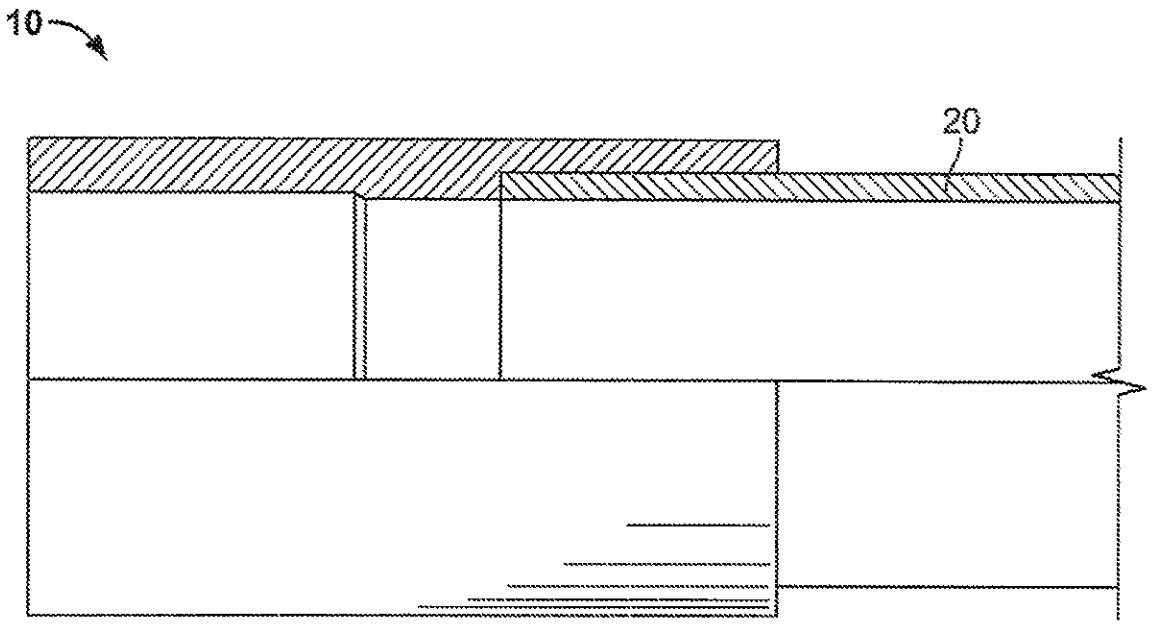


FIG. 7A

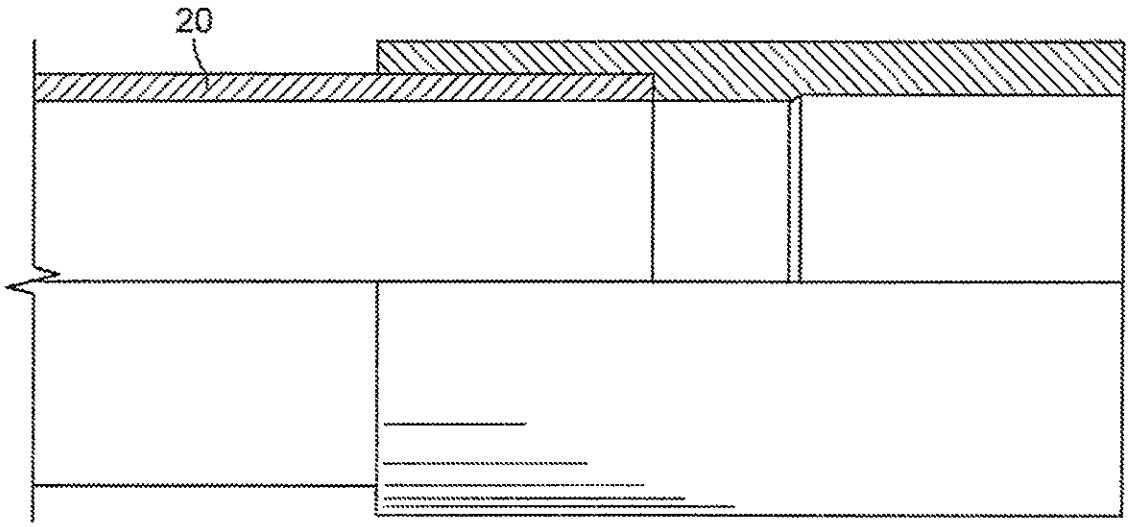


FIG. 7D

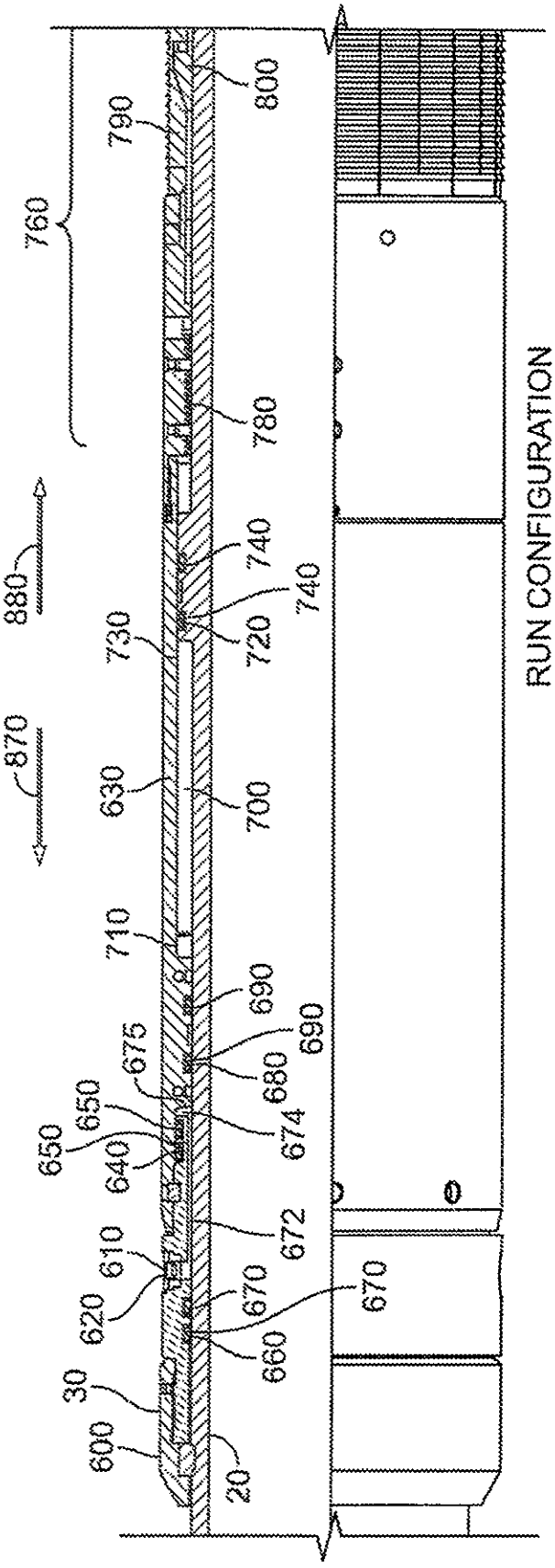

RUN CONFIGURATION
760
790
800
780
880
740
740
720
730
870
630
700
710
690
690
675
680
650
650
674
640
672
610
620
670
670
660
30
600
20

FIG. 7B

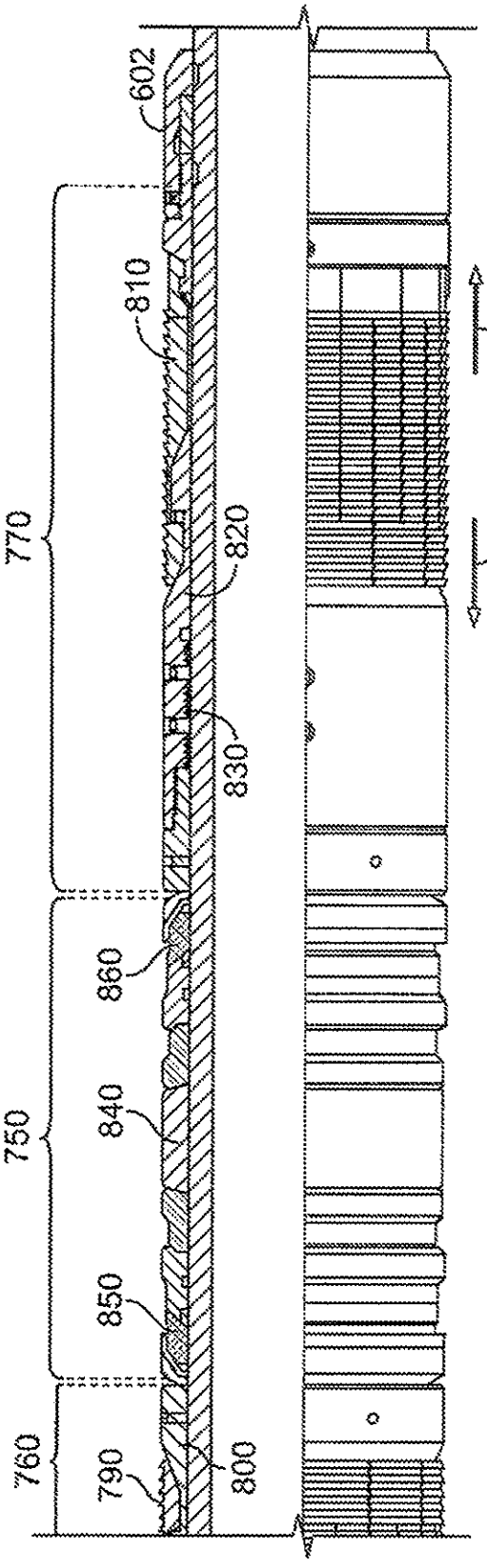

602
810
880
820
770
870
830
860
840
750
850
760
790
800

FIG. 7C

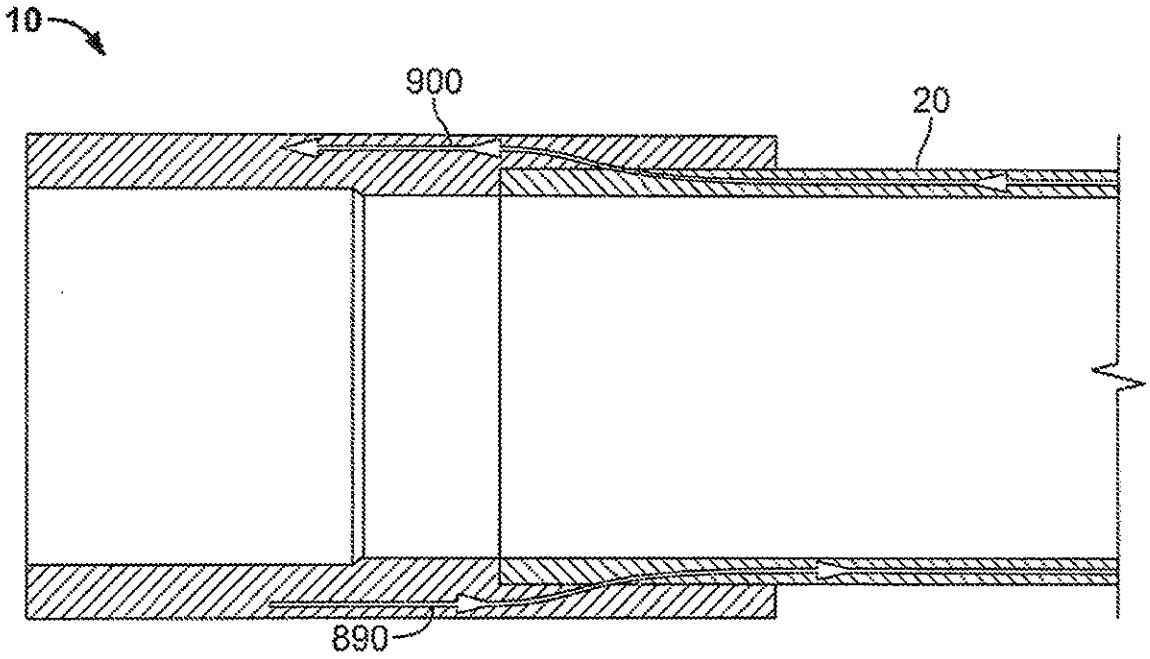


FIG. 8A

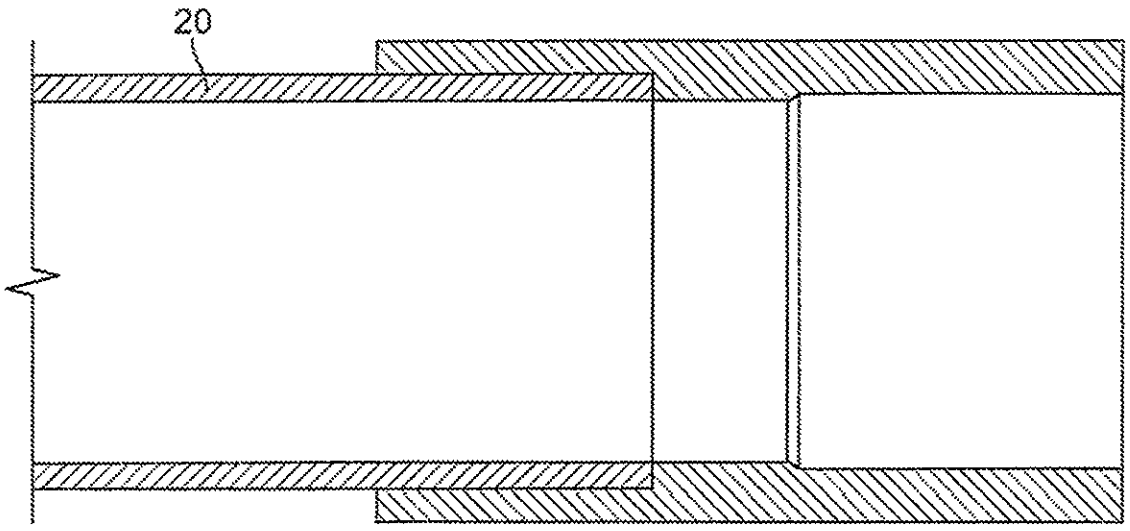


FIG. 8D

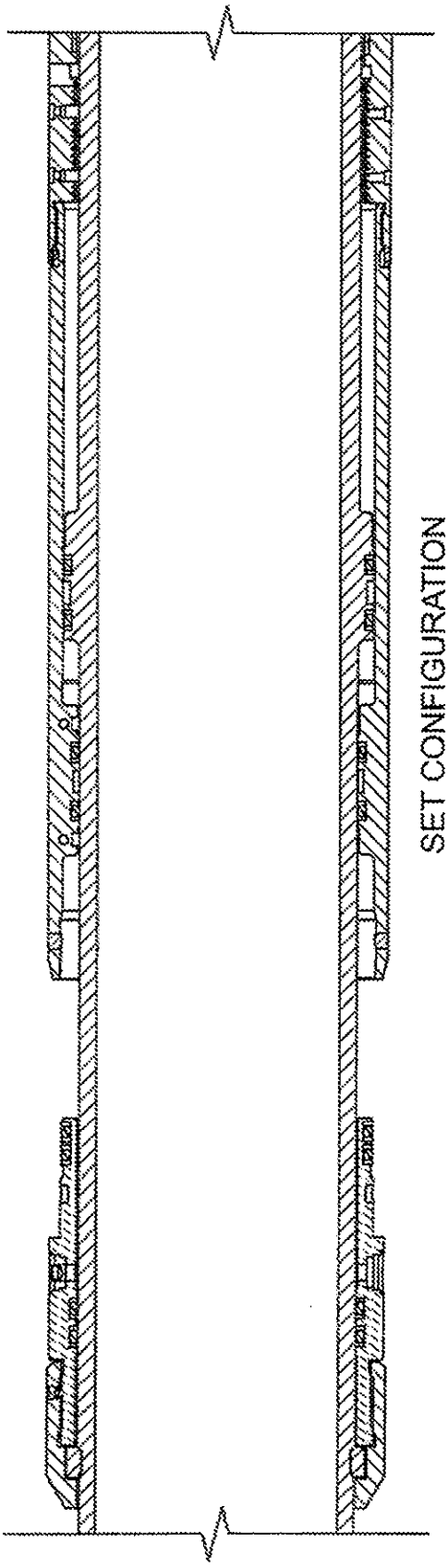


FIG. 8B

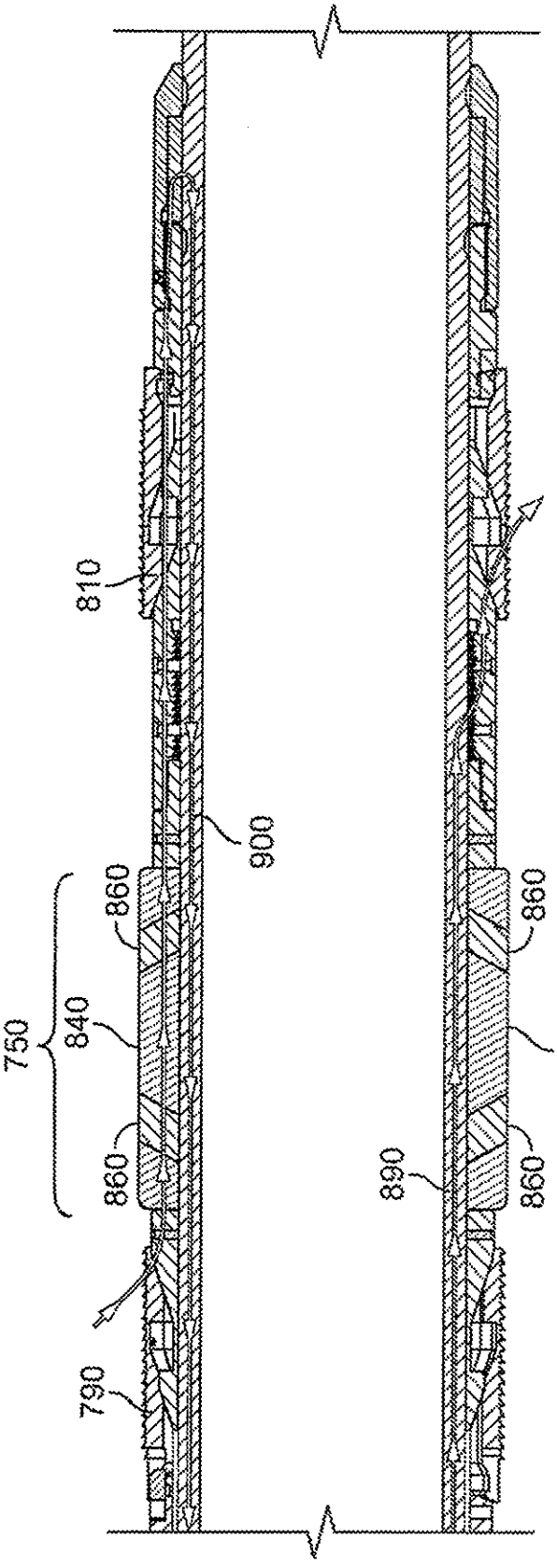


FIG. 8C

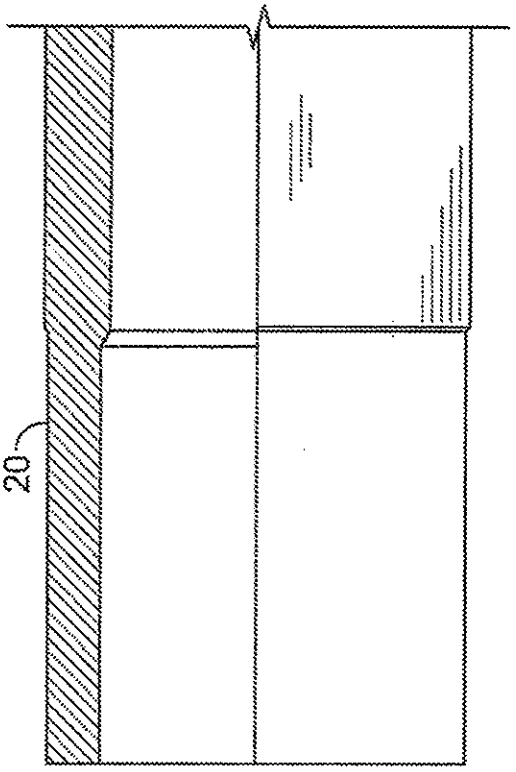


FIG. 9A

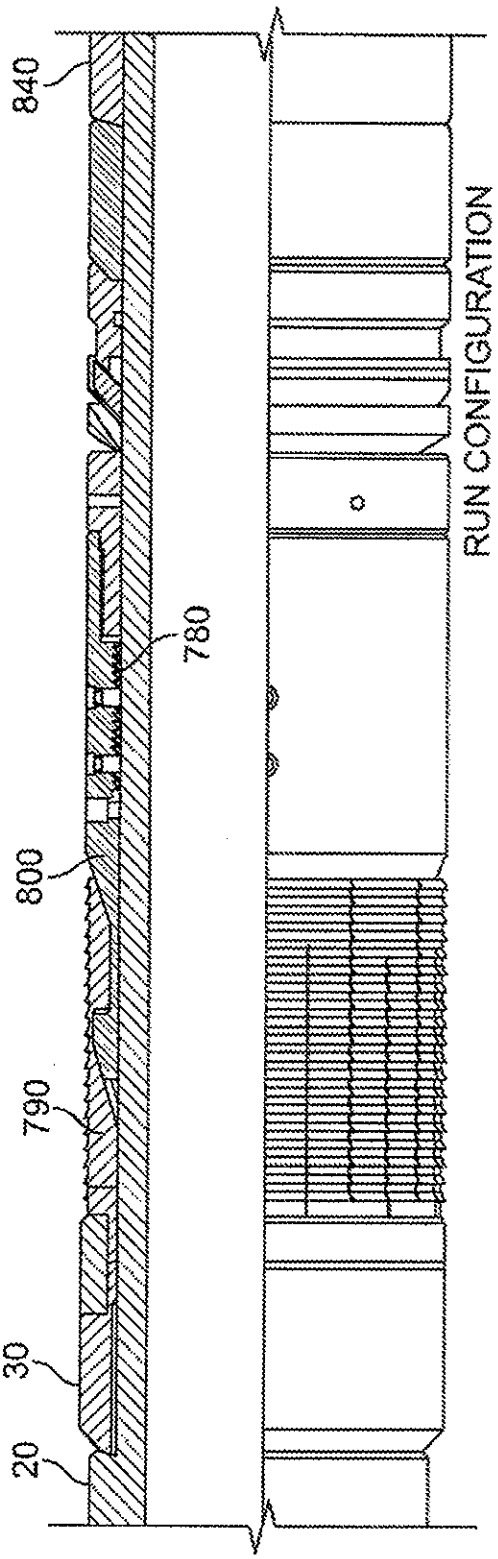


FIG. 9B

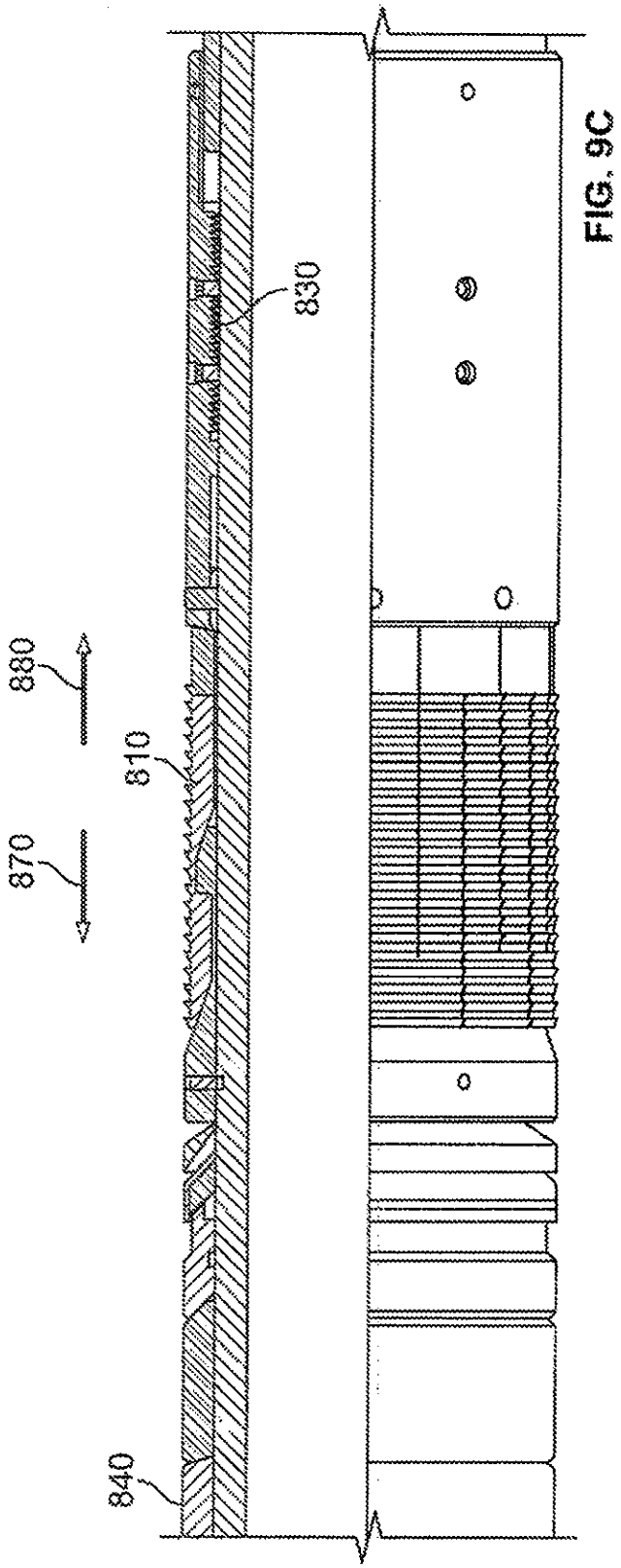


FIG. 9C

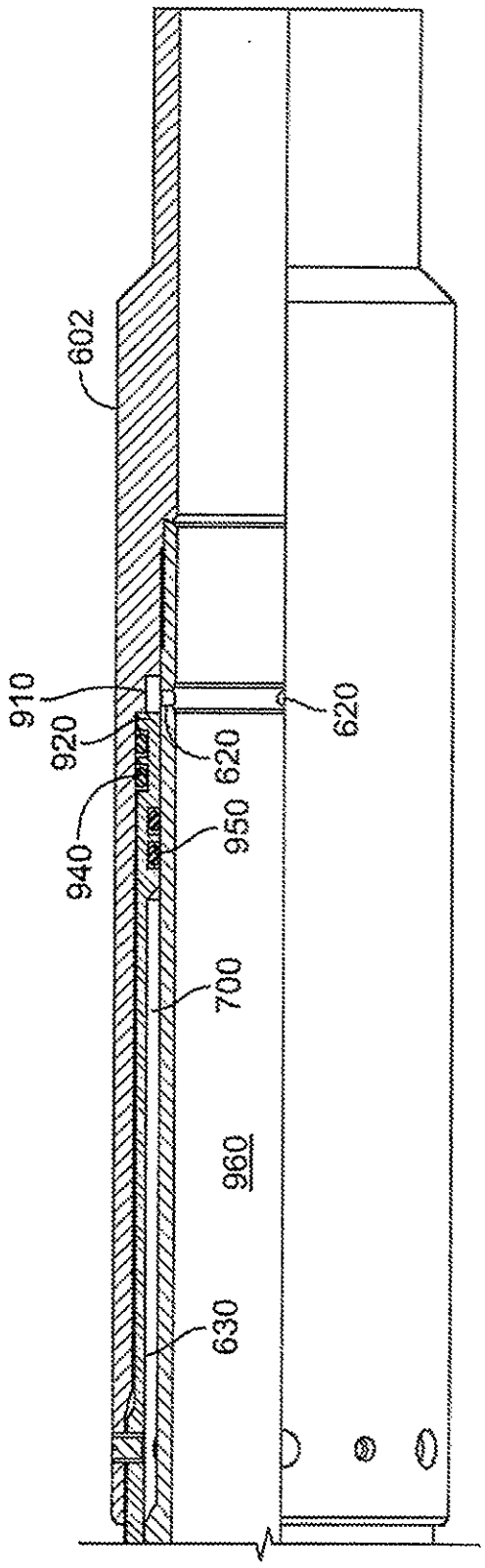


FIG. 9D

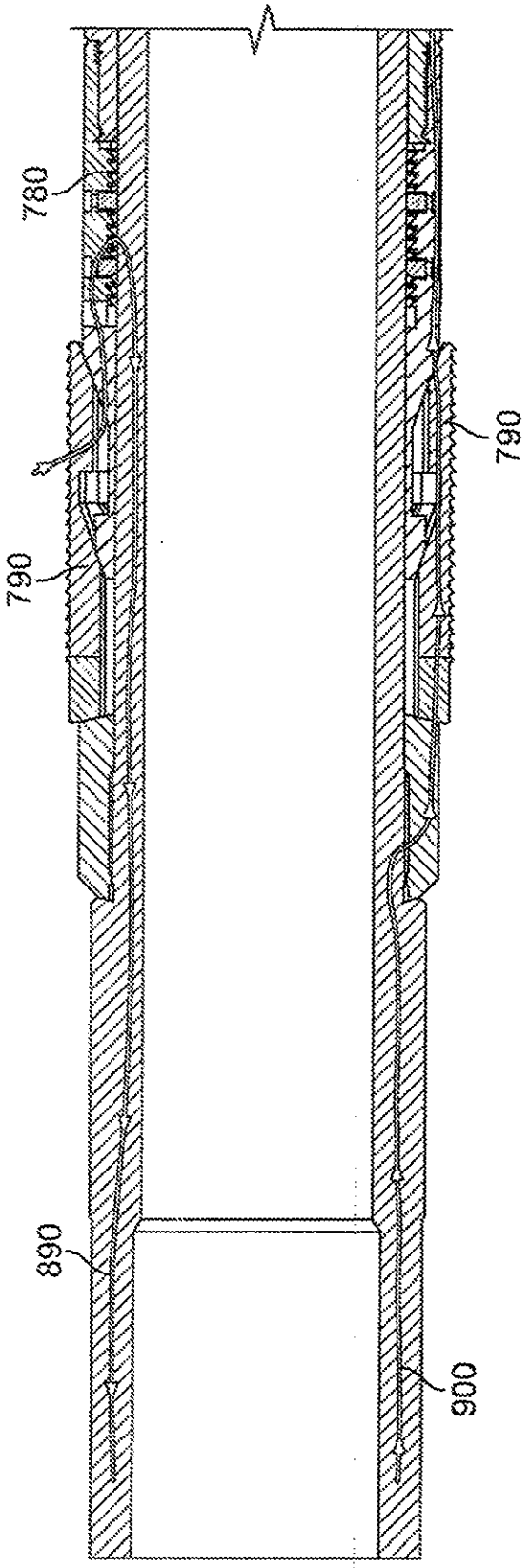


FIG. 10A

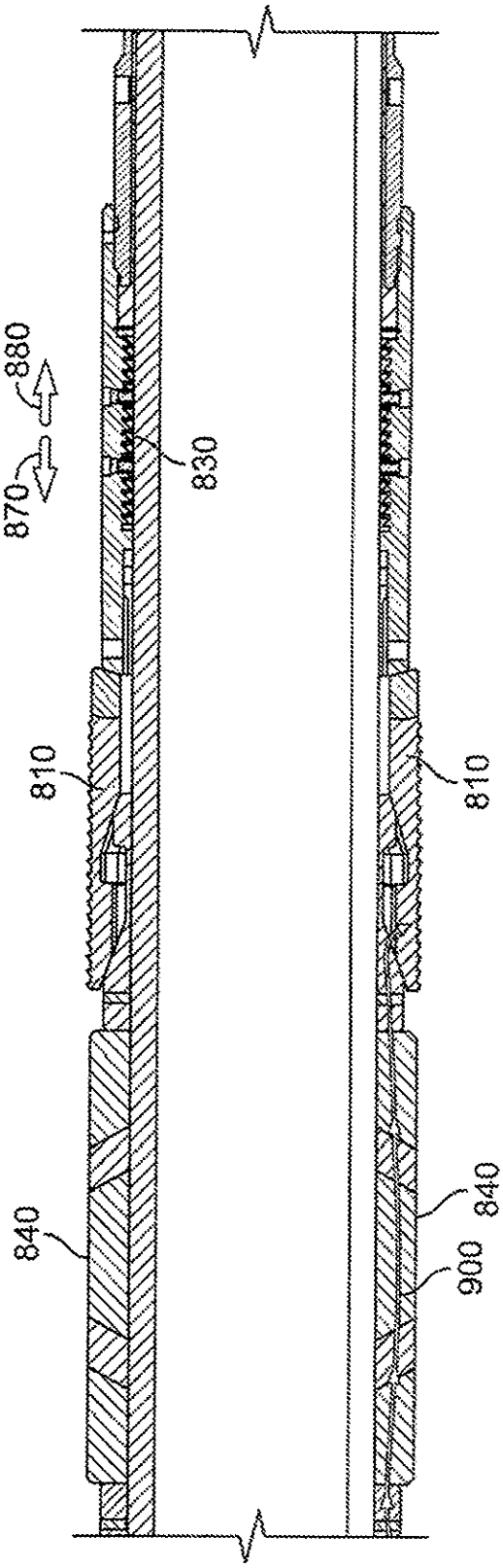


FIG. 10B

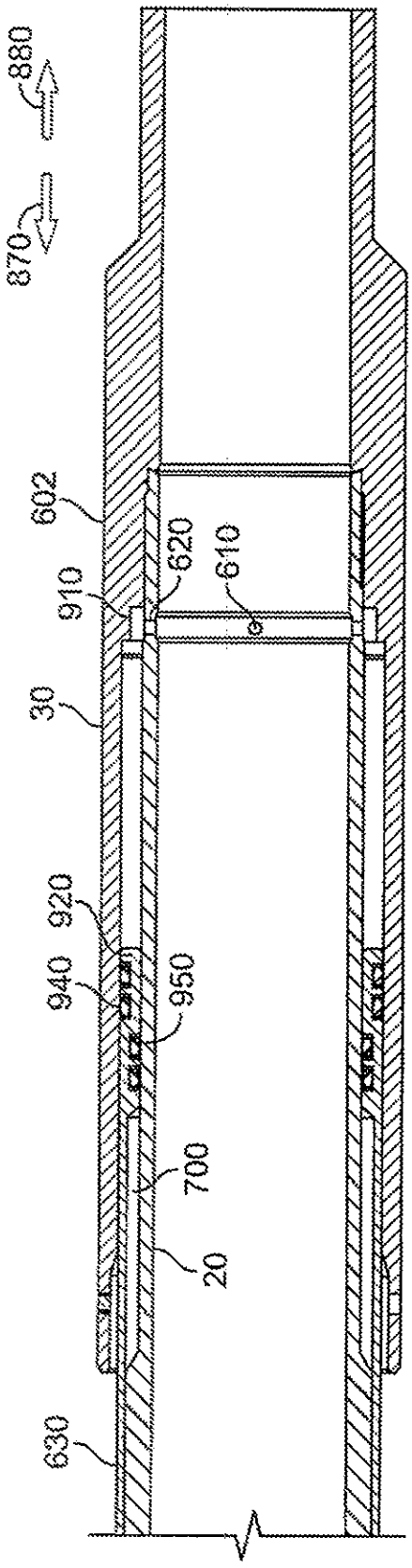


FIG. 10C