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SOLID ELECTROLYTIC CAPACITOR
AND METHOD OF MANUFACTURING THE SAME

ABSTRACT

The present invention aims at providing a chip-type solid electrolytic capacitor of large capacitance and low ESR, and its manufacturing method without expanding the mounting area when mounted on a device. An element section of the capacitor is composed of an anode oxide film layer which is to function as dielectrics, a solid electrolyte layer including conductive polymer and a cathode conductive body, which are stacked on a metallic electrode. A plurality of flat capacitor elements with the element section and an anode pulling-out section are stacked, and a laminated unit is produced where the cathode conductive layer and the anode pulling-out section of the flat capacitor elements are connected to a single metallic terminal member. The solid electrolytic capacitor of the present invention has at least two laminated unit placed parallel to each other, and connected to a transversely disposed comb terminal. The construction presented by the present invention achieves a solid electrolytic capacitor of a large capacitance and a low ESR without expanding its mounting area.

Fig. 3



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