

[27] Fig. 6D is a graph of a reflected light intensity collected over multiple cycles of a multi-mode pulse process, for implementing embodiments of the present disclosure.

[28] Fig. 6E is a graph of a pattern of one or more selected process variable during multiple cycles of a multi-mode pulse process, for implementing embodiments of the present disclosure.

[29] Fig. 6F is a graph of a endpoint portion of the pattern compared to graph of one or more selected process variables during multiple cycles of a multi-mode pulse process, for implementing embodiments of the present disclosure.

[30] Fig. 6G is a graph of the difference distance between the graph and the endpoint portion of the pattern , for implementing embodiments of the present disclosure.

[31] ⁷
Fig. 7.1 is a flowchart diagram that illustrates the method operations performed in identifying a selected process point in a multi-mode pulsing process, for implementing embodiments of the present disclosure.

[32] Fig. 8 is a block diagram of an example computer system for carrying out the processing according to the invention.