ABSTRACT

Liquid Crystal Display Driver System and Method Therefor

An LCD Driver System (10) includes a row decoder (38) for providing command signals to row drivers (50) and a RAM row decoder (64) for controlling a RAM (76) which supplies data to a column driver (88). The row and column drivers (50 and 88) are supplied with power by a power source (58) which can supply either high or low voltages. In normal mode, the power source (58) supplies high voltage and the row driver (50) drives both a dot matrix portion (14) and an icon portion (16) of an LCD display. In an icon mode, a disable signal disables the row decoder (38) and the RAM row decoder (64), enables only the icon row driver (54) of the row drivers (50) and the icon row of the RAM (76) and switches the power source (58) to supply low voltage so as to save power.

FIG. 1 accompanies this abstract.