

FIG. 1

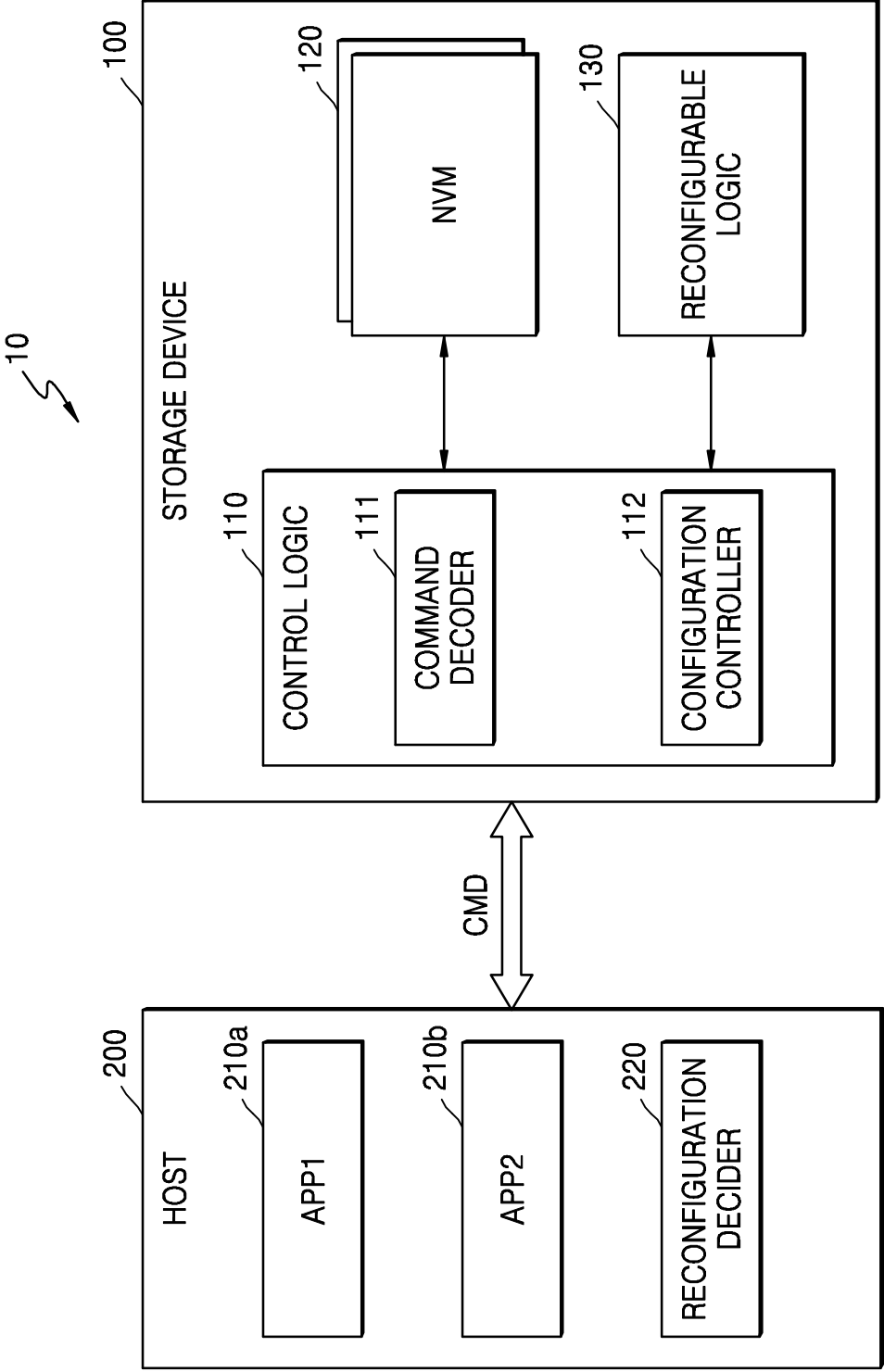


FIG. 2

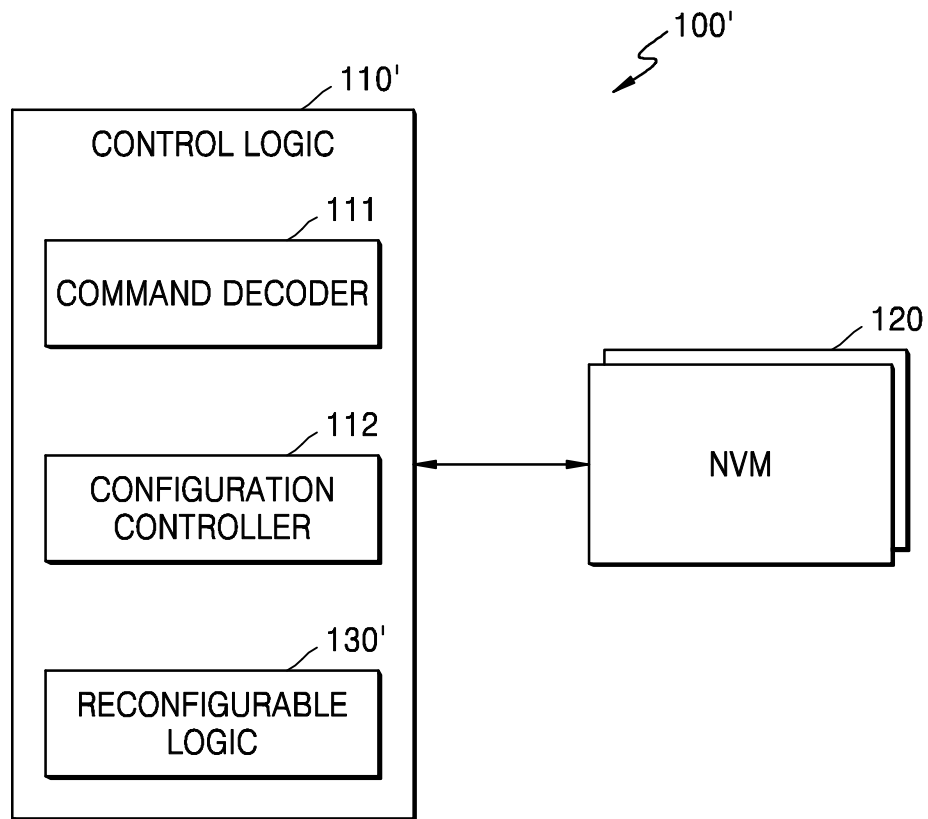


FIG. 3

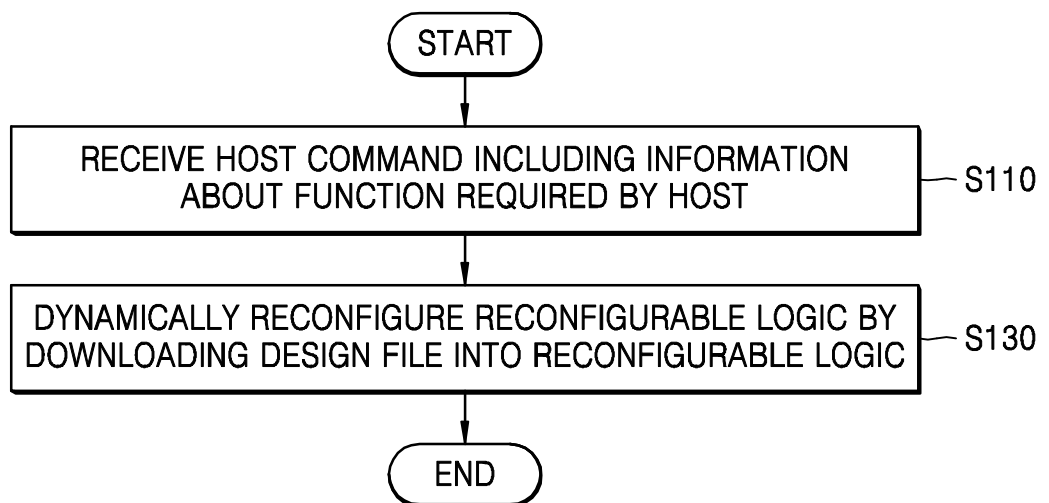


FIG. 4

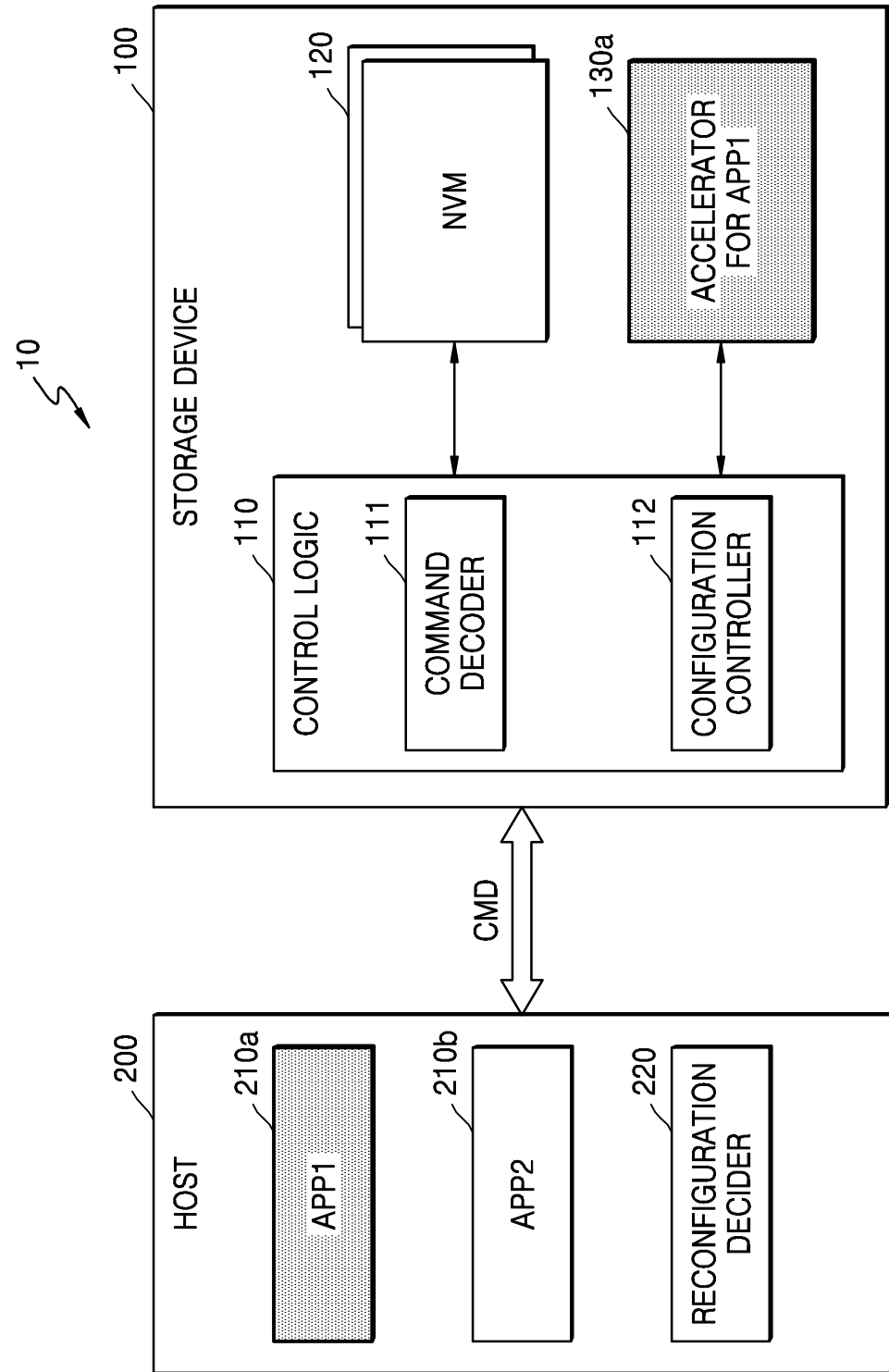
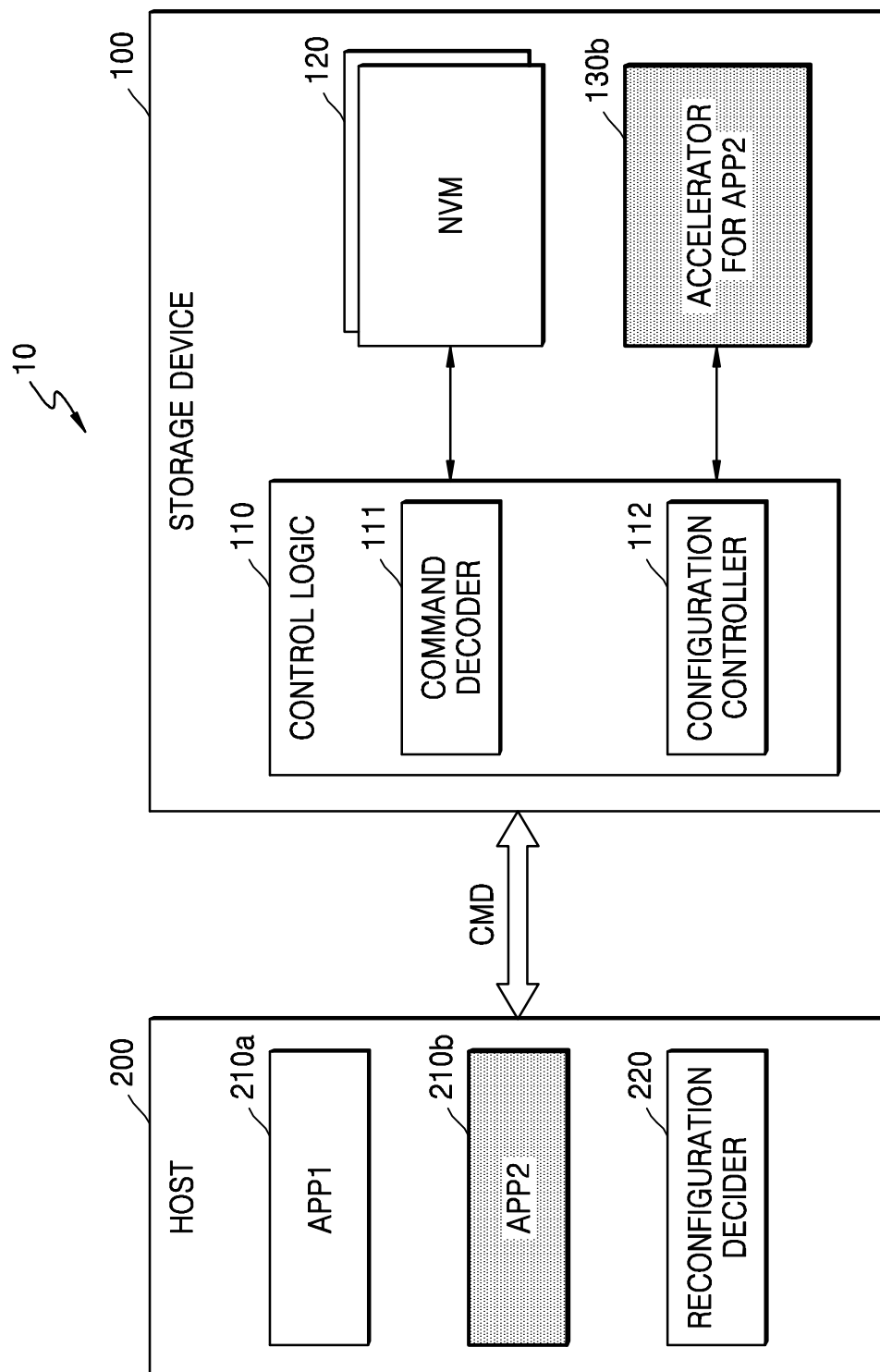


FIG. 5



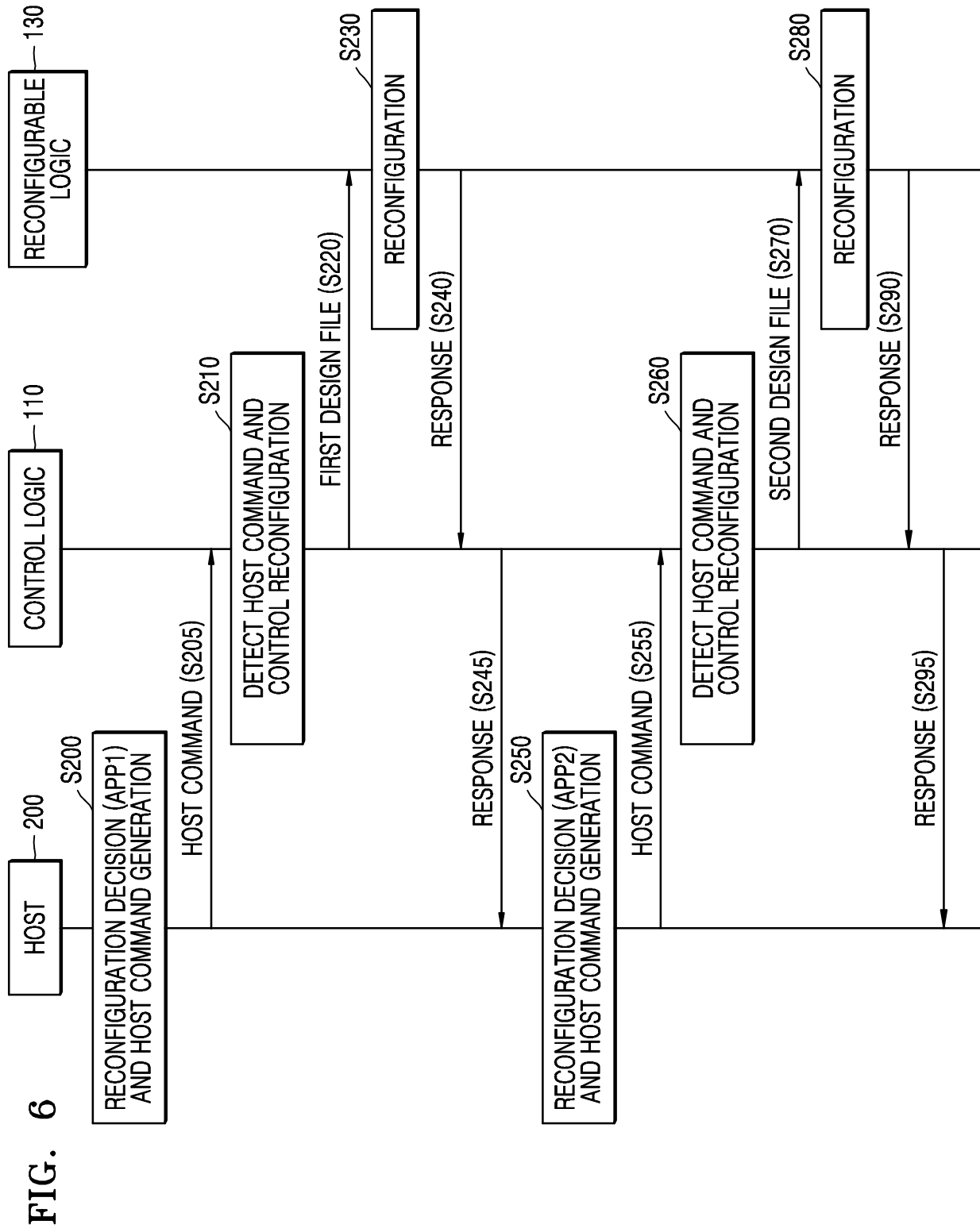


FIG. 7

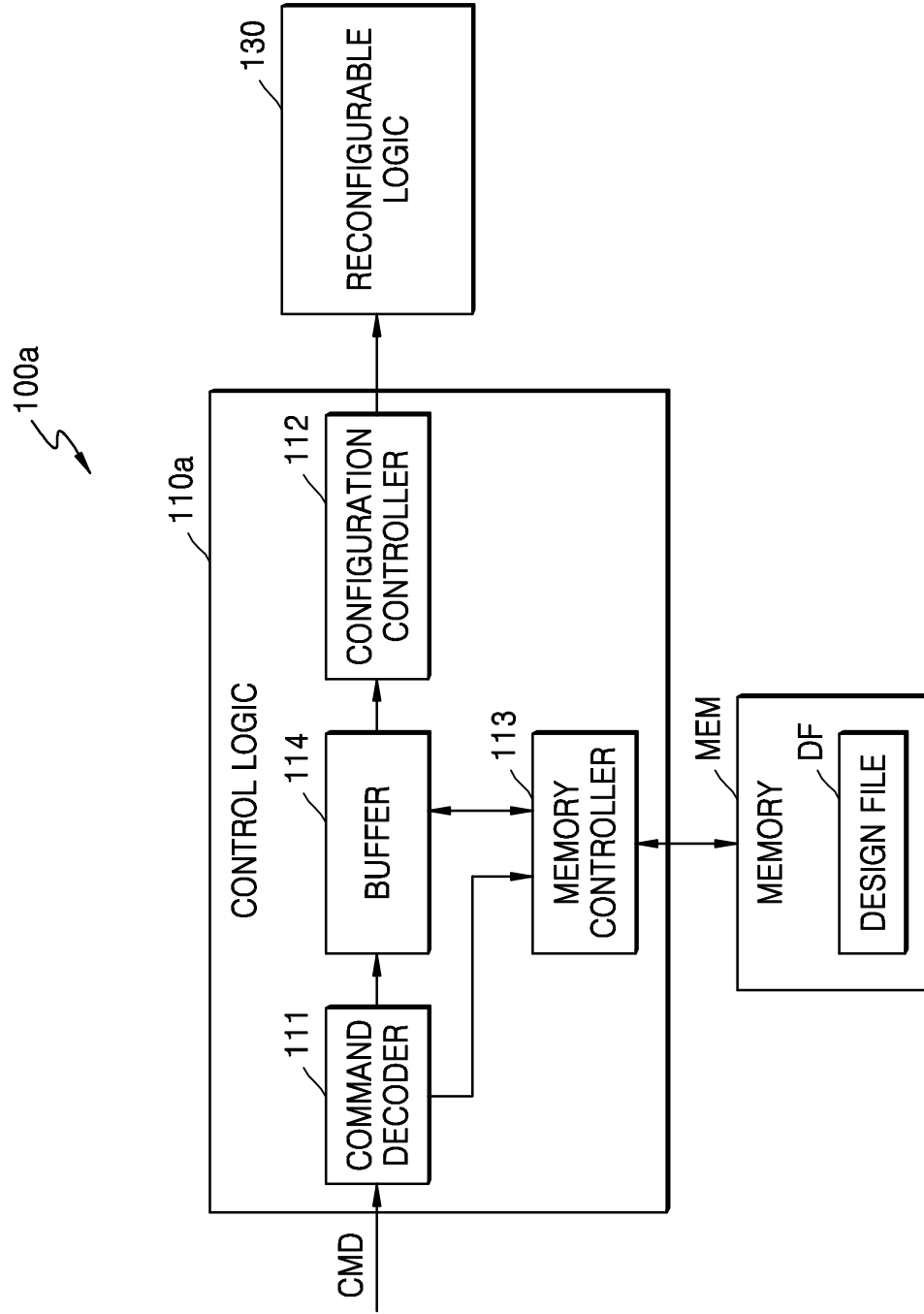


FIG. 8

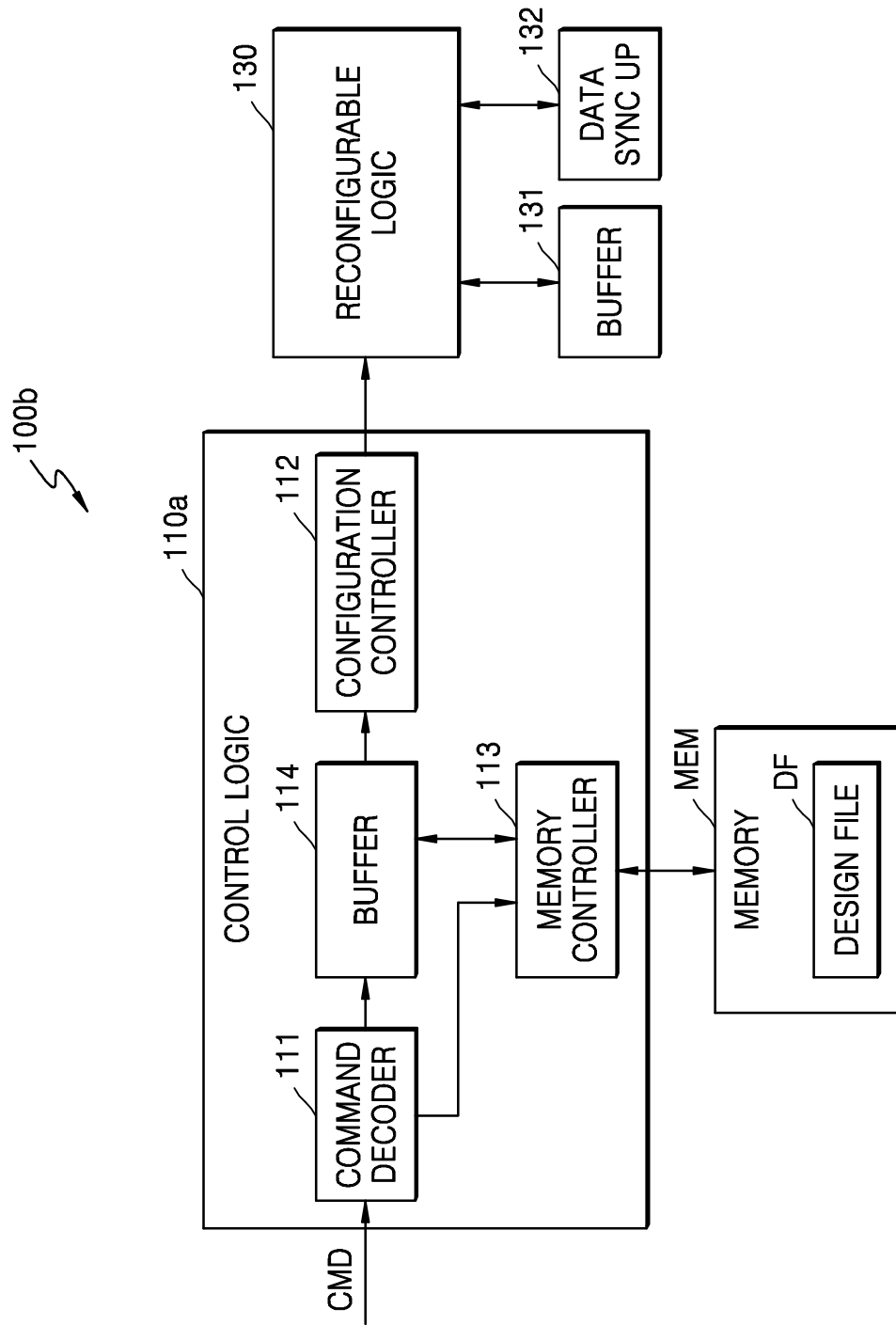


FIG. 9

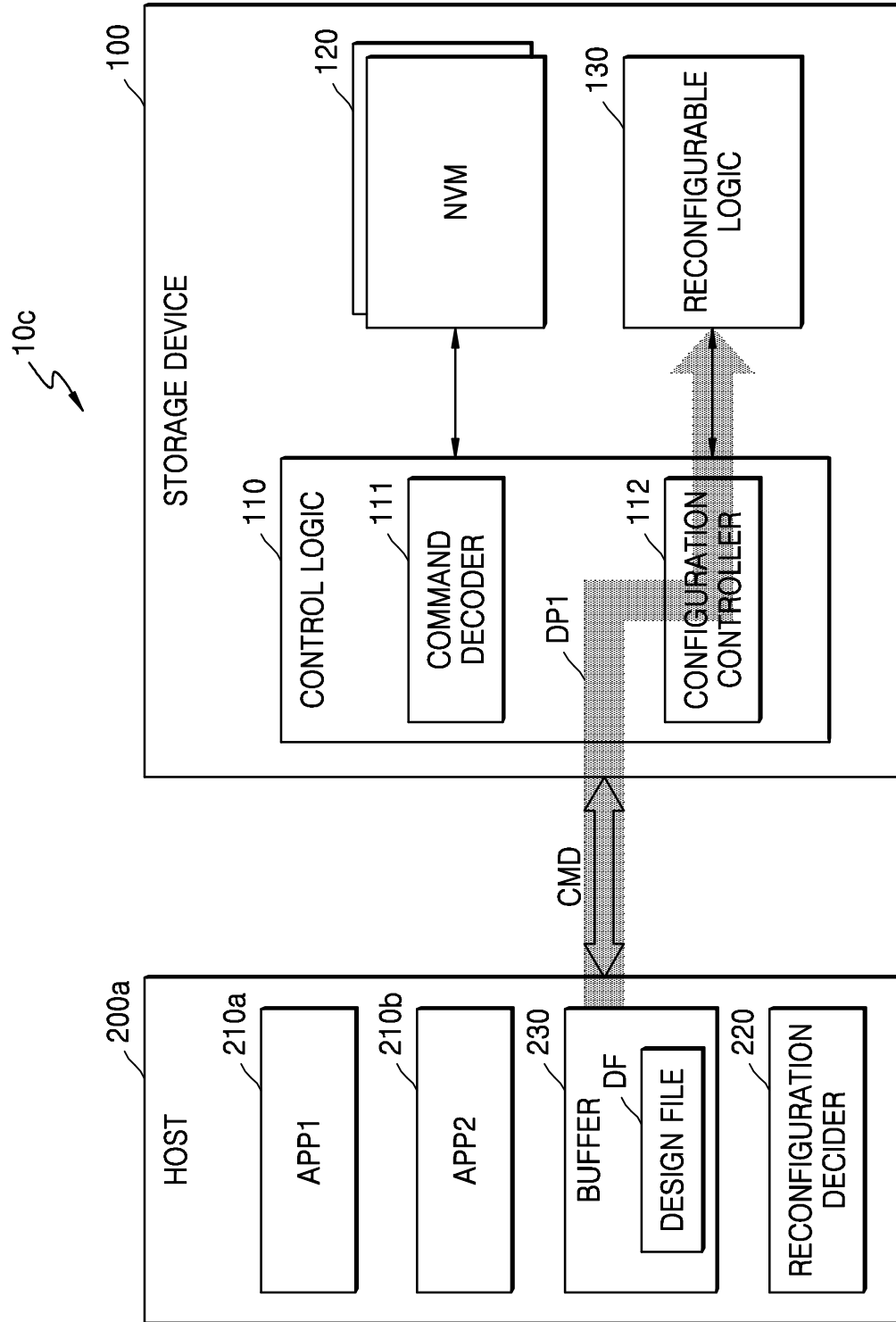


FIG. 10

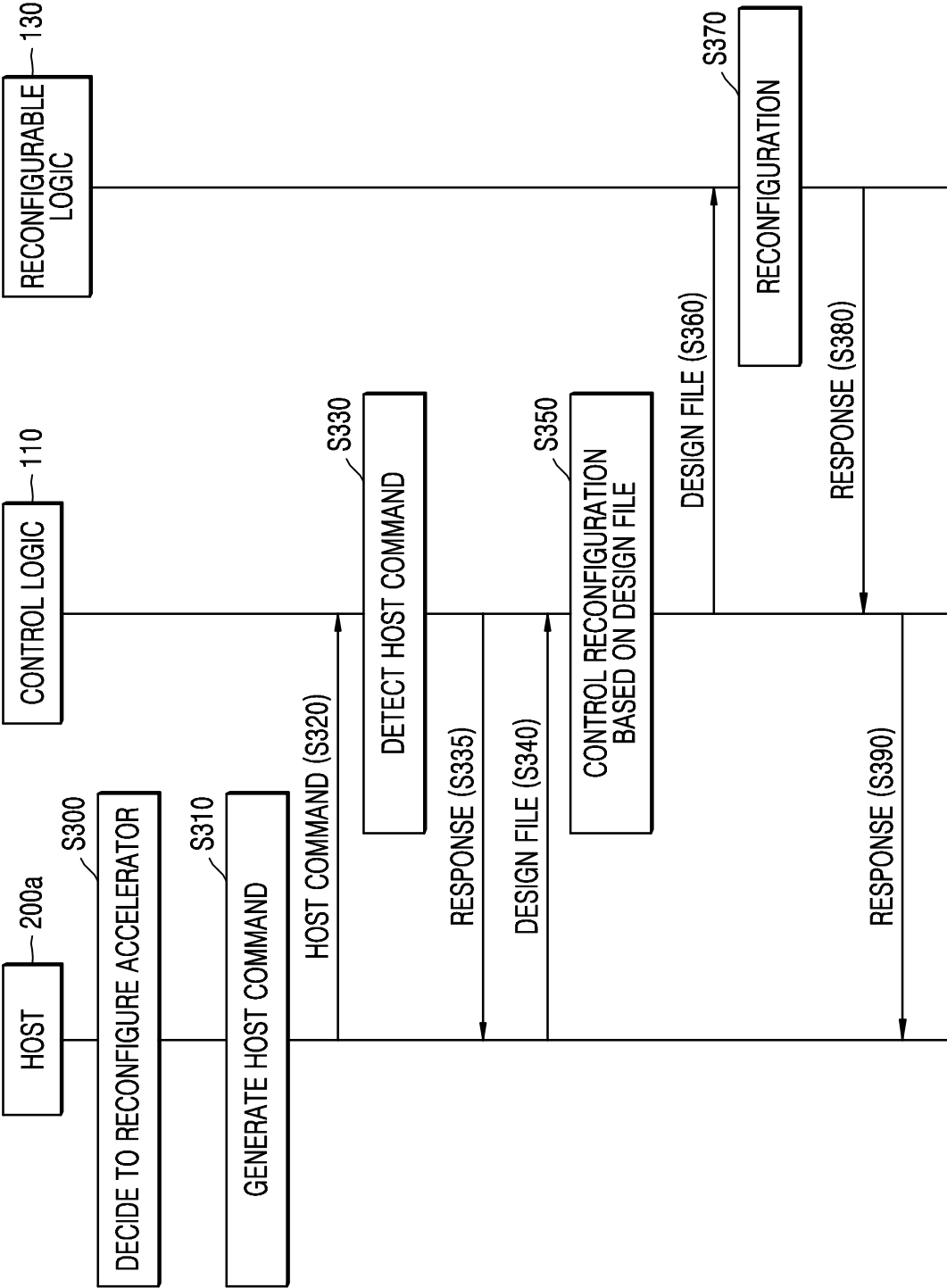


FIG. 11

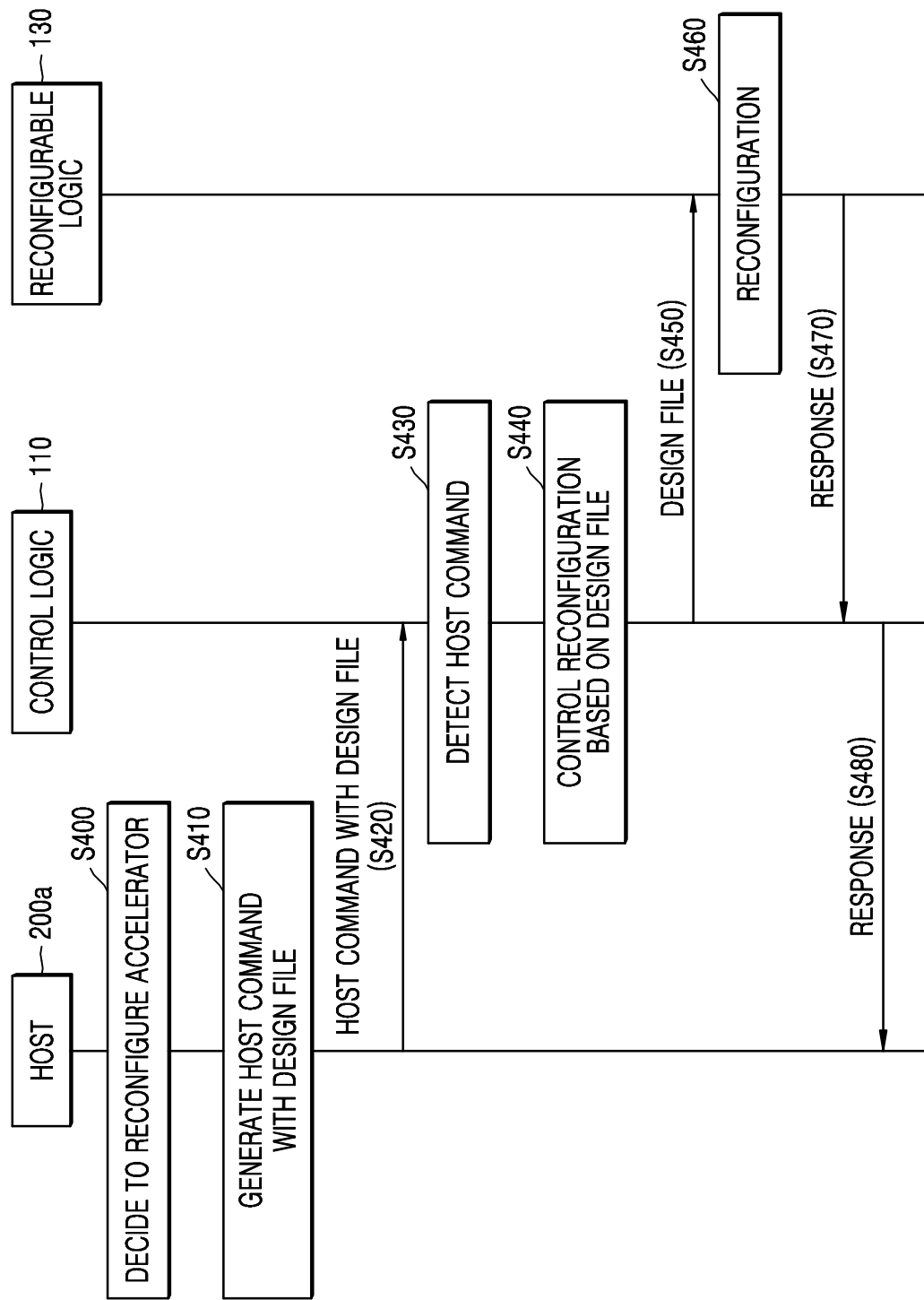


FIG. 12

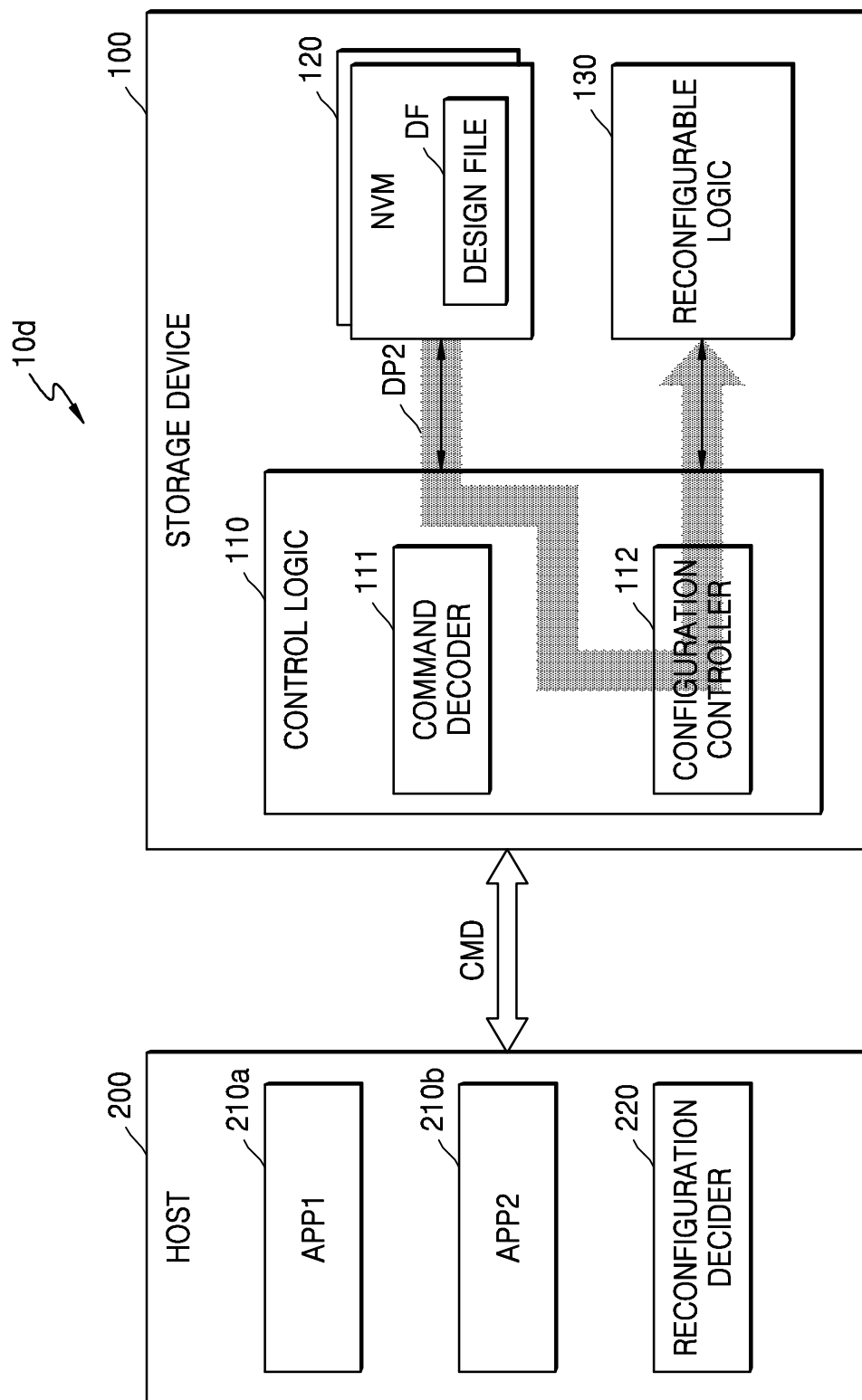


FIG. 13

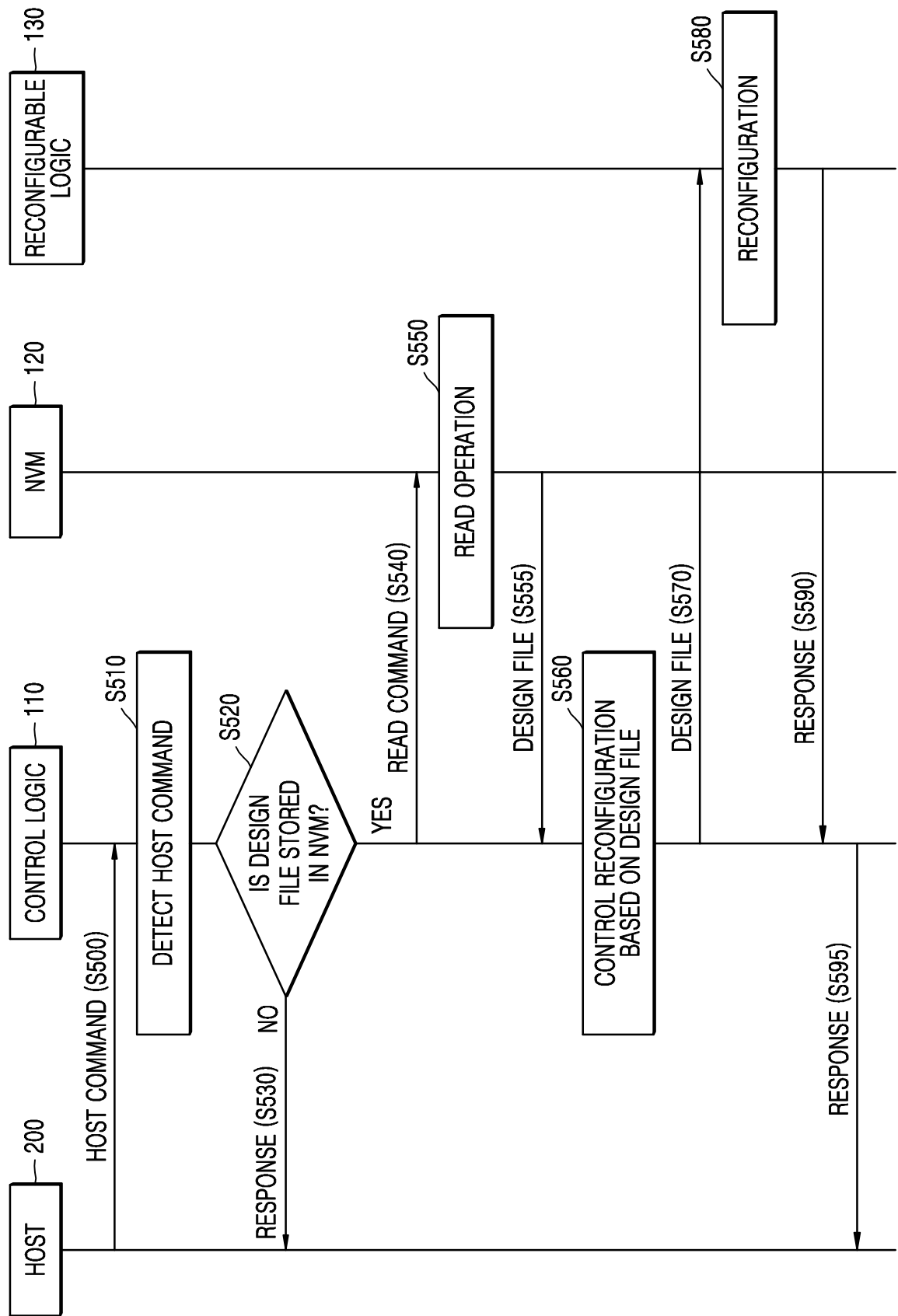


FIG. 14

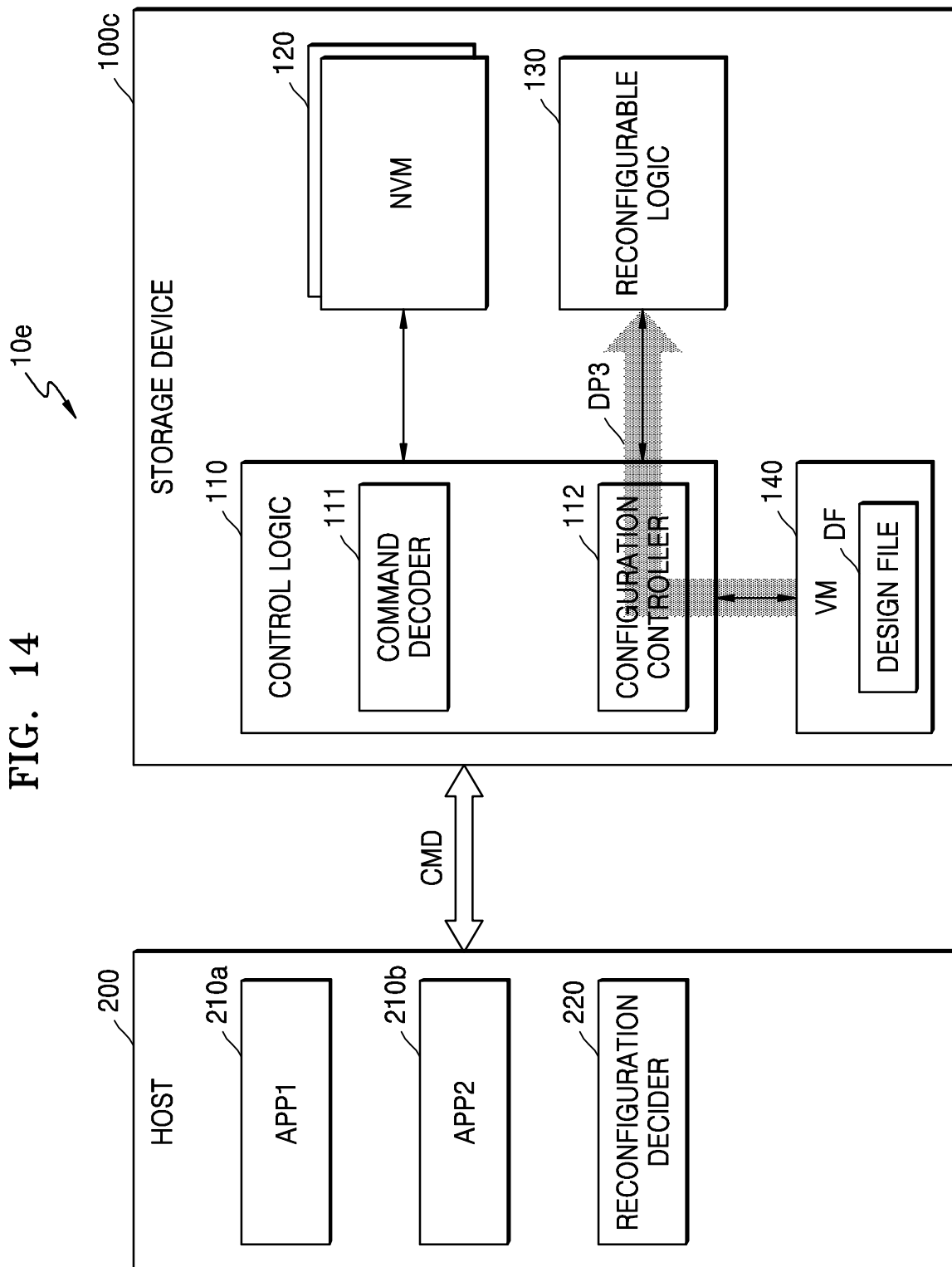


FIG. 15

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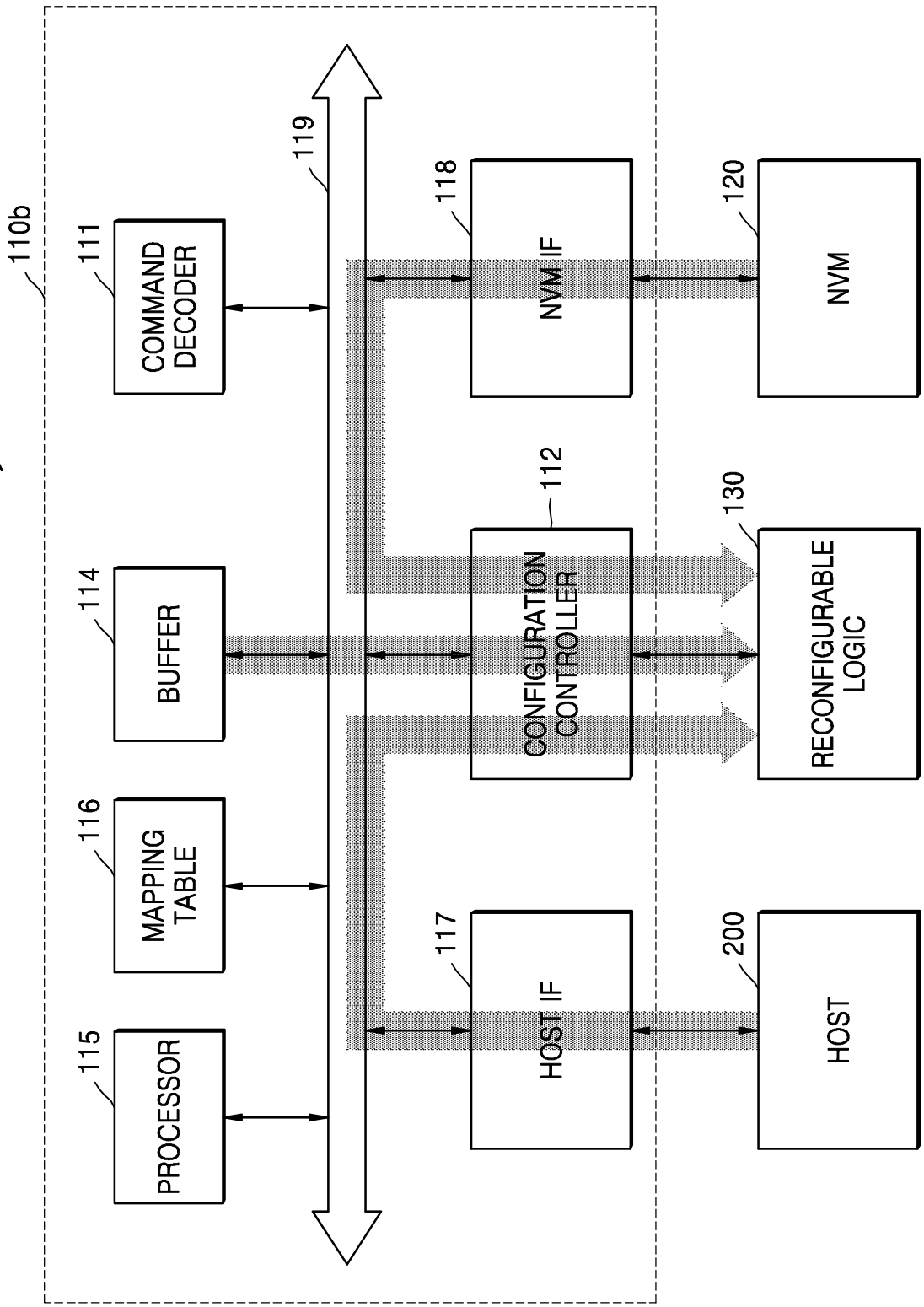
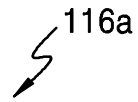
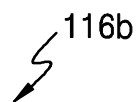


FIG. 16AMAPPING TABLE FOR NVM

DESIGN FILE	PPN
DF1a	PPN1a
DF2a	PPN2a
DF3a	PPN3a
...	...

FIG. 16BMAPPING TABLE FOR VM

DESIGN FILE	PPN
DF1b	PPN1b
DF2b	PPN2b
DF3b	PPN3b
...	...

FIG. 17

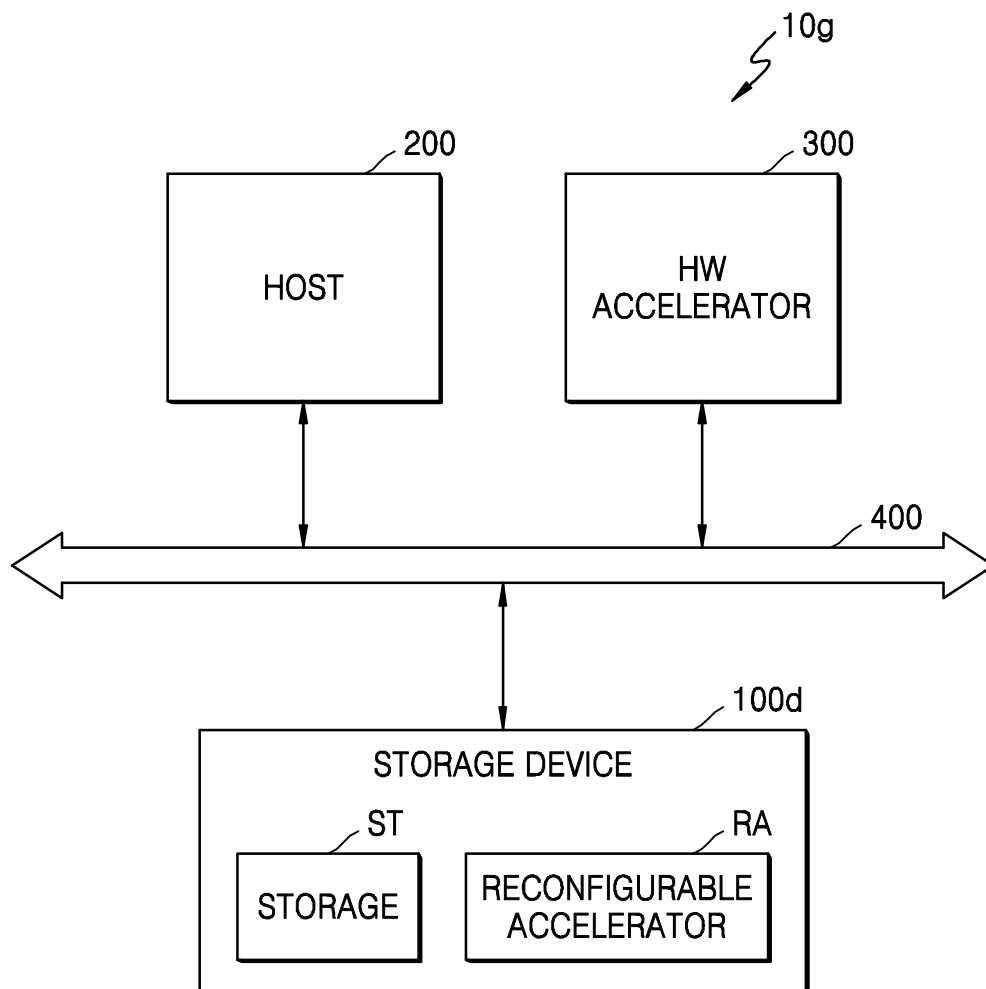


FIG. 18

