

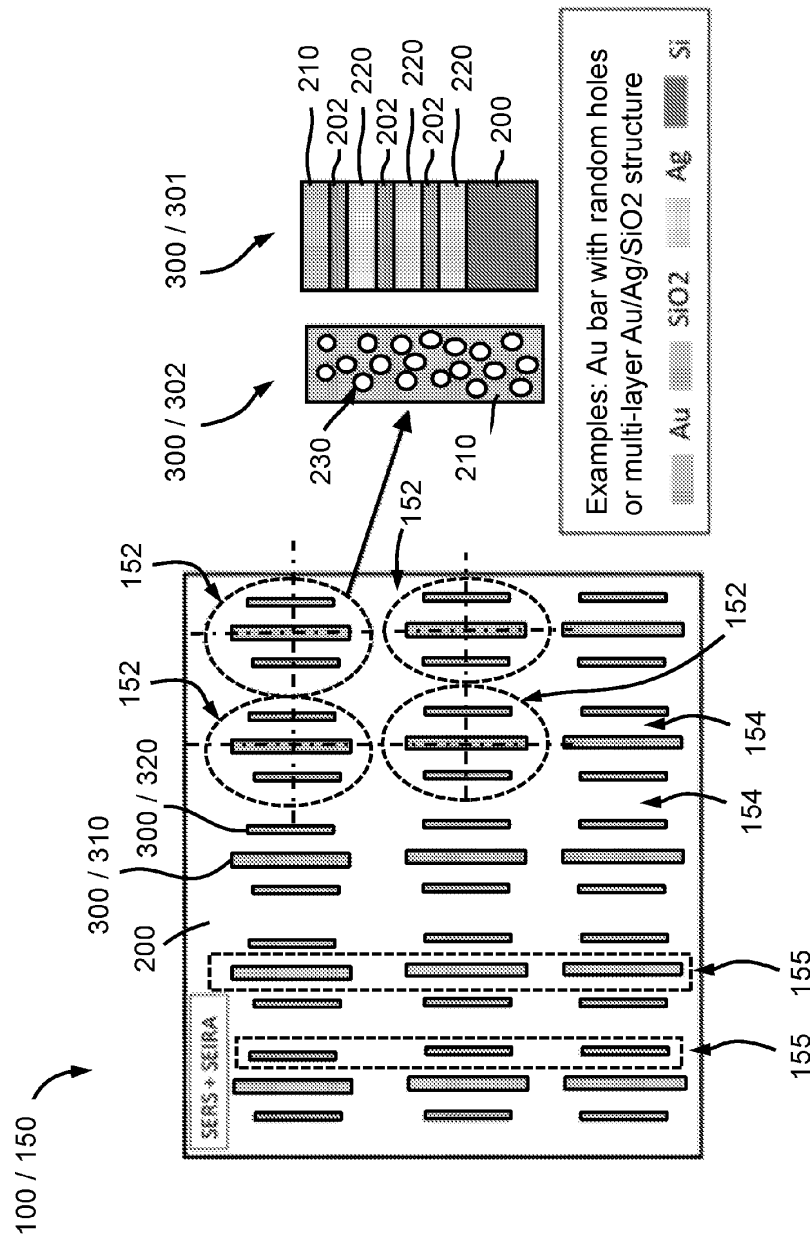
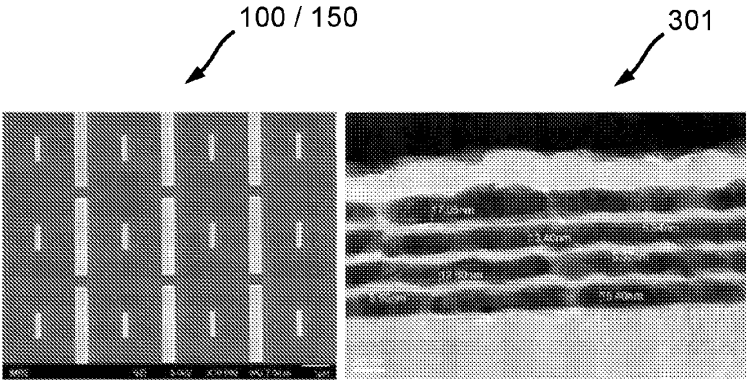
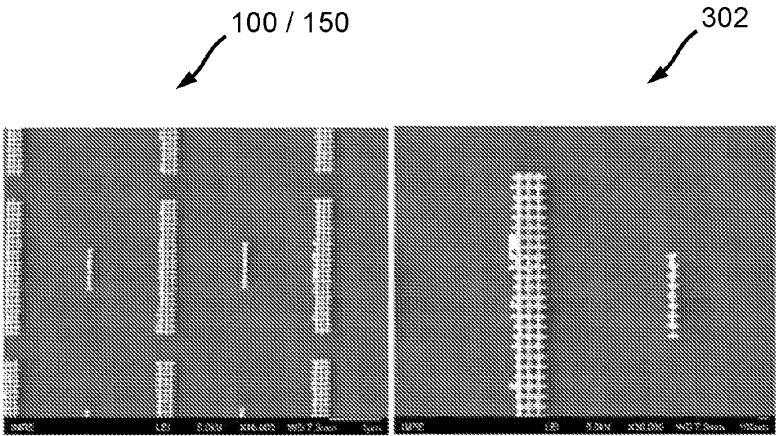


FIG. 1



SEM images of fabricated dual SERS-SEIRA substrate with multi-layer metal-dielectric-metal structure

FIG. 2



SEM images of fabricated dual SERS-SEIRA substrate with patterned holes.

FIG. 3

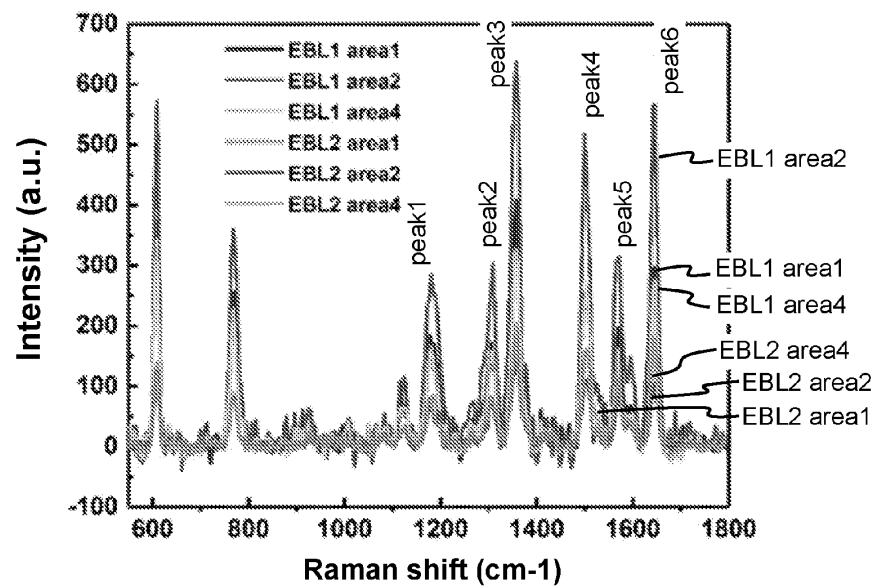


FIG. 4

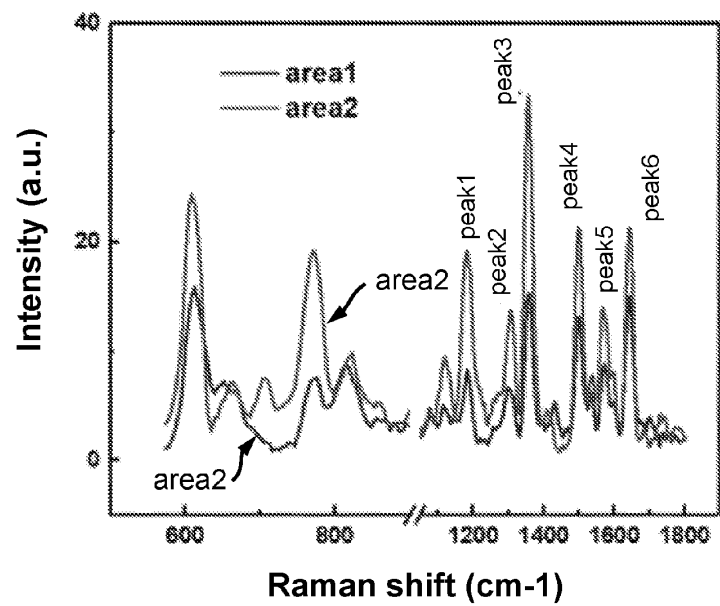


FIG. 5

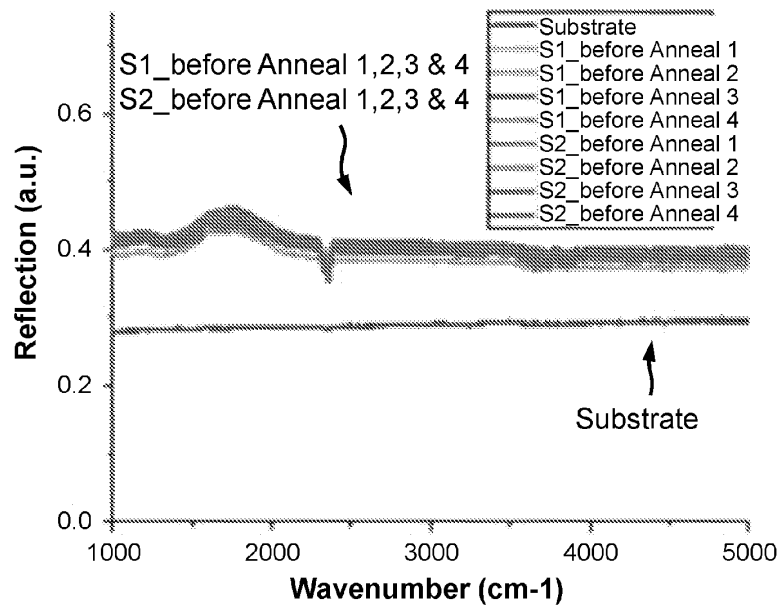


FIG. 6

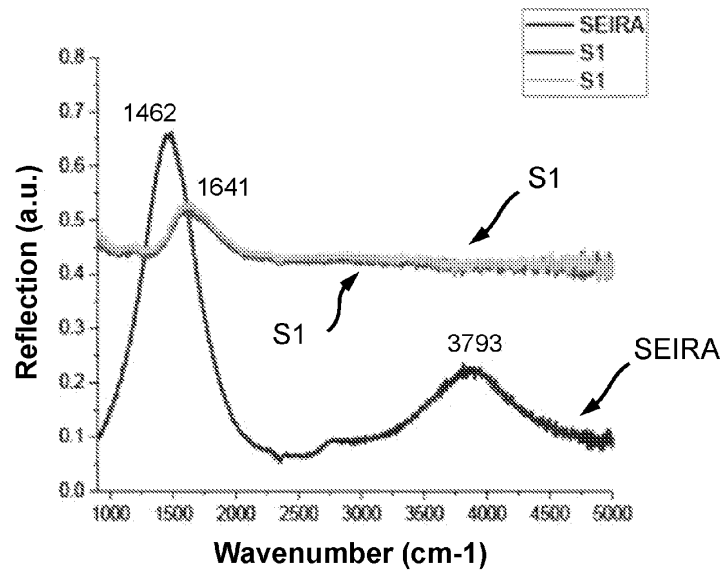


FIG. 7

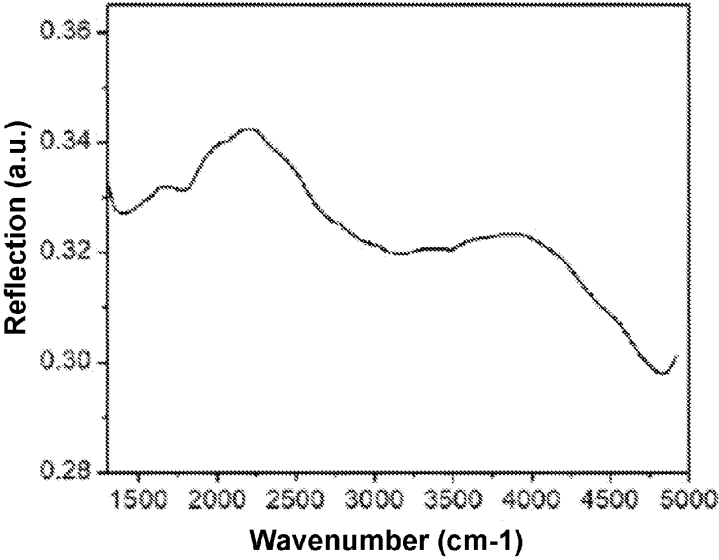


FIG. 8

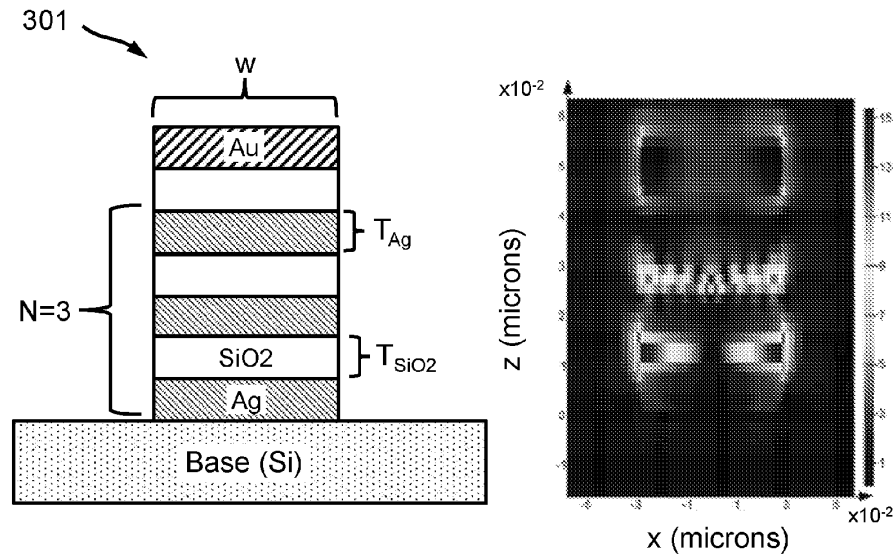


FIG. 9

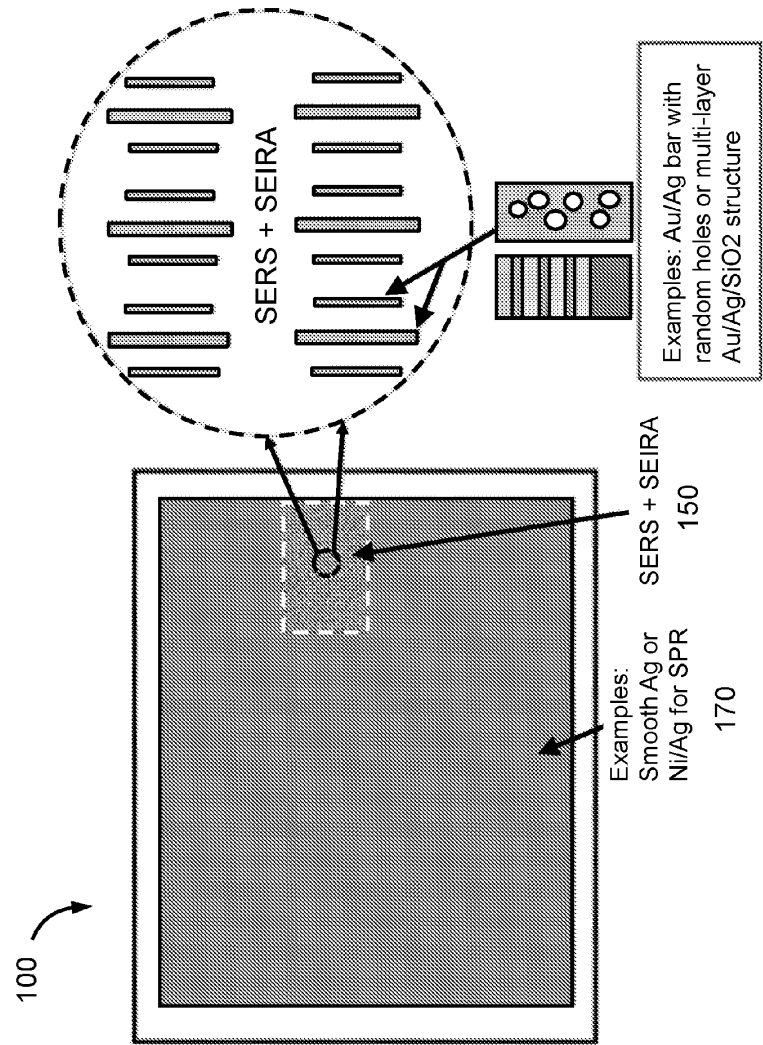


FIG. 10