

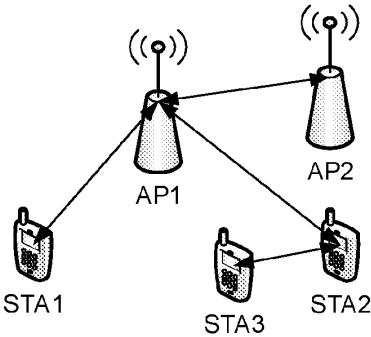


图 1A

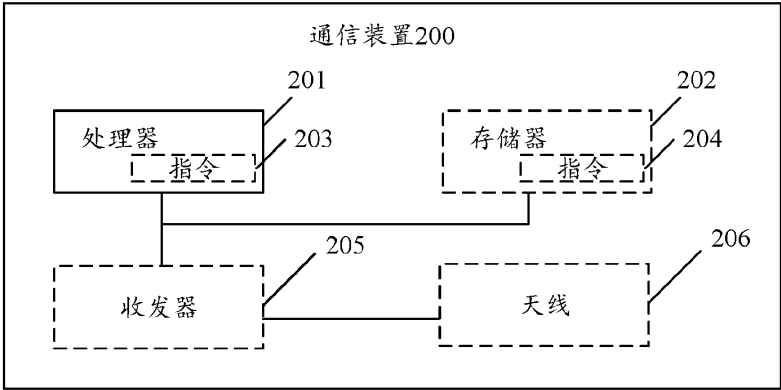


图 1B

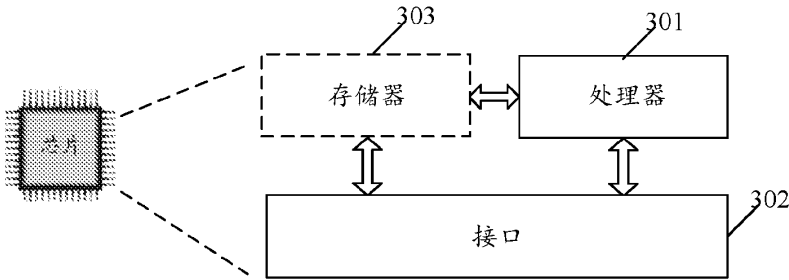


图 1C

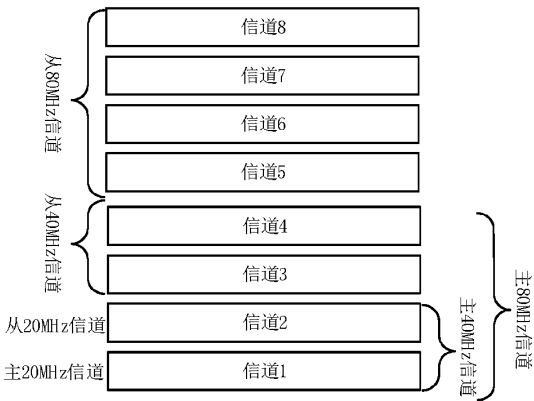


图 2

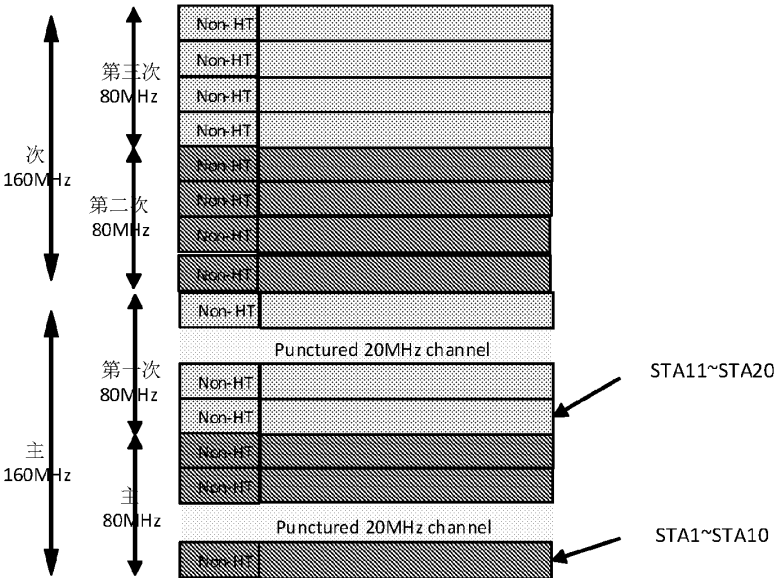


图 3

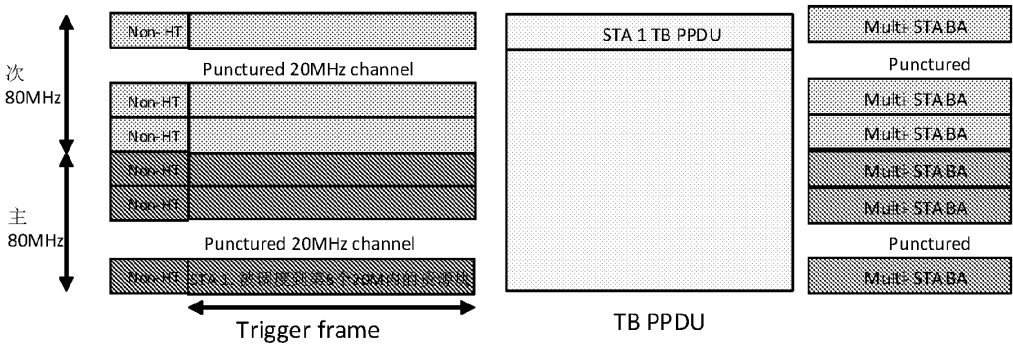


图 4

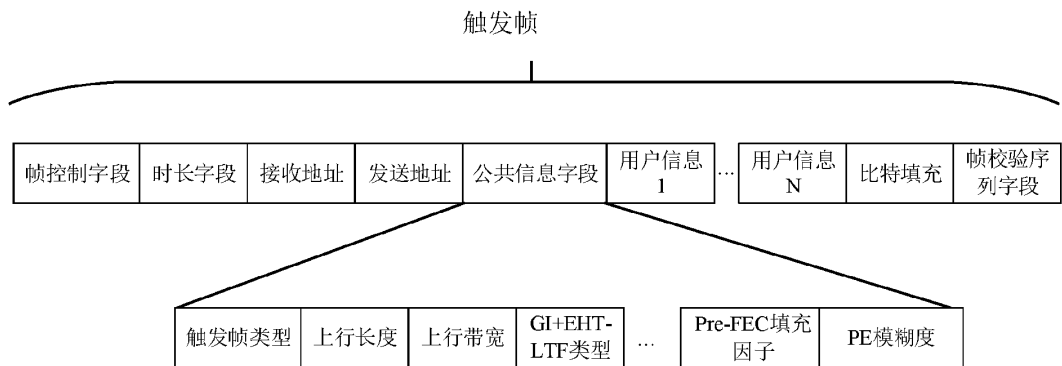


图 5

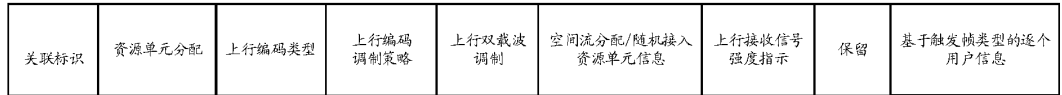


图 6

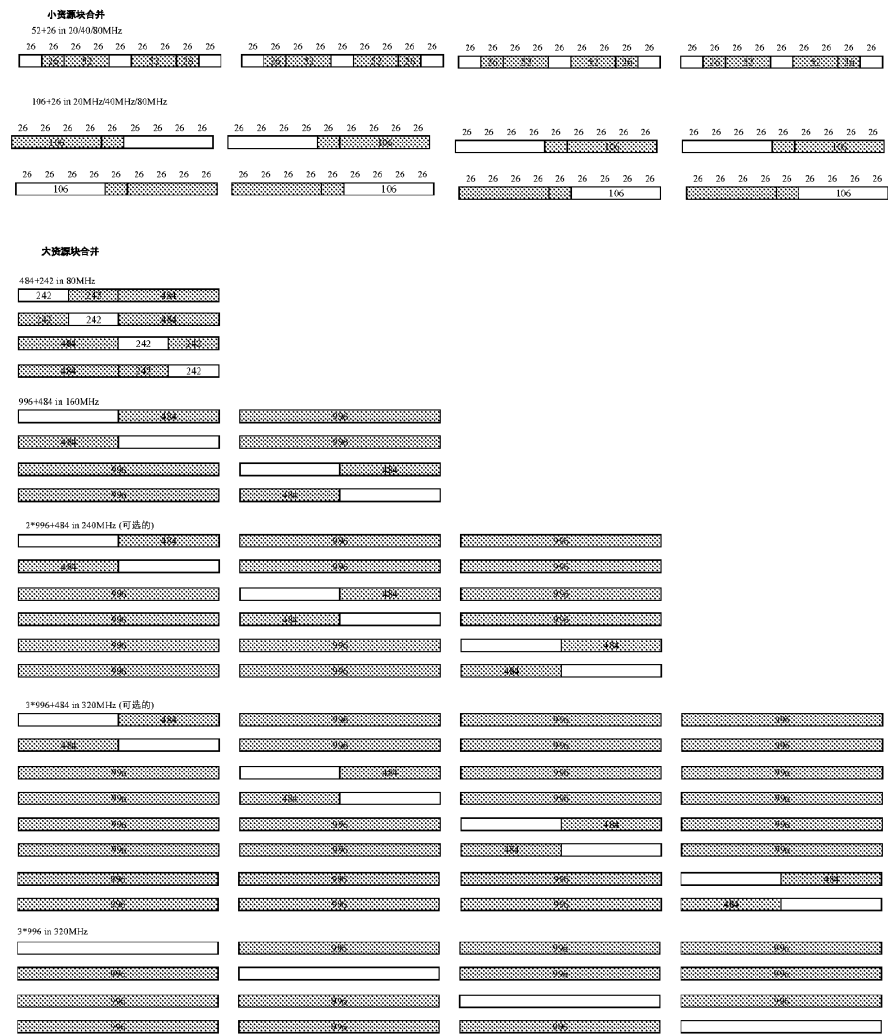


图 7a

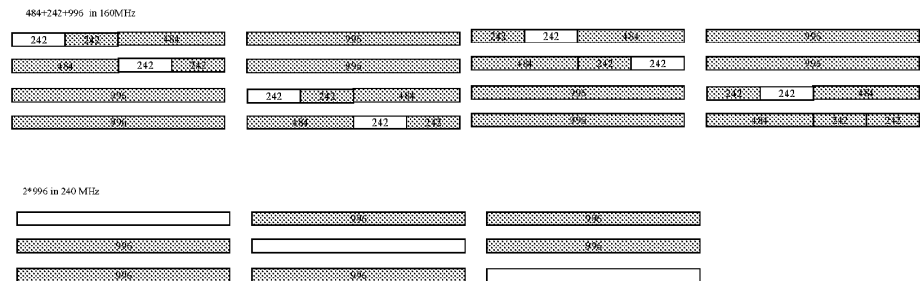


图 7b

次 80MHz	L-STF	L-LTF	L-SIG	RL-SIG	U-SIG1	EHT-STF#1	EHT-LTF#1	Data #1
	L-STF	L-LTF	L-SIG	RL-SIG	U-SIG1			
	L-STF	L-LTF	L-SIG	RL-SIG	U-SIG1			
	L-STF	L-LTF	L-SIG	RL-SIG	U-SIG1			
主 80MHz	L-STF	L-LTF	L-SIG	RL-SIG	U-SIG2	EHT-STF#2	EHT-LTF#2	Data #2
	L-STF	L-LTF	L-SIG	RL-SIG	U-SIG2			
	L-STF	L-LTF	L-SIG	RL-SIG	U-SIG2			
	L-STF	L-LTF	L-SIG	RL-SIG	U-SIG2			

图 8

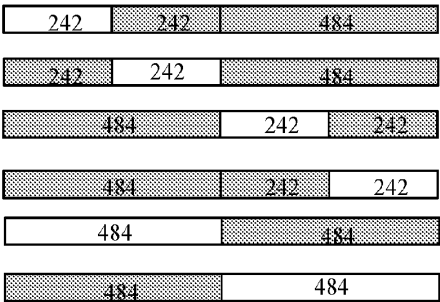


图 9

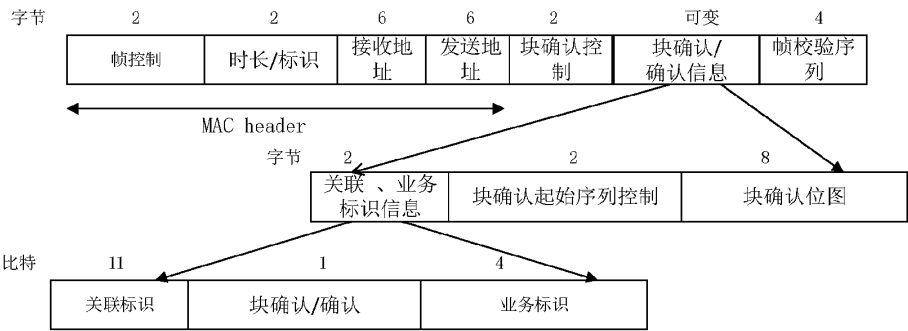


图 10