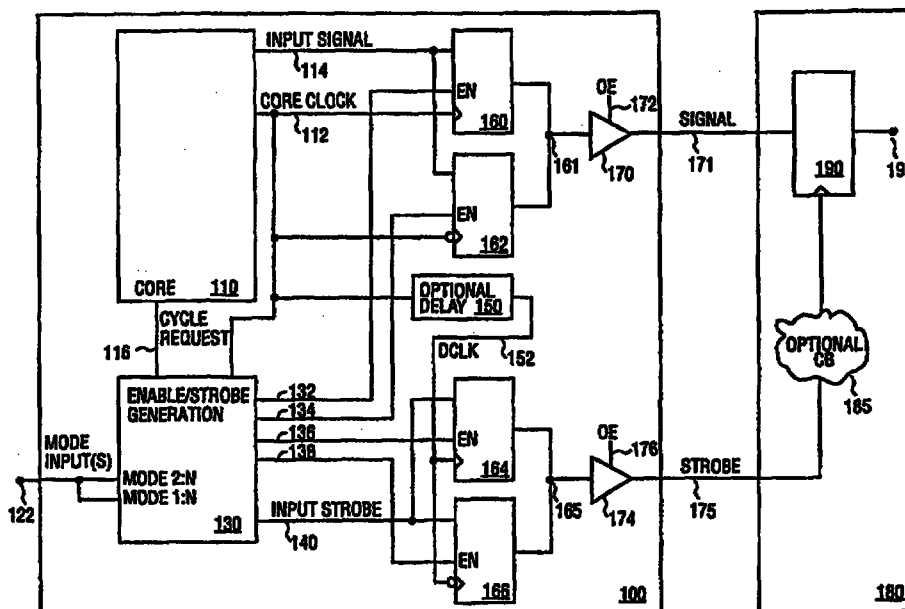




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(54) Title: **A METHOD AND APPARATUS FOR SOURCE SYNCHRONOUS TRANSFERS AT FREQUENCIES INCLUDING AN ODD FRACTION OF A CORE FREQUENCY**



(57) Abstract

A disclosed apparatus includes a signal driver circuit and a strobe signal driver circuit. The signal driver circuit is coupled to generate a cycle for a first signal at a first frequency from a core signal from a core operating at a core clock frequency that is an odd fractional multiple of the first frequency. The strobe signal driver circuit is coupled to generate a strobe signal at an intermediate point of the cycle to allow latching of the first signal triggered by the strobe signal.