

A METHOD AND SYSTEM
FOR FAULT ISOLATION FOR PCI BUS ERRORS

ABSTRACT

Method and system aspects for fault isolation on a bus are provided. In a method aspect, a method for isolating a fault condition on a bus of a computer system, the computer system including an input/output (I/O) subsystem formed by a plurality of I/O devices communicating via the bus, includes categorizing, in a recursive manner, the I/O subsystem, and isolating a source of an error condition within the I/O subsystem. Further, the I/O subsystem communicates via a peripheral component interconnect, PCI, bus. In a system aspect, a computer system for isolating a fault condition on a PCI bus includes a processing mechanism and an input/output mechanism (18), coupled to the processing mechanism (10), comprising a plurality of input/output devices (20) and bridges (22) coupled to a PCI bus (21) and communicating according to a PCI standard. In addition, the system includes a fault isolation mechanism within the processing mechanism (10).

Figure 1