## ABSTRACT OF THE DISCLOSURE

A burst control circuit for use in a TDMA communications system comprises a power amplifier having two separate control terminals. The circuits also comprises an ALC circuit for producing and supplying a level control signal to the first control terminal of the power amplifier to perform negative feedback operation and to keep the level of an output burst signal constant. The burst control circuit further comprises a waveform control circuit which generates in response to a burst timing signal a waveform control signal having gently sloped leading and trailing edges and supplies the waveform control signal to the second control terminal of the power amplifier to control the waveform of the output burst signal.

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