

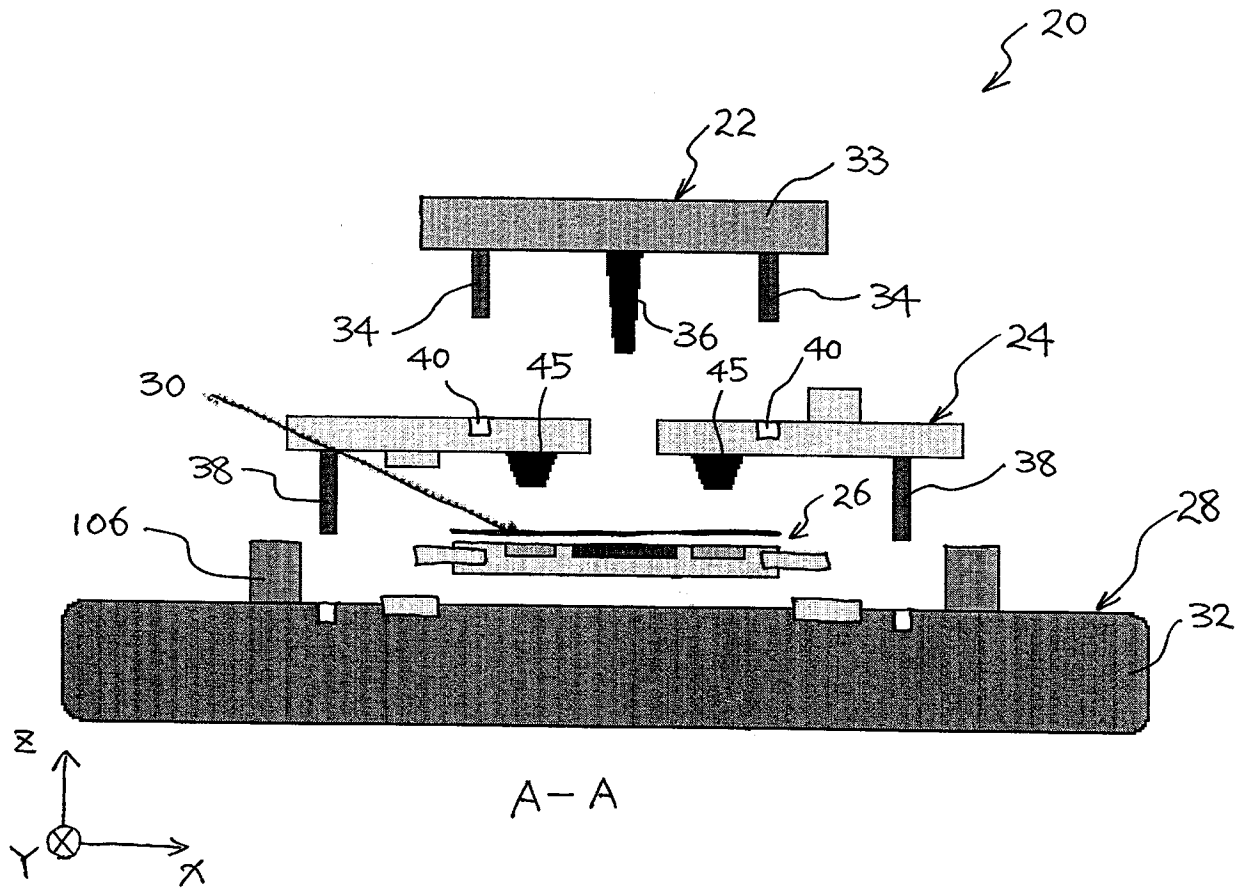


Fig. 1

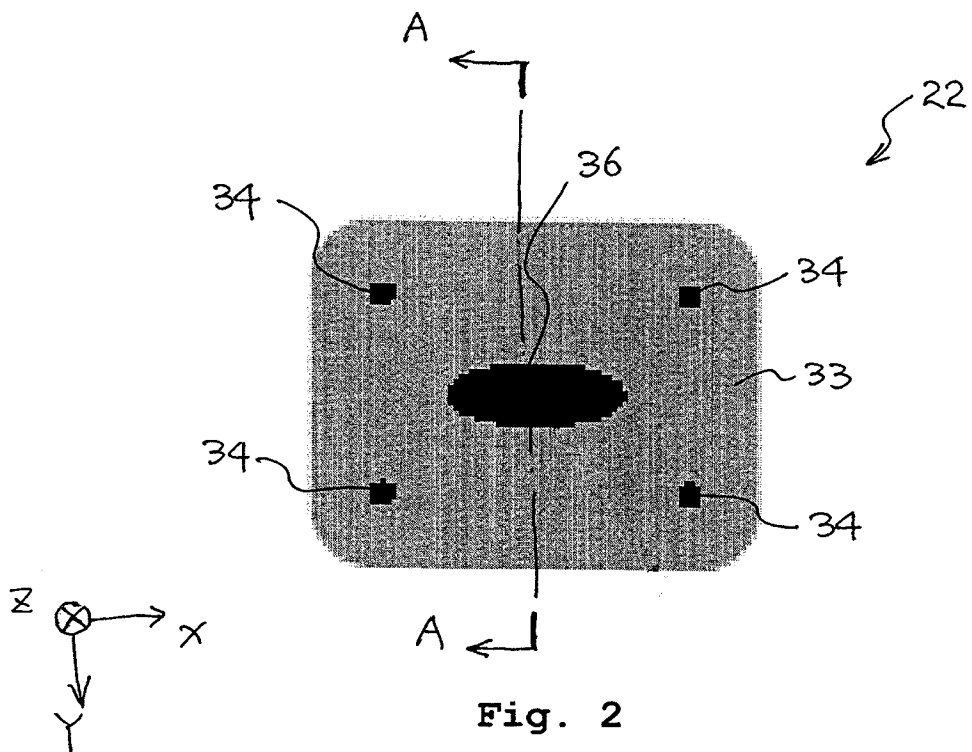


Fig. 2

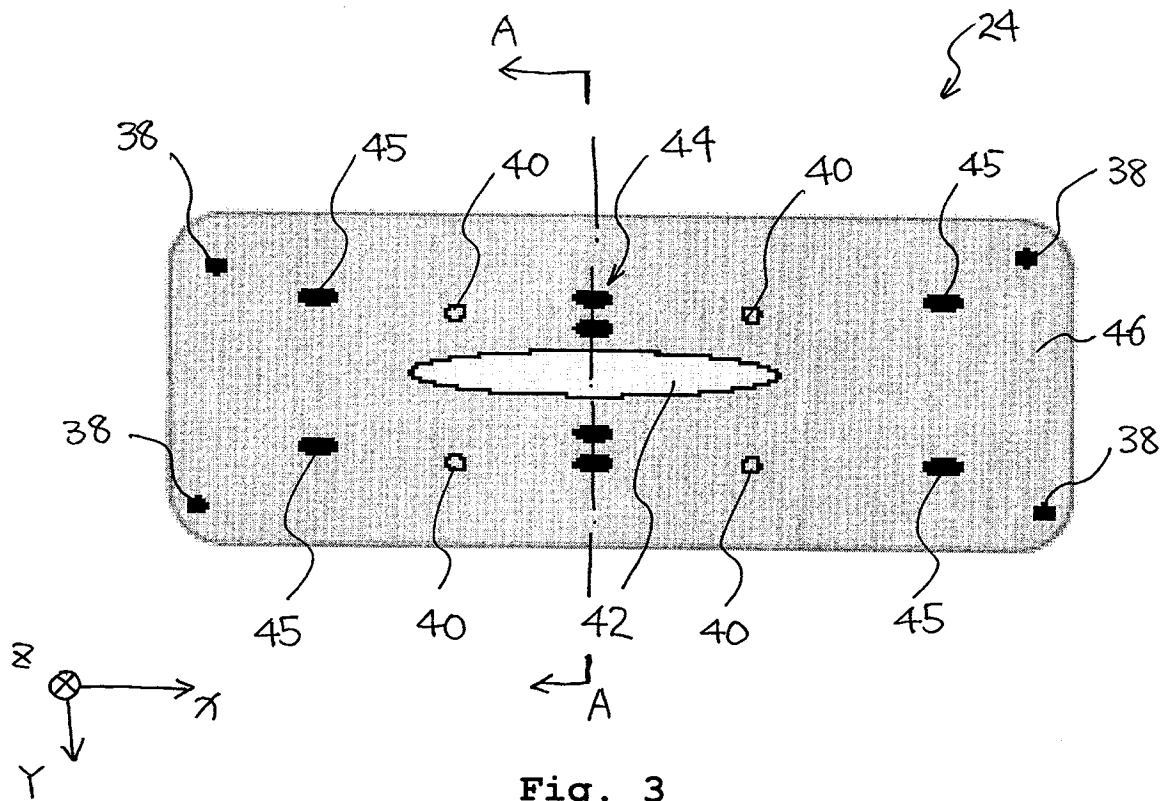


Fig. 3

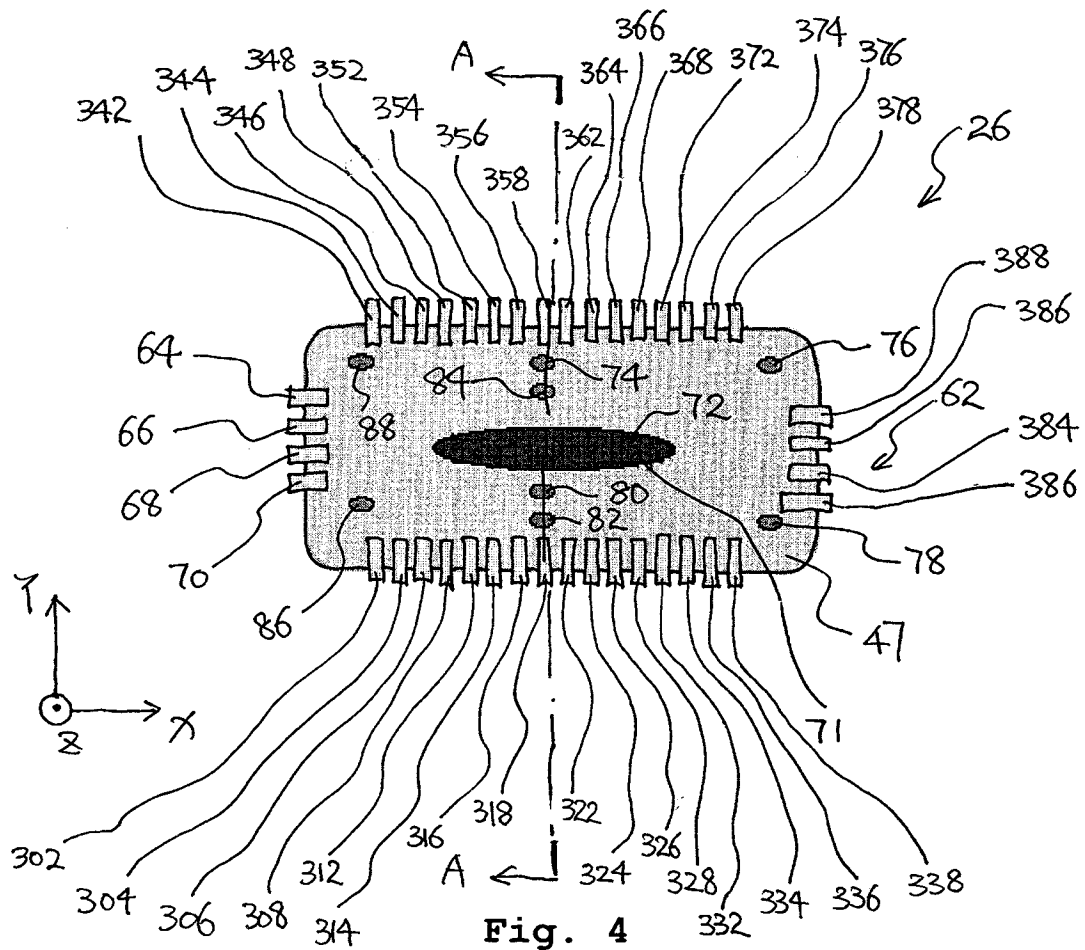


Fig. 4

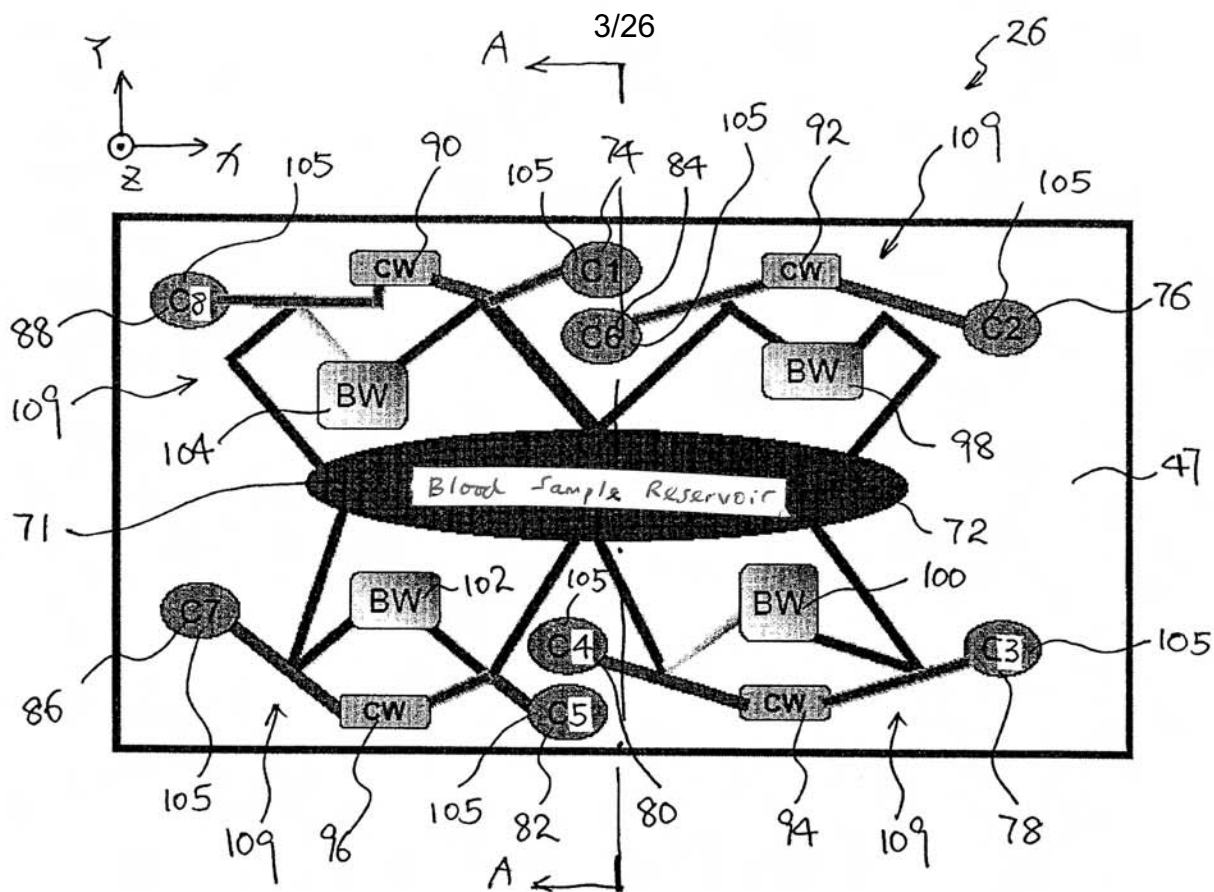


Fig. 5

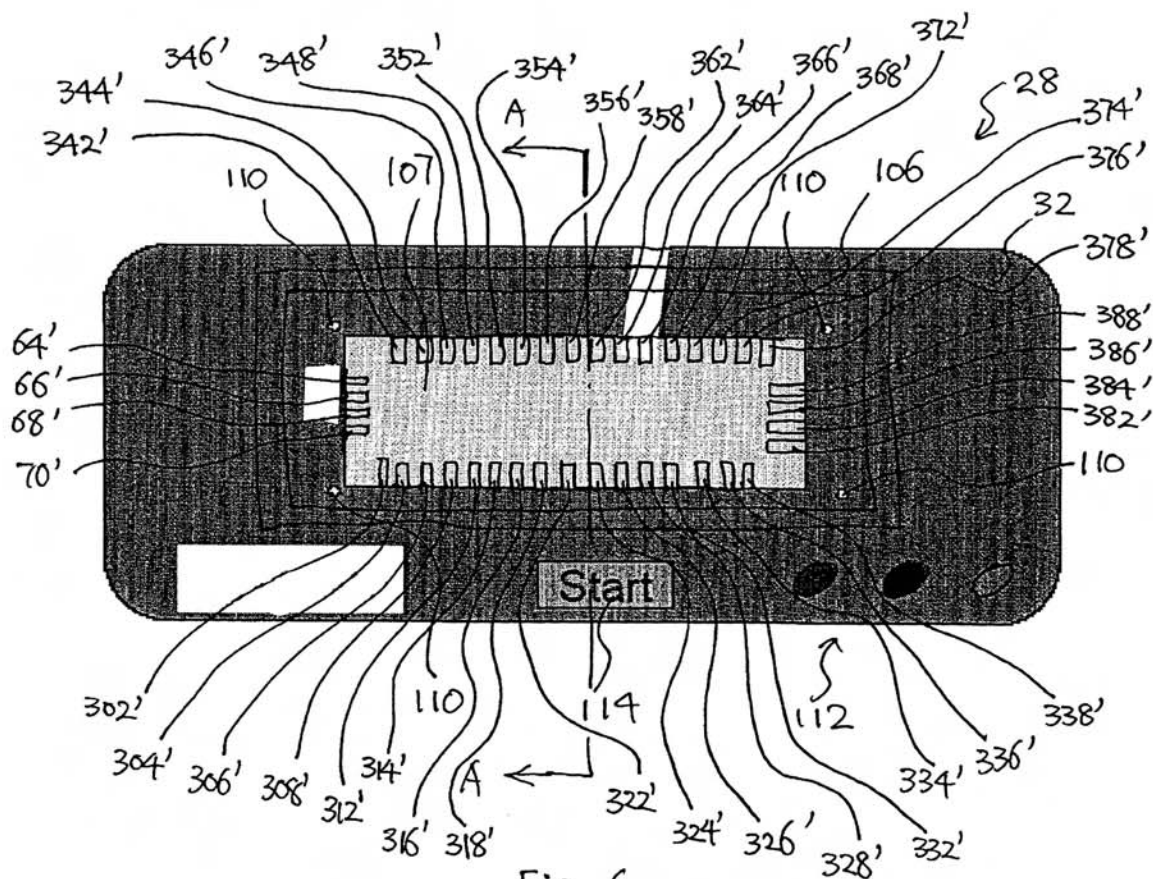


Fig. 6

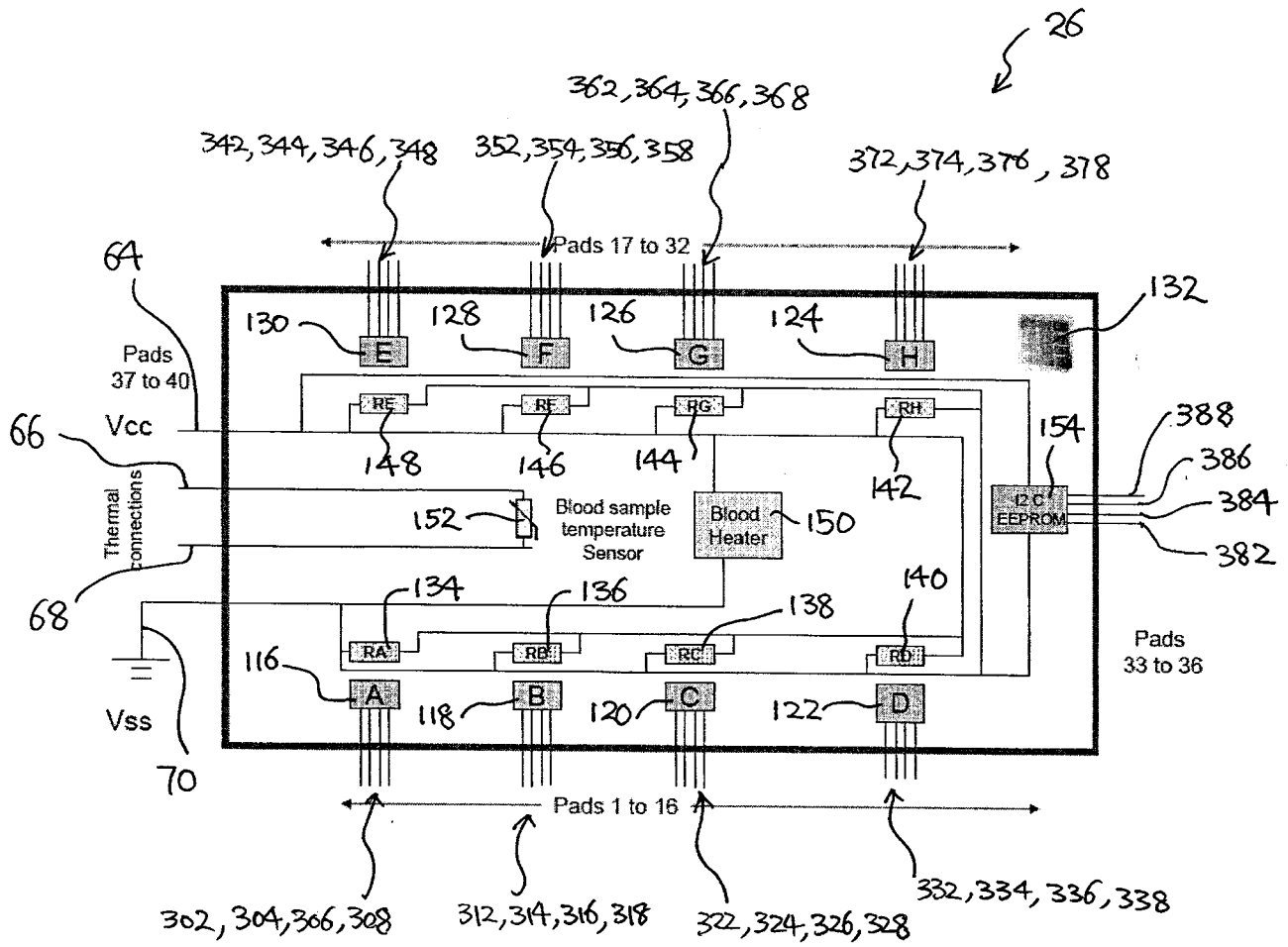


Fig. 7

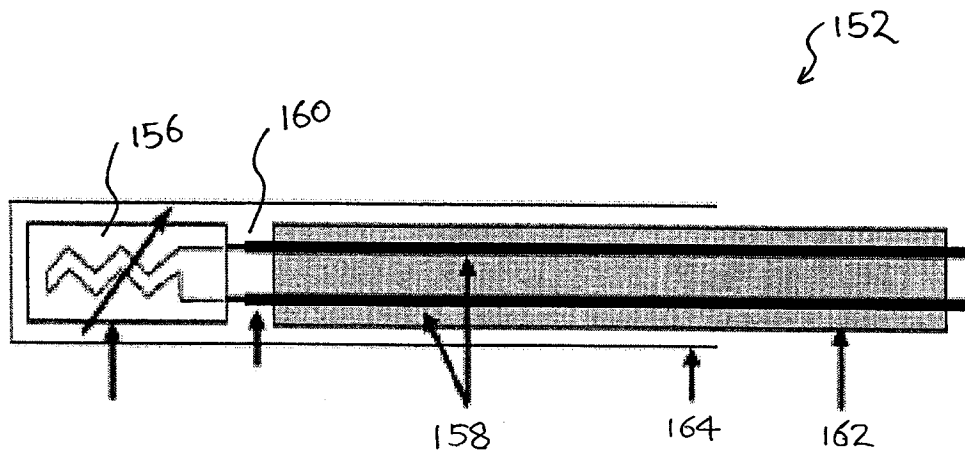


Fig. 8

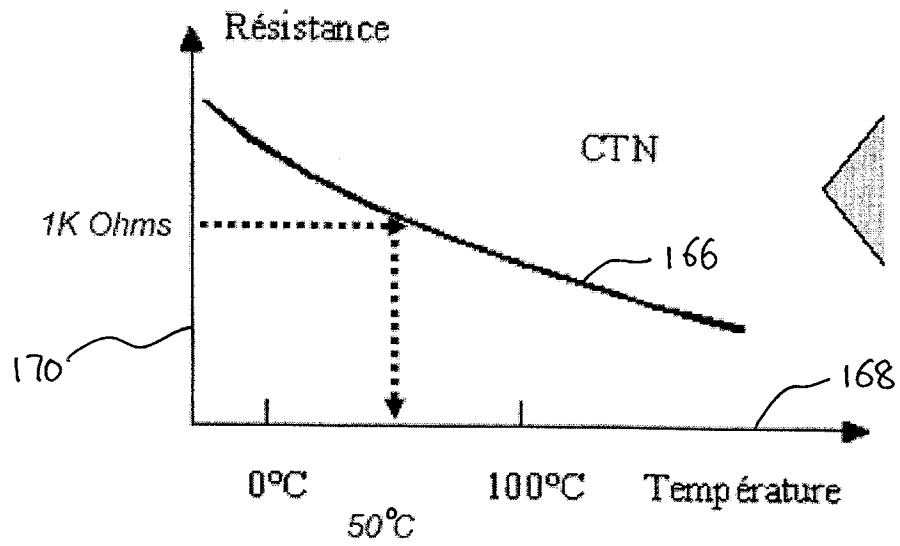


Fig. 9

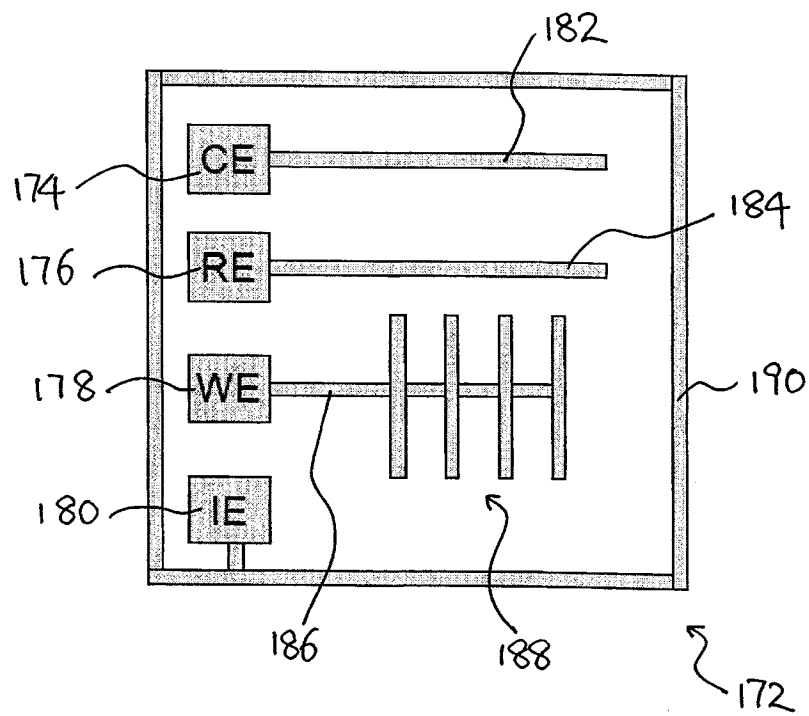


Fig. 10

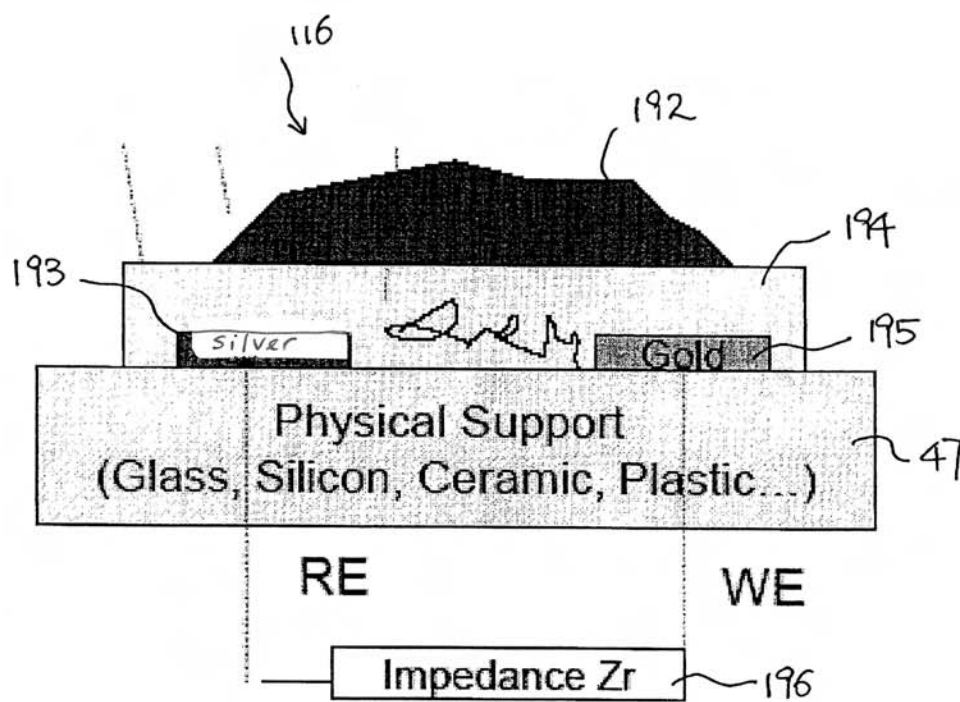


Fig. 11

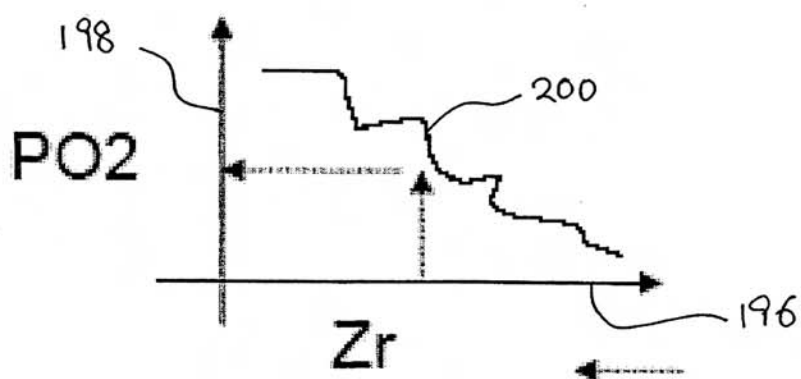


Fig. 12

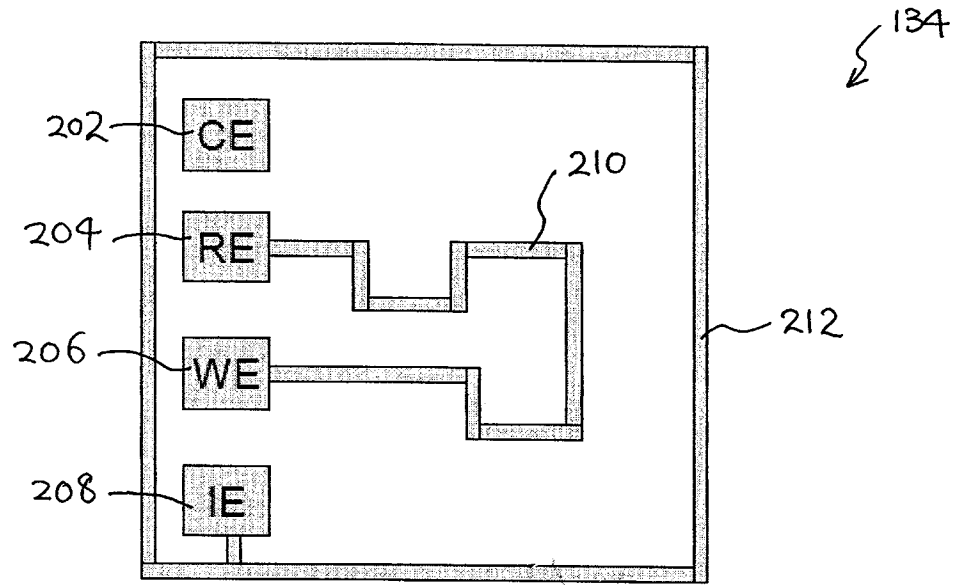


Fig. 13

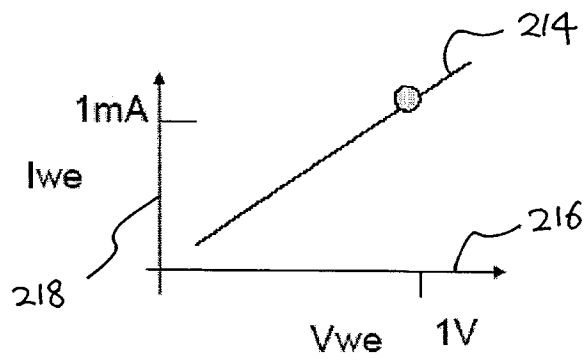


Fig. 14

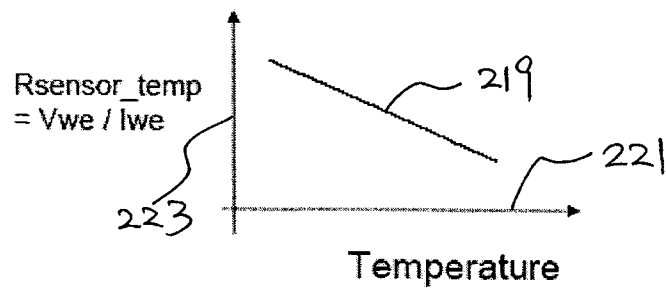


Fig. 15

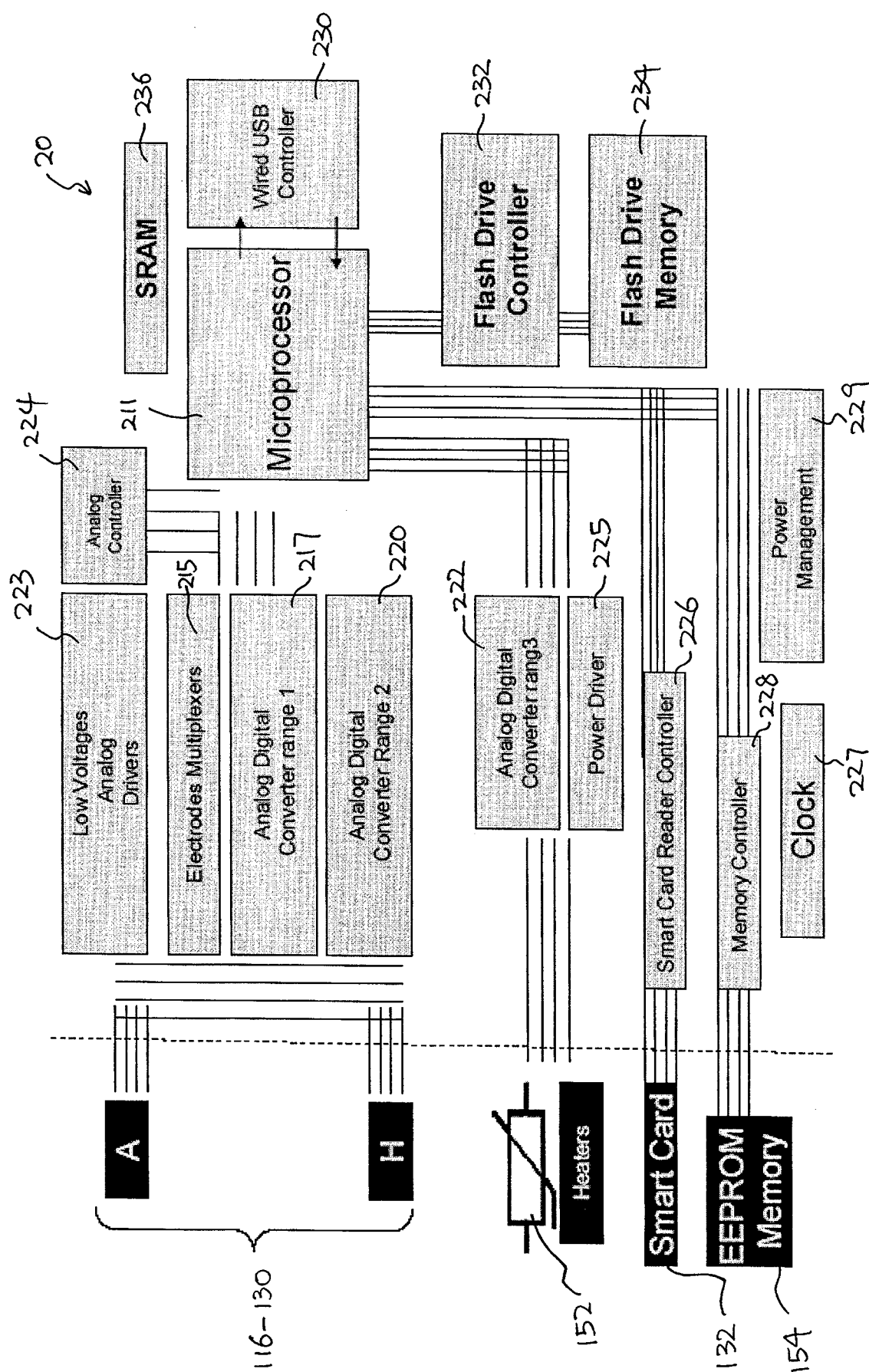


Fig. 16

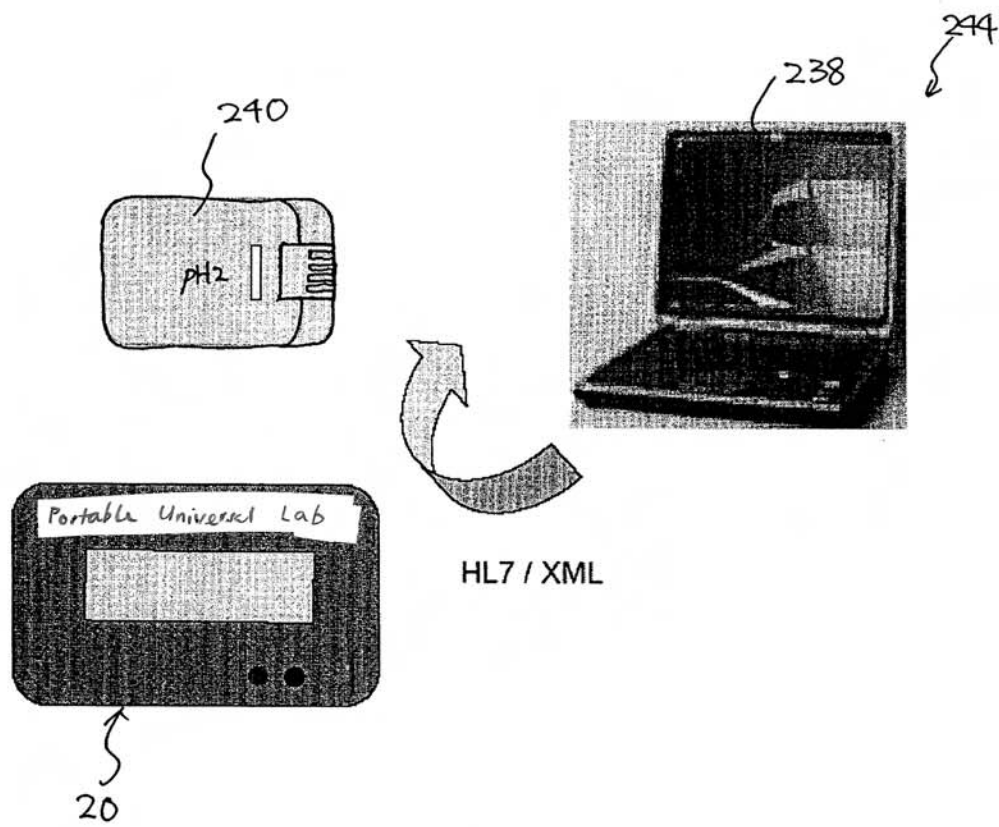


Fig. 18

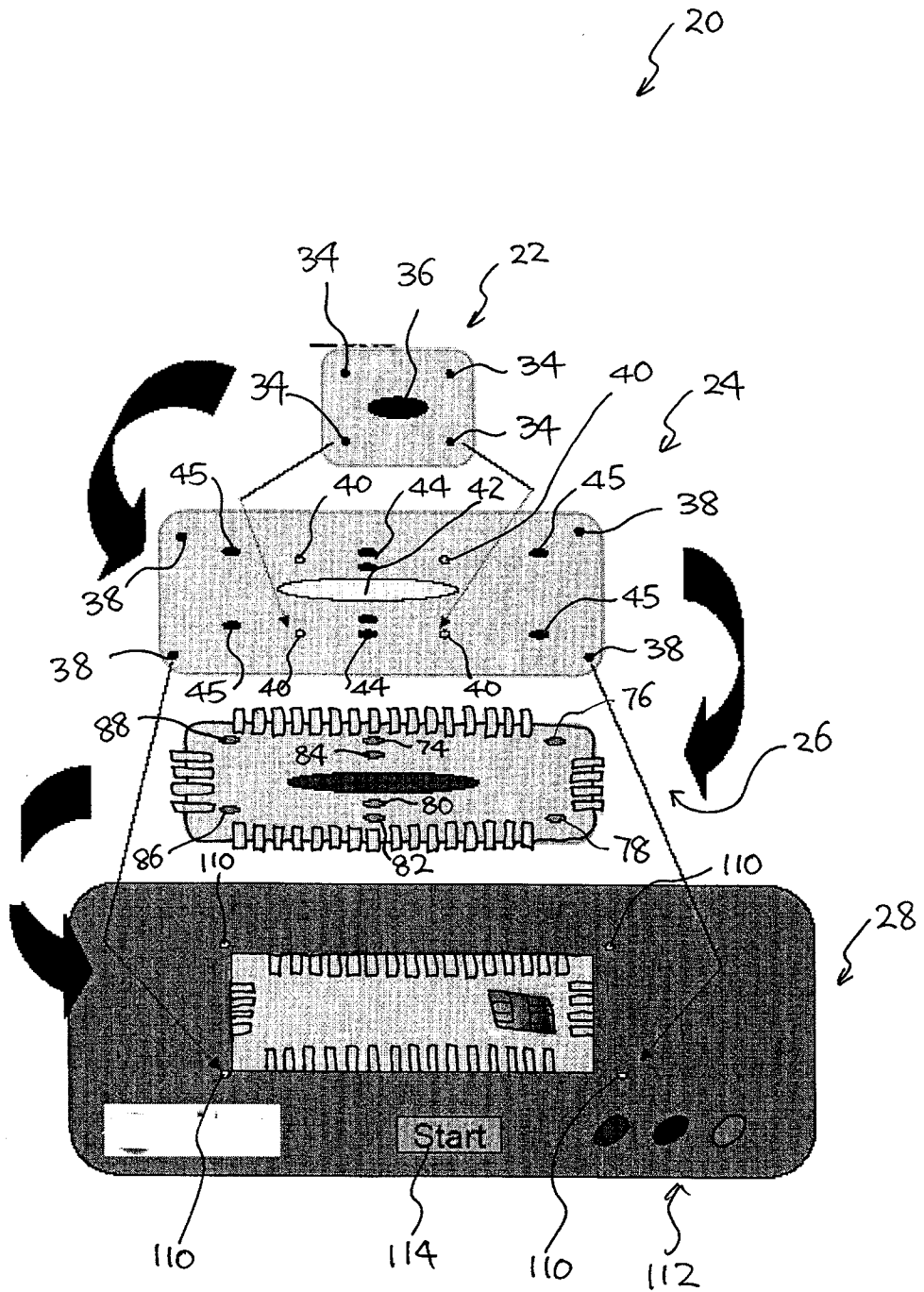


Fig. 19

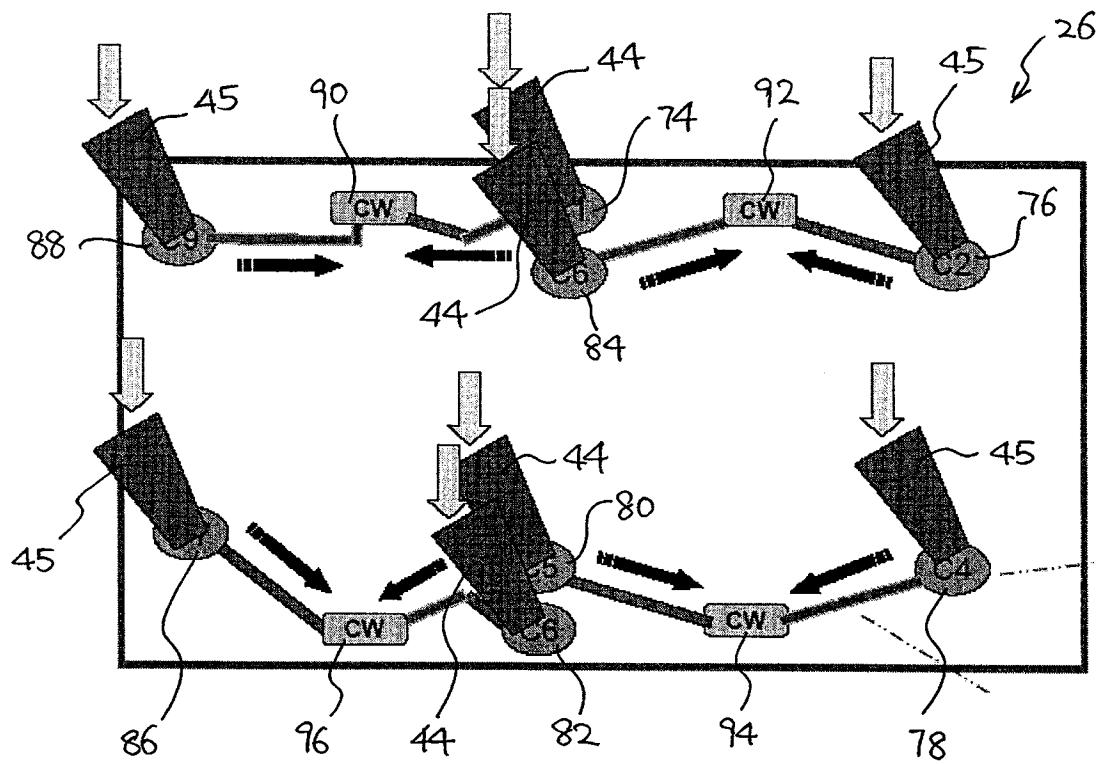


Fig. 20

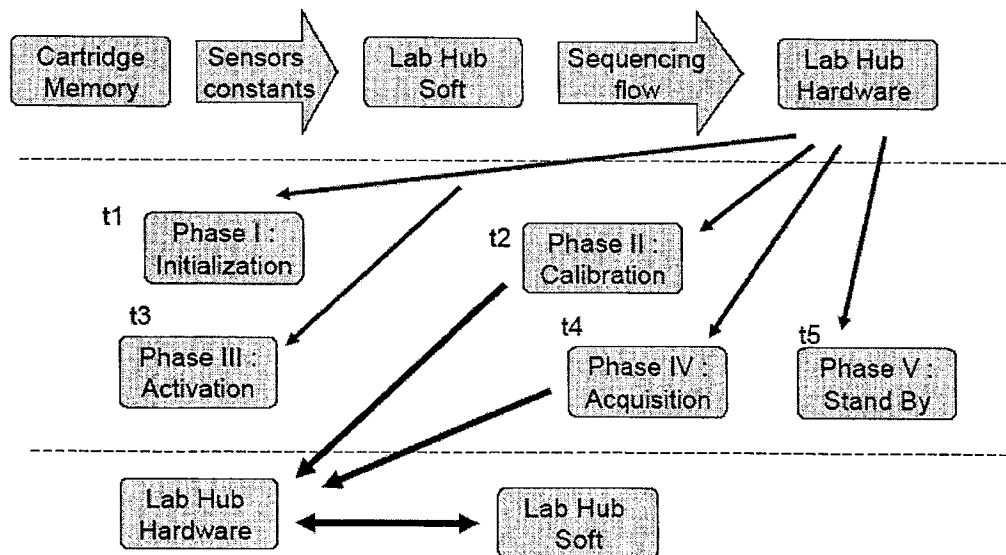


Fig. 21

$t_1 = 0s$

Phase I :
Initialization

Code	Voltage
CE	GND
RE	GND
WE	GND
IE	GND

Fig. 22

 $t_2 = t_1 + 2s$

Phase II :
Calibration

	AC / duration 0.5s Measurment 1		AC / duration 0.5s Measurment 2	
	Input	Output	Input	Output
CE	GND	0 A	GND	0 A
RE	GND	none	GND	0 A
WE	1 V	Iwe 1	2 V	Iwe 2
IE	GND	0 A	GND	0 A

Fig. 23

t3 t1= Xs

Phase III :
Activation

Code	Voltage
CE	GND
RE	GND
WE	1.5 V
IE	GND

Fig. 24

t4

Phase IV :
Acquisition

Code	Input	Output
CE	GND	none
RE	GND	GND
WE	1.5 V	I WE
IE	GND	none

Fig. 25

t5

Phase V :
Stand By

Code	Voltage
CE	GND
RE	GND
WE	GND
IE	GND

Fig. 26

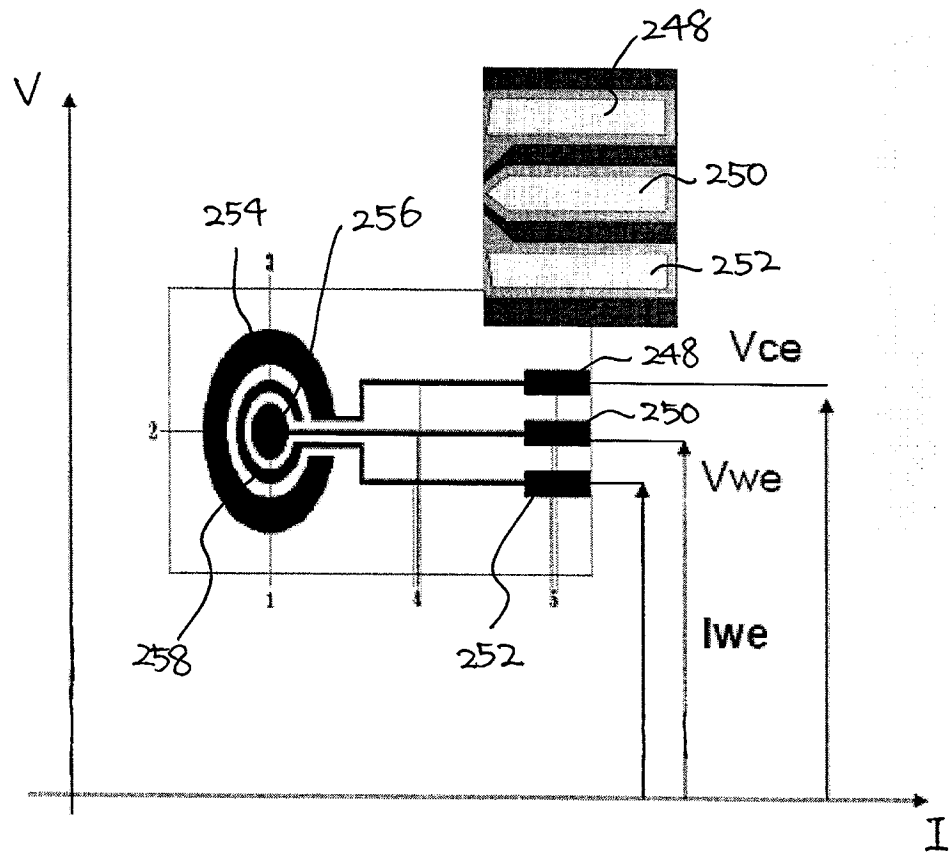


Fig. 27

$t_1 = 0s$

Phase I :
Initialization

Code	Voltage
CE	GND
RE	GND
WE	GND
IE	GND

Fig. 28

 $t_2 = t_1 + 2s$

Phase II :
Calibration

	AC / duration 20s Measurement 1		AC / duration 0.5s Measurement 2	
	Input	Output	Input	Output
CE	GND	0 A	GND	0 A
RE	GND	none	GND	0 A
WE	1 V	I _{we 1}	2 V	I _{we 2}
IE	GND	0 A	GND	0 A

Fig. 29

t3 t1= 20s

Phase III :
Activation

Code	Voltage
Time	20s
CE	GND
RE	GND
WE	1.5 V
IE	GND

Fig. 30

t4

Phase IV :
Acquisition

Code	Input	Output
CE	Ice	0.5V
RE	Ire	0.5V
WE	1.0 V	I WE
IE	GND	none

Fig. 31

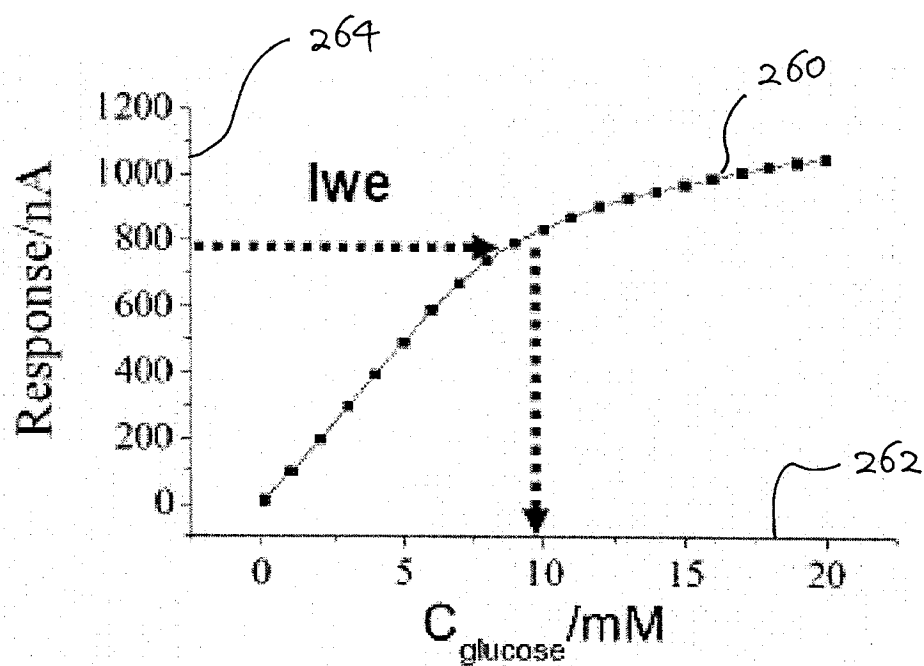


Fig. 32

t5

Phase V :
Stand By

Code	Voltage
CE	GND
RE	GND
WE	GND
IE	GND

Fig. 33

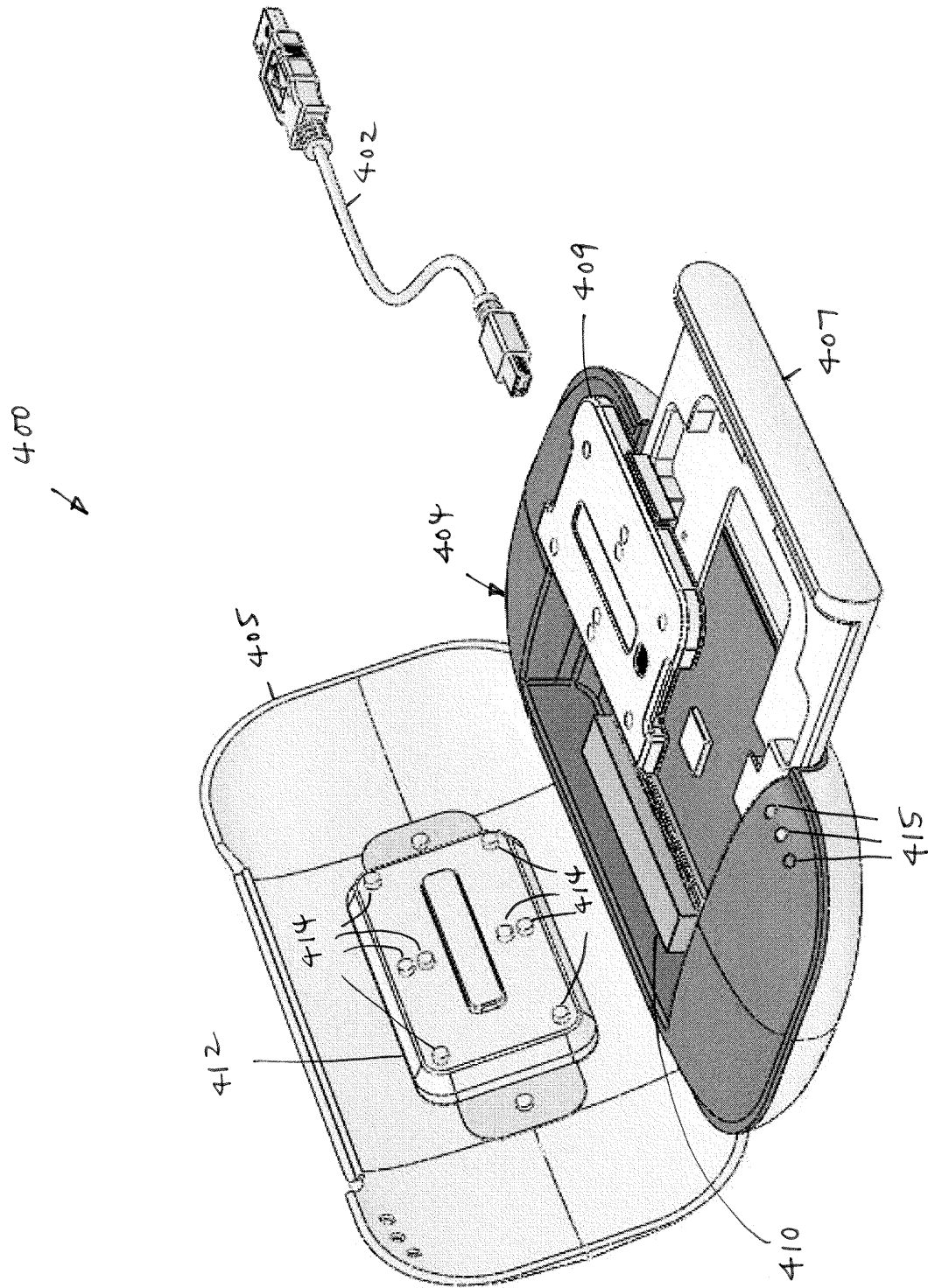


Fig. 34

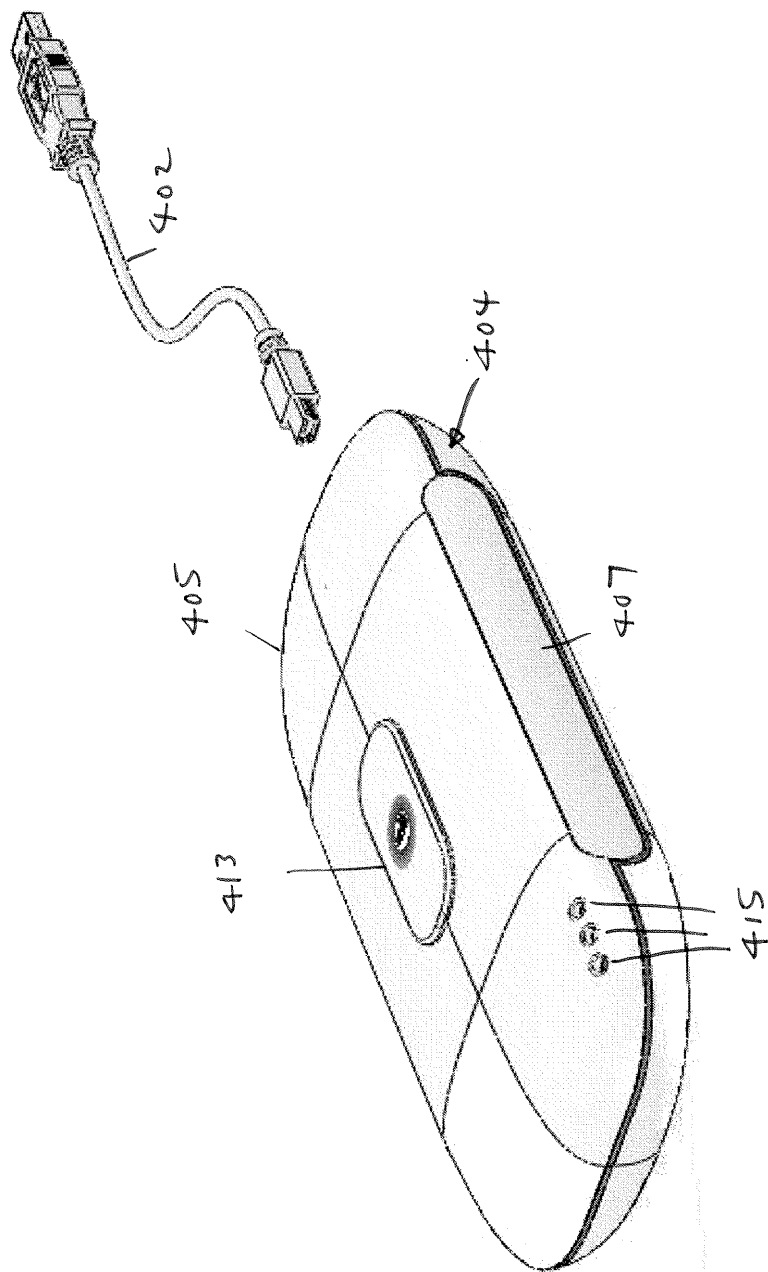


Fig. 35

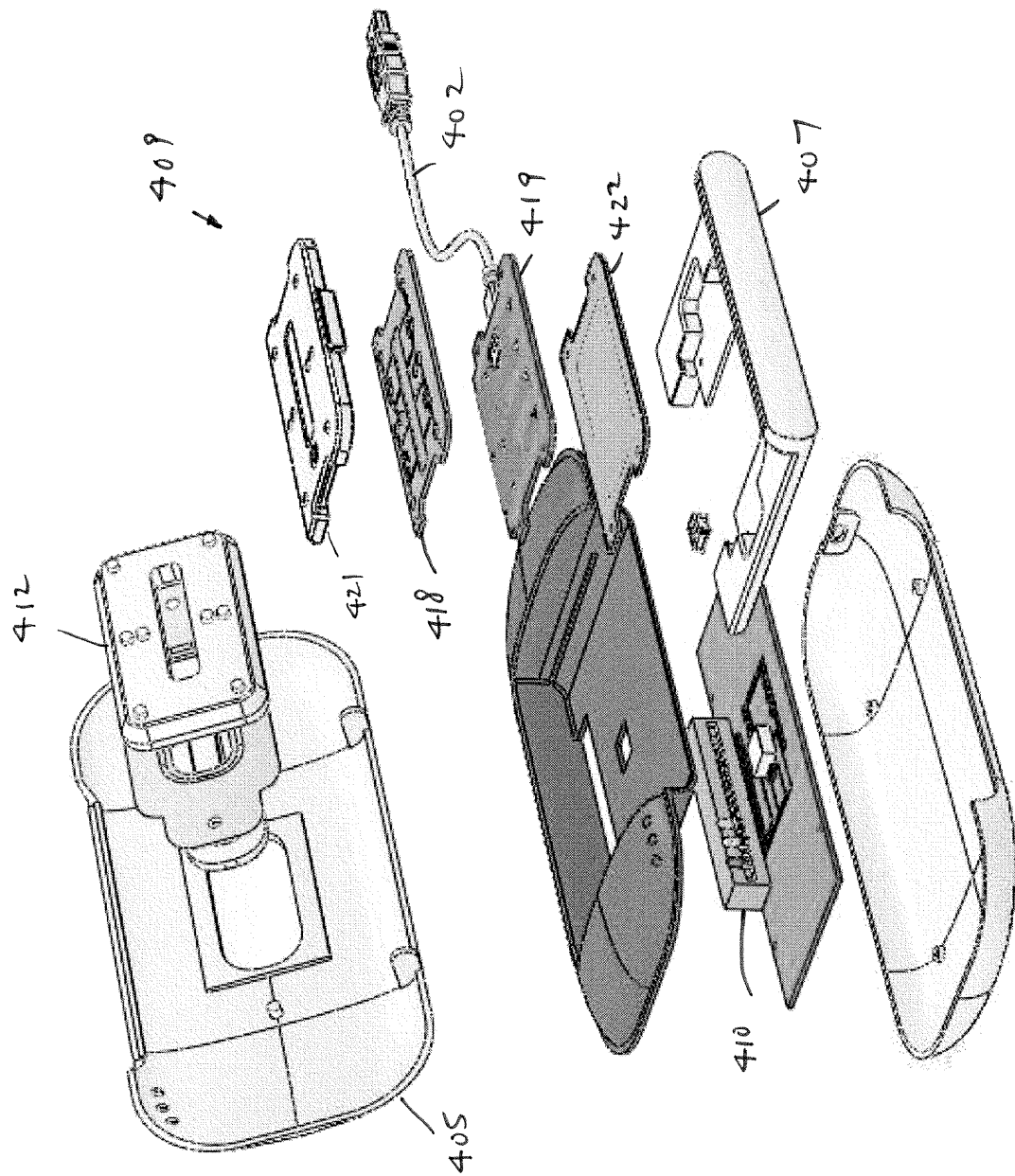


Fig. 36

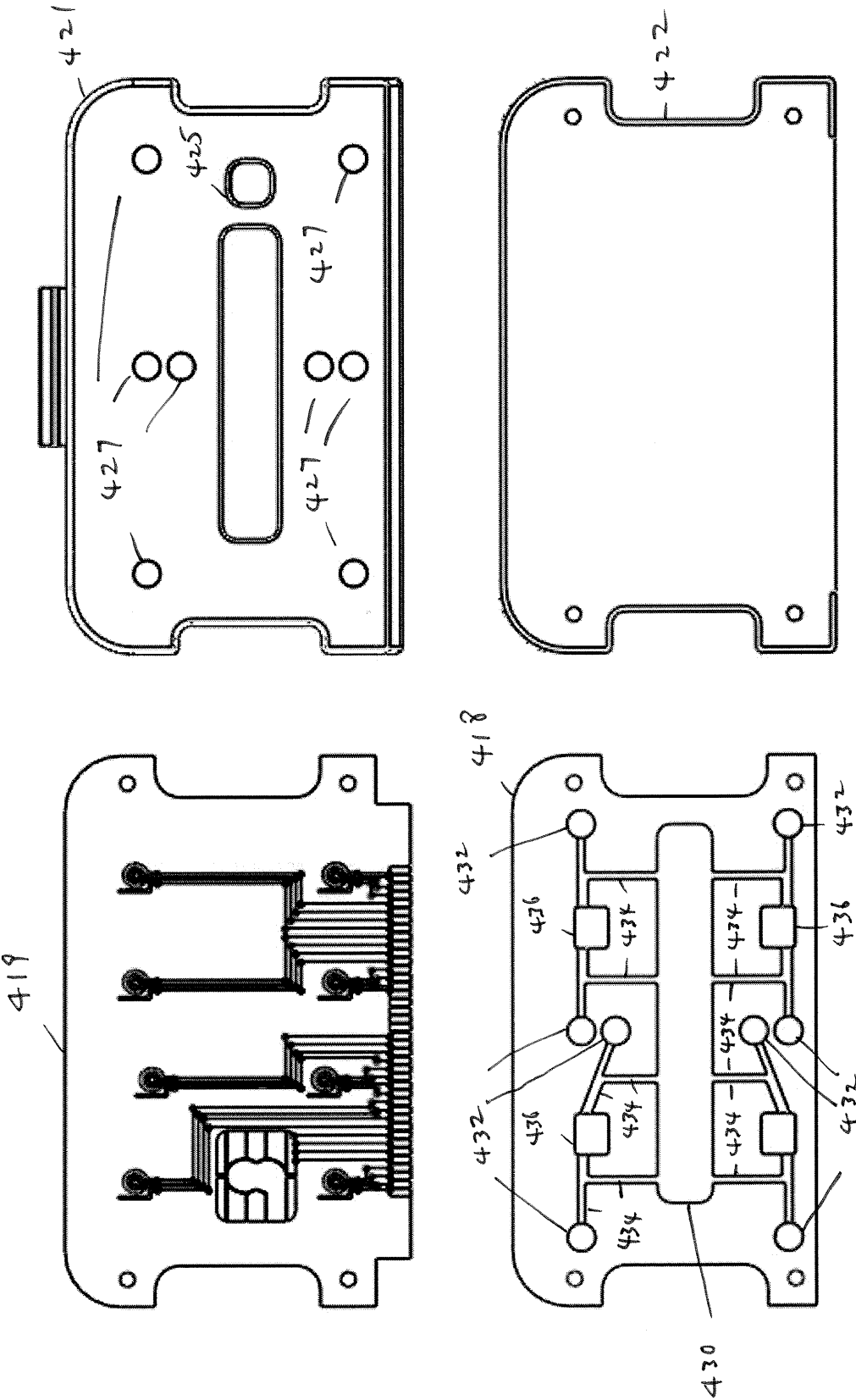


Fig. 37

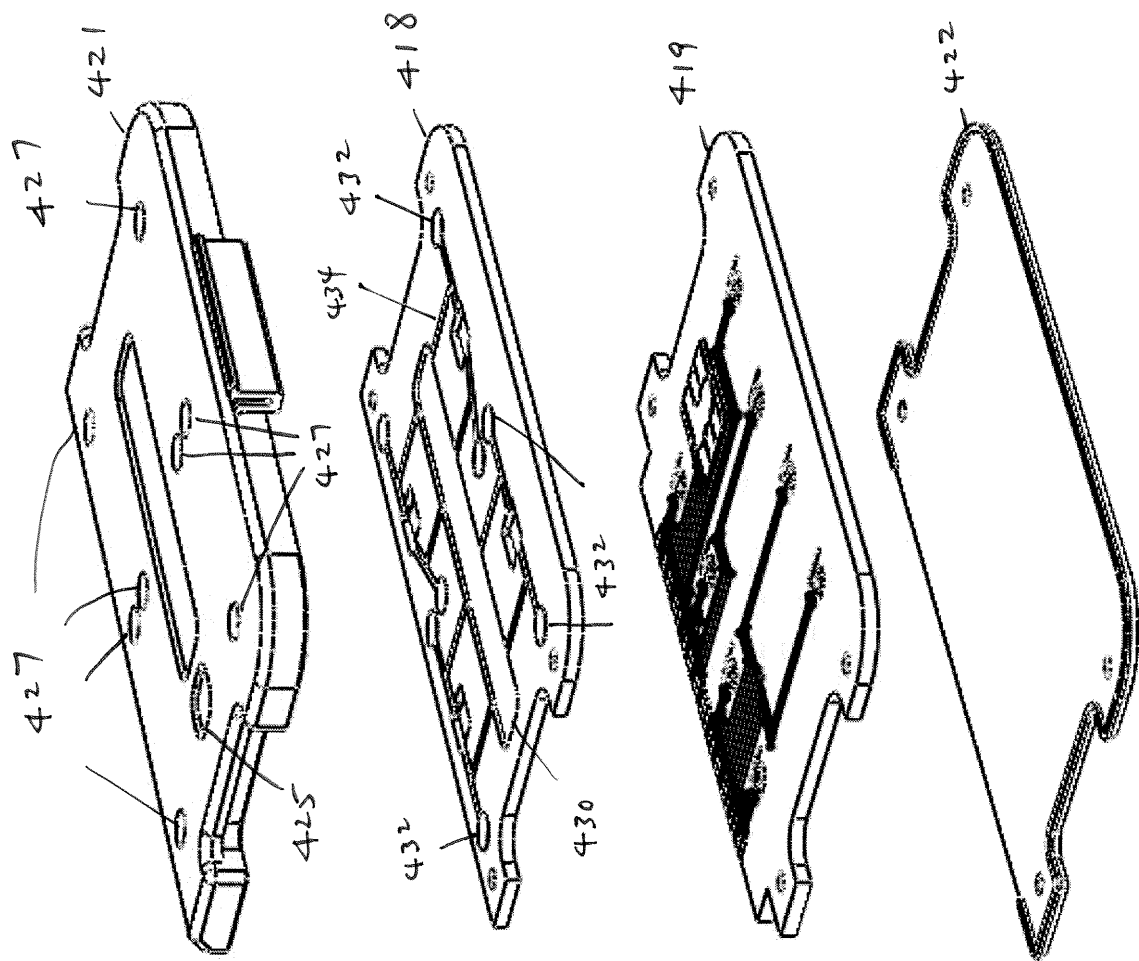


Fig. 38

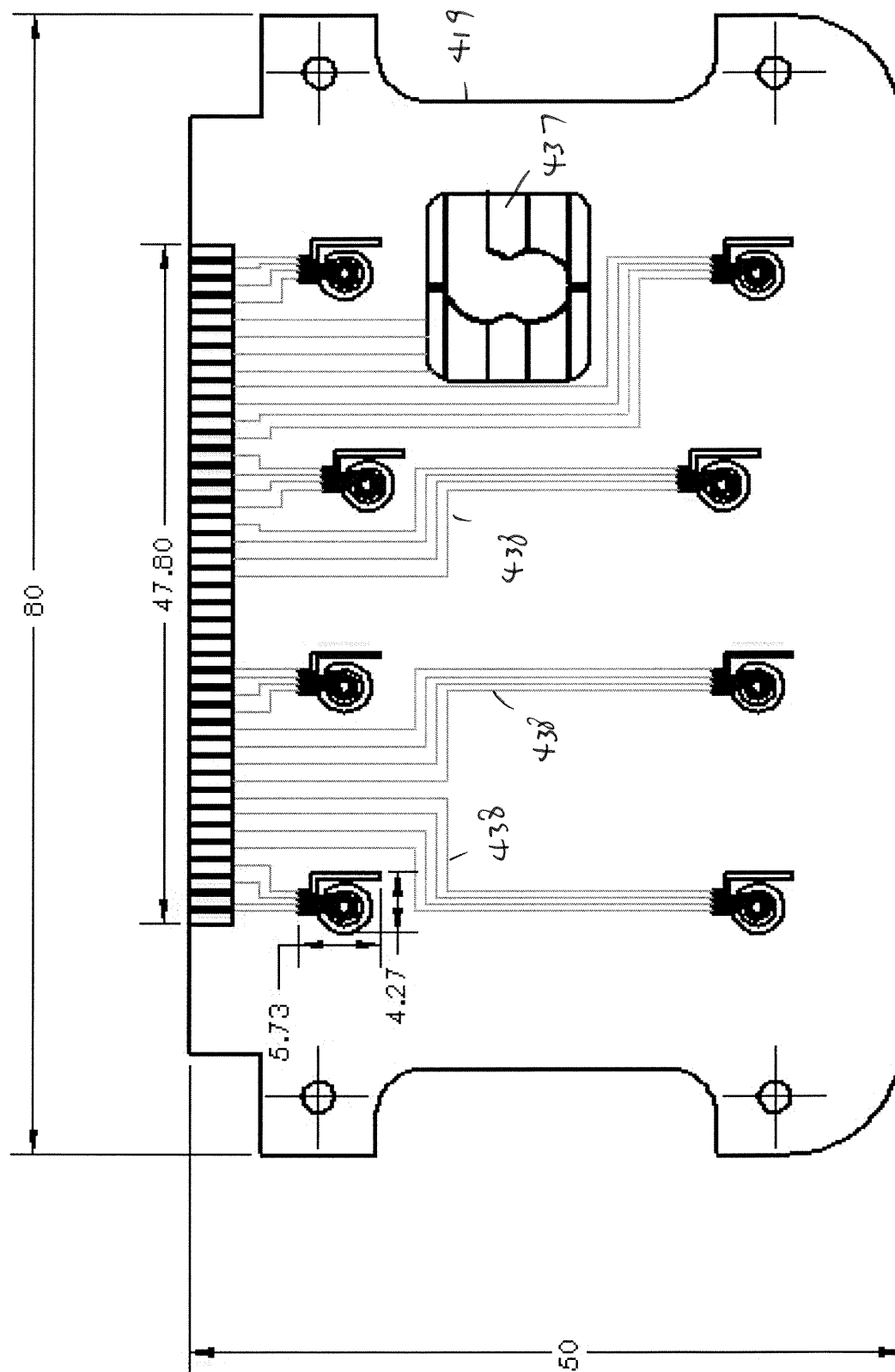


Fig. 39

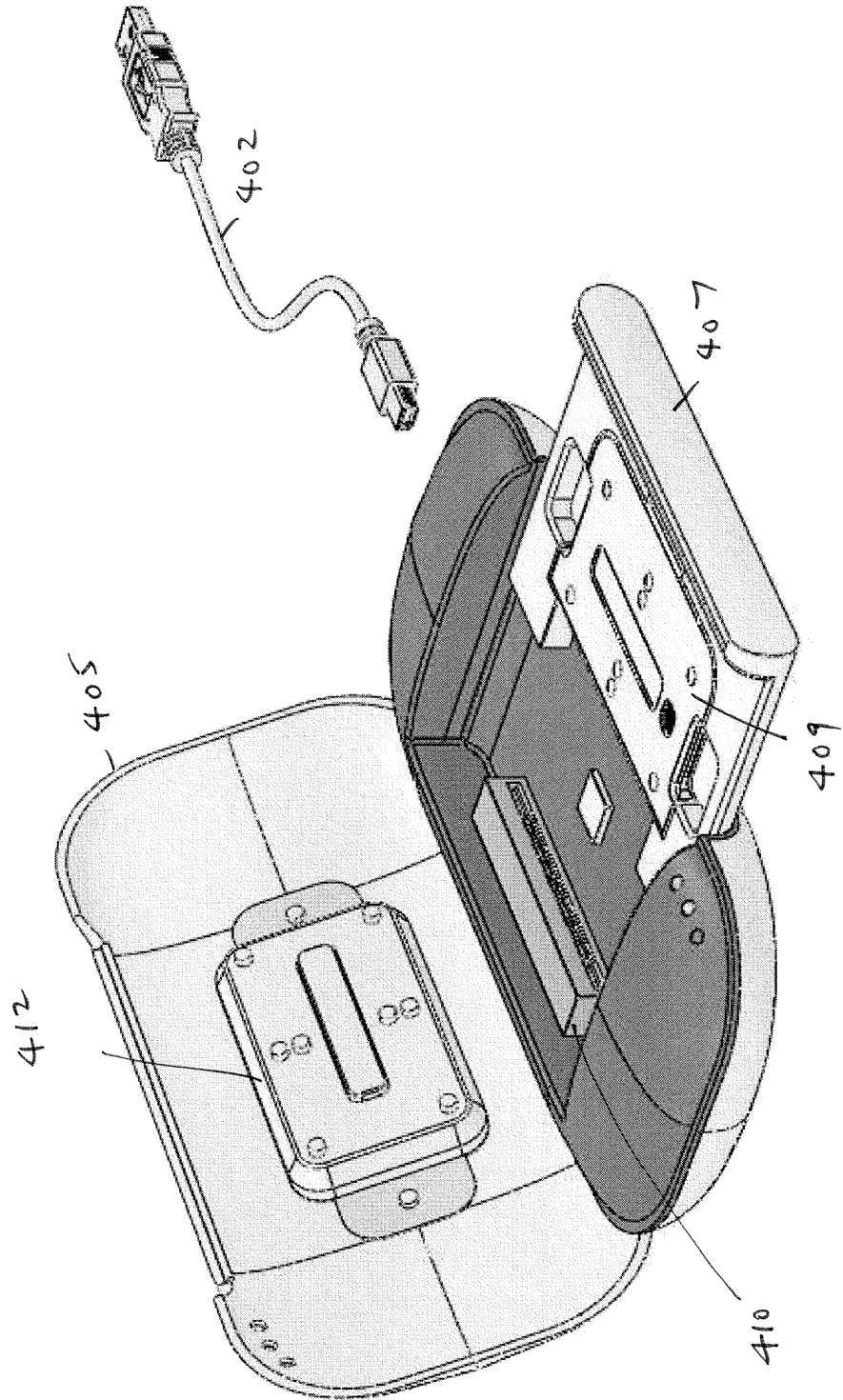


Fig. 40

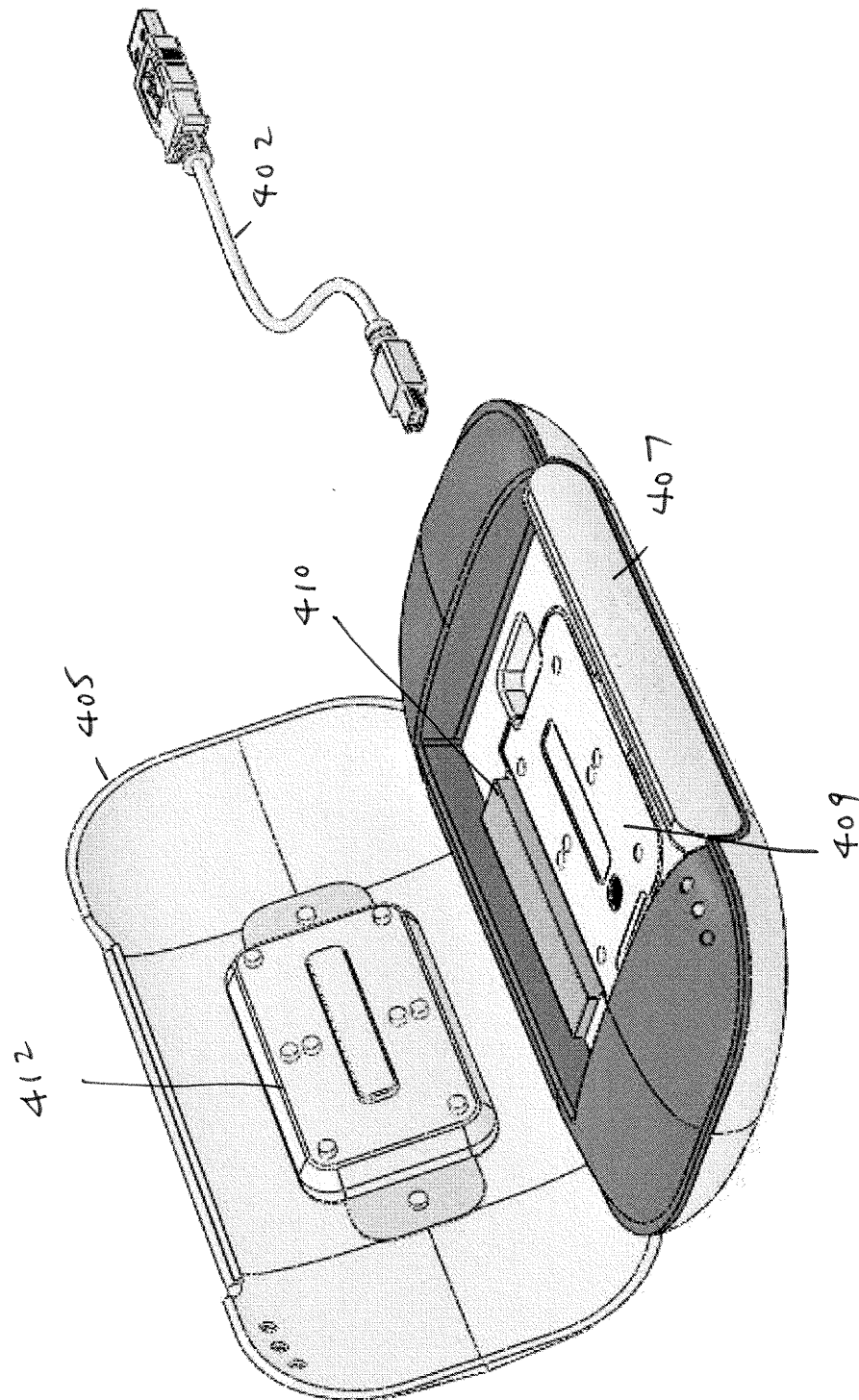


Fig. 41