INTEGRATED CIRCUIT FOR CONTROLLING A REMOTELY LOCATED MASS STORAGE PERIPHERAL DEVICE

ABSTRACT OF THE DISCLOSURE

An integrated circuit for use in a computer system having a host computer and at least one mass storage peripheral device has a controller circuit for the mass storage peripheral device for receiving information from the device and an interface circuit to interface the information from the device to the host computer via a bus mastering bus, such as a PCI bus, a 1394 bus, or the like, in the host computer. The integrated circuit is adapted to be located within the host computer and to interface to the peripheral device. The integrated circuit may include a portion of a read channel circuit, which is connected to receive information from the peripheral device. The integrated circuit may also contain other circuitry for the control and operation of the peripheral device, such as a digital signal processor, a buffer manager, a speed matching buffer, servo logic to control servo circuits to spin a motor of the peripheral device, or the like.

FIGURE 1