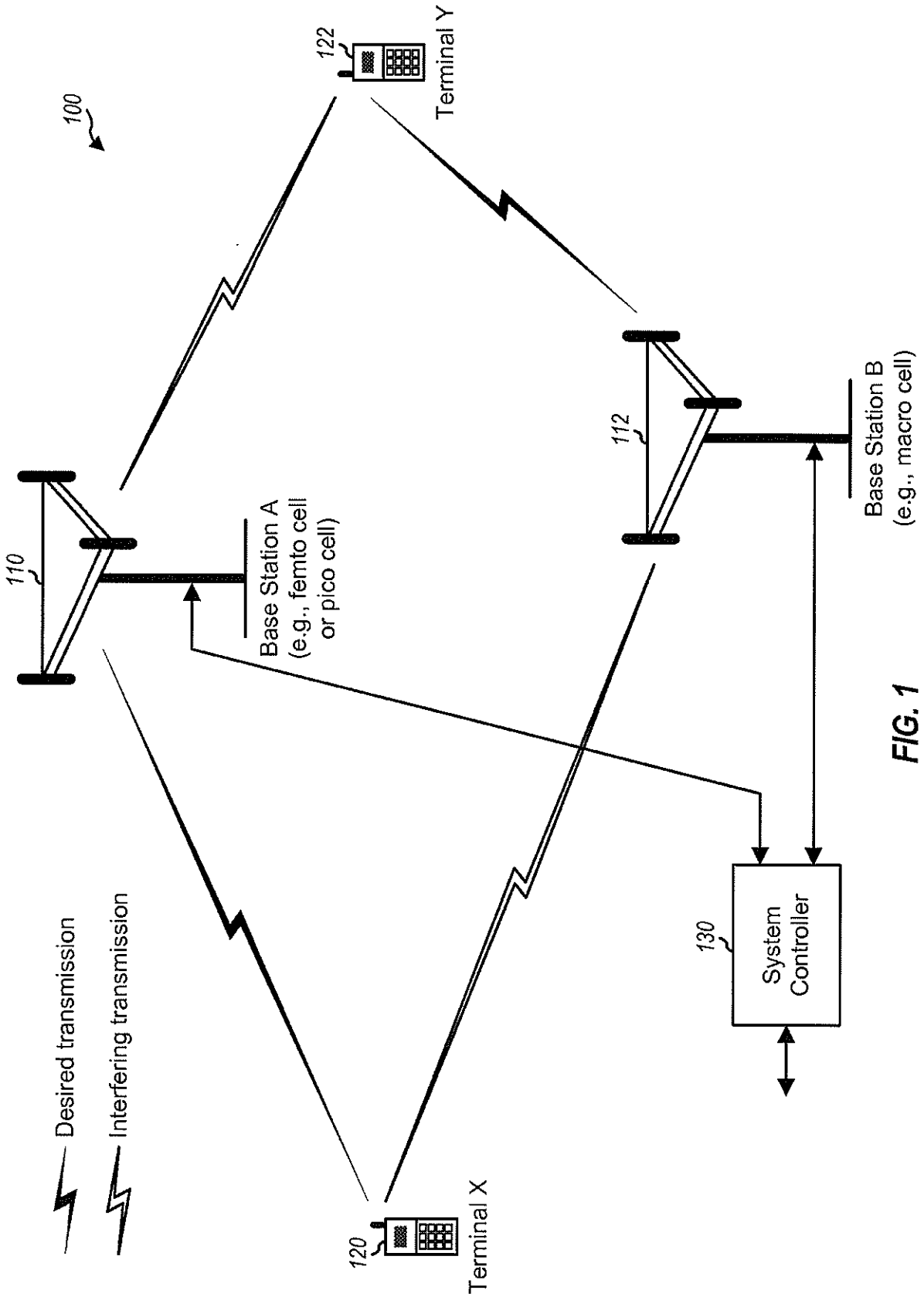

100
122
Terminal Y
110
Base Station A
(e.g., femto cell
or pico cell)
112
Base Station B
(e.g., macro cell)
Desired transmission
Interfering transmission
130
System
Controller
120
Terminal X

FIG. 1

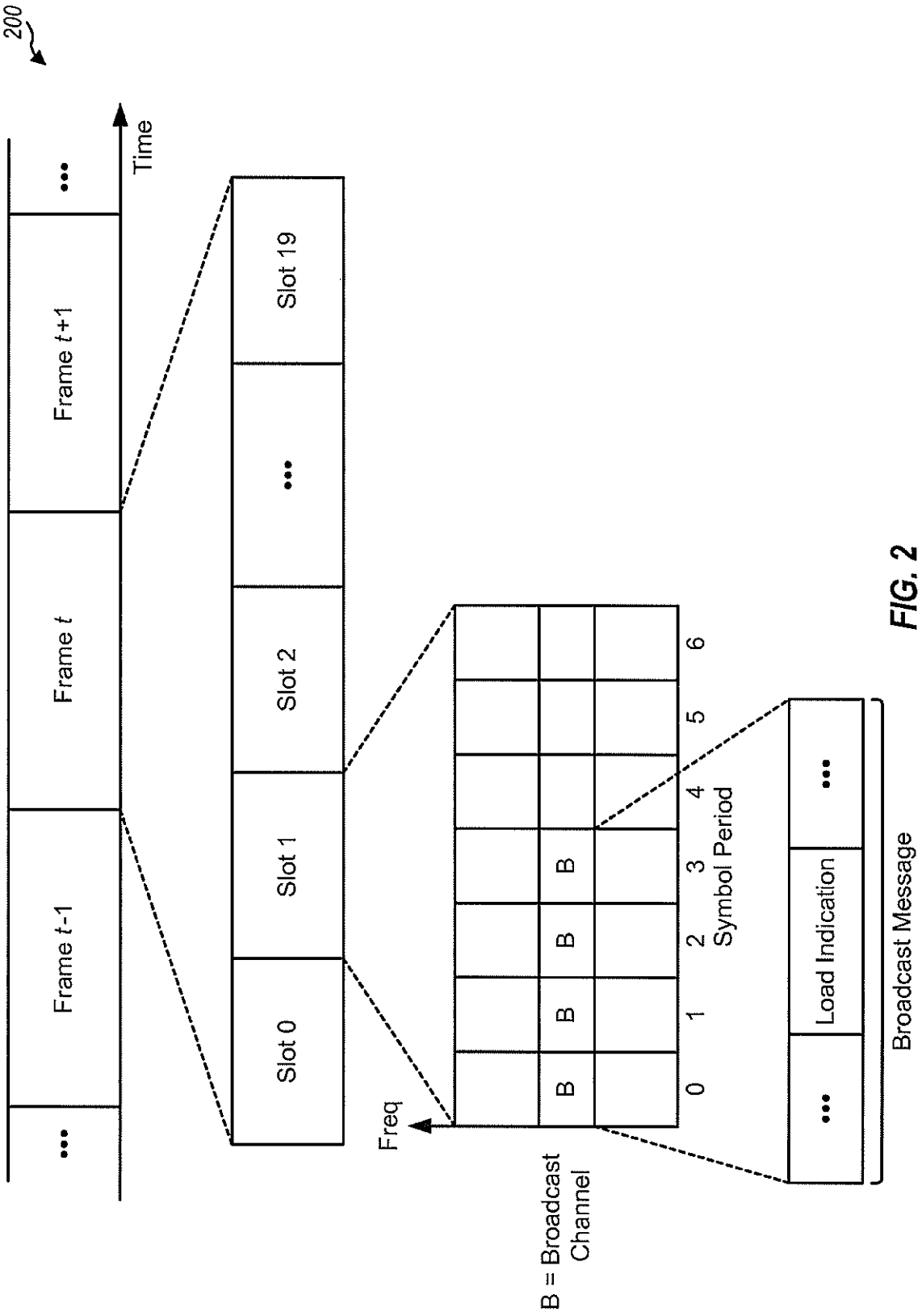

200
Time
Frame t-1
Frame t
Frame t+1
Slot 0
Slot 1
Slot 2
Slot 19
Freq
0
1
2
3
4
5
6
Symbol Period
B = Broadcast Channel
Load Indication
Broadcast Message

FIG. 2

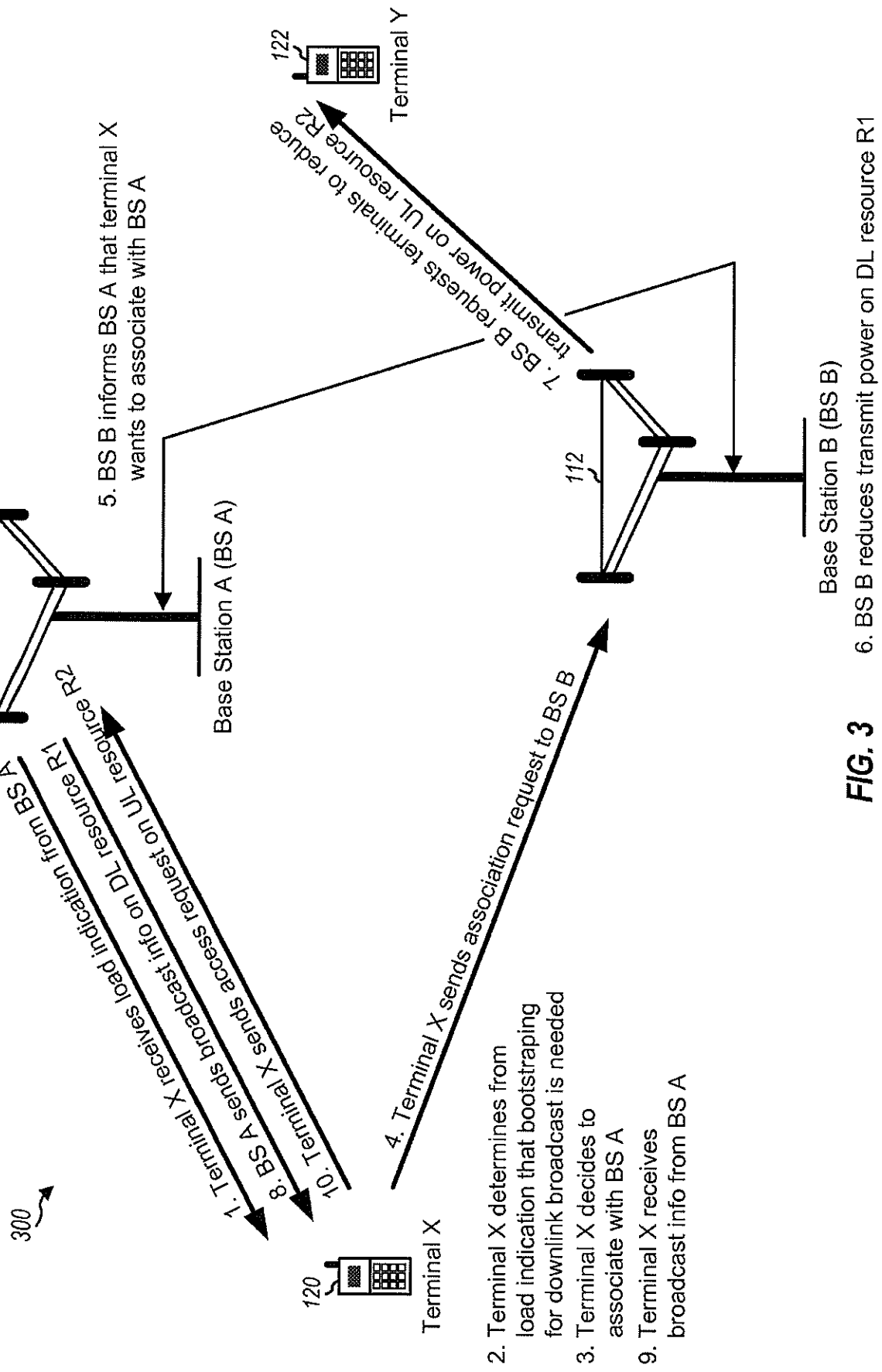

300
120
Terminal X
110
Base Station A (BS A)
112
Base Station B (BS B)
122
Terminal Y
1. Terminal X receives load indication from BS A
2. Terminal X determines from load indication that bootstraping for downlink broadcast is needed
3. Terminal X decides to associate with BS A
4. Terminal X sends association request to BS B
5. BS B informs BS A that terminal X wants to associate with BS A
6. BS B reduces transmit power on DL resource R1
7. BS B requests terminals to reduce transmit power on UL resource R2
8. BS A sends broadcast info on UL resource R2
9. Terminal X receives broadcast info from BS A
10. Terminal X sends access request on DL resource R1

FIG. 3

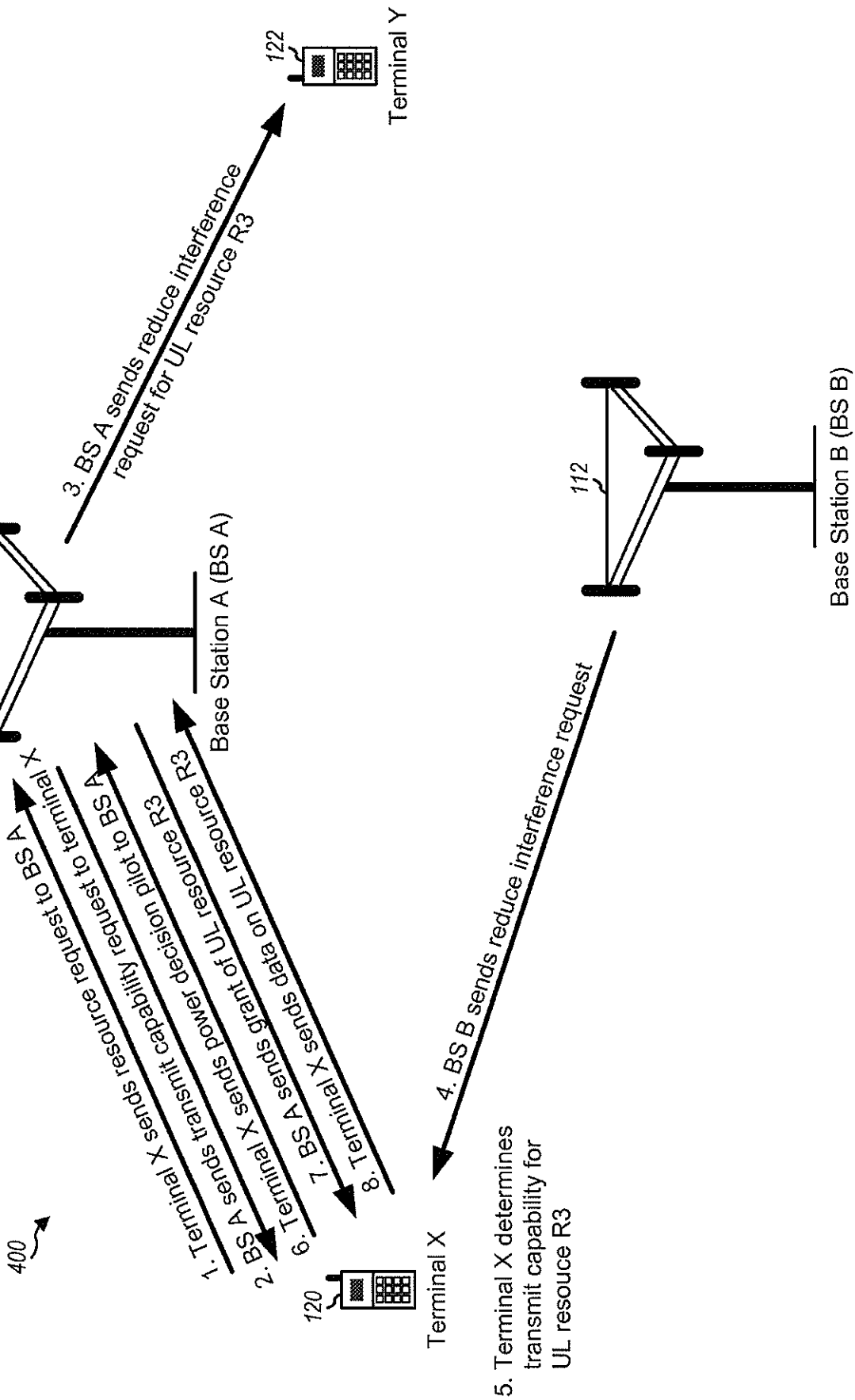

400
110
Base Station A (BS A)
112
Base Station B (BS B)
120
Terminal X
122
Terminal Y
1. Terminal X sends resource request to BS A
2. BS A sends transmit capability request to terminal X
3. BS A sends reduce interference request for UL resource R3
4. BS B sends reduce interference request
5. Terminal X determines transmit capability for UL resouce R3
6. Terminal X sends power decision pilot to BS A
7. BS A sends grant of UL resource R3
8. Terminal X sends data on UL resource R3

FIG. 4

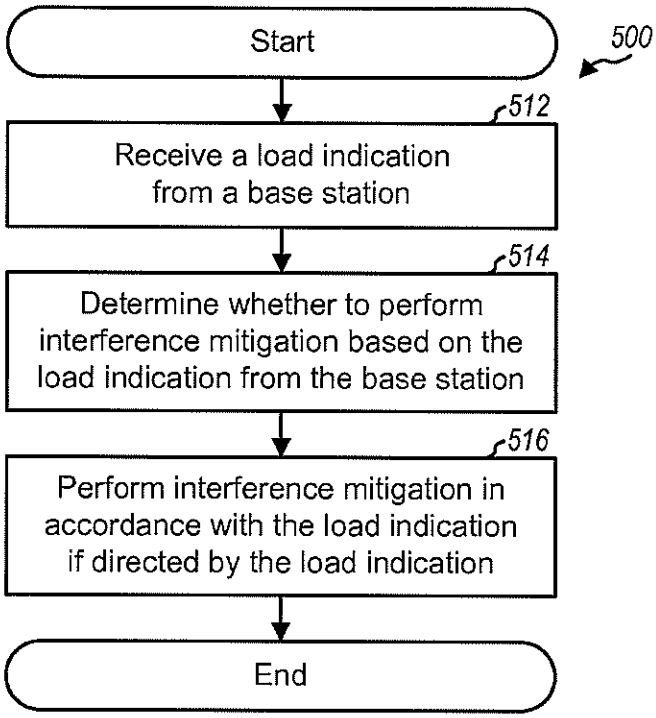


FIG. 5

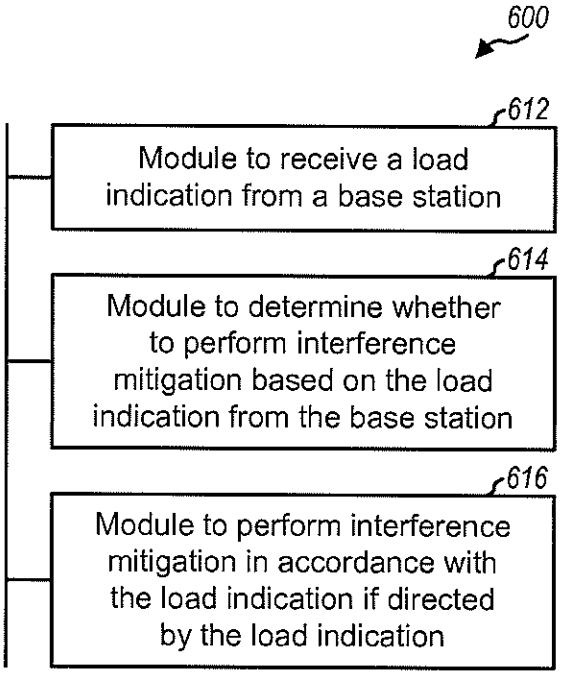


FIG. 6

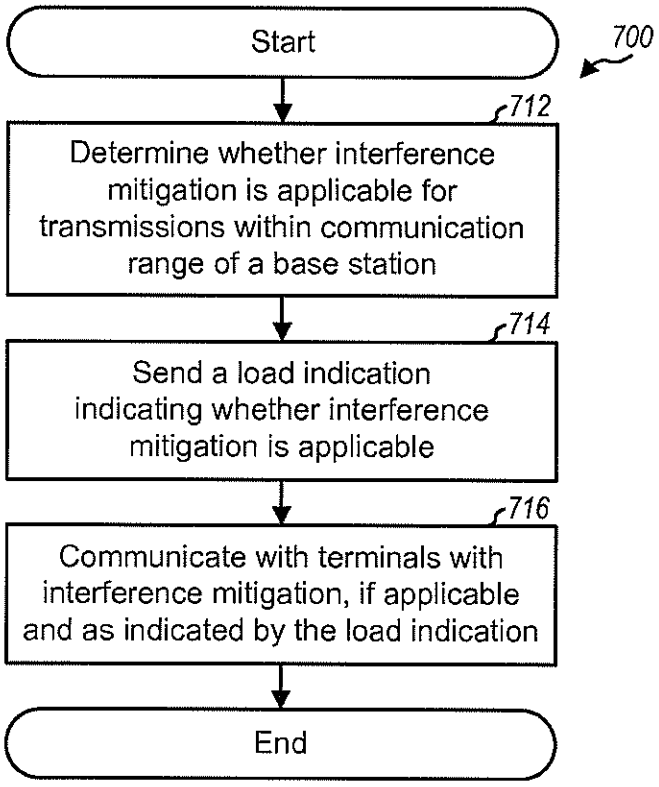


FIG. 7

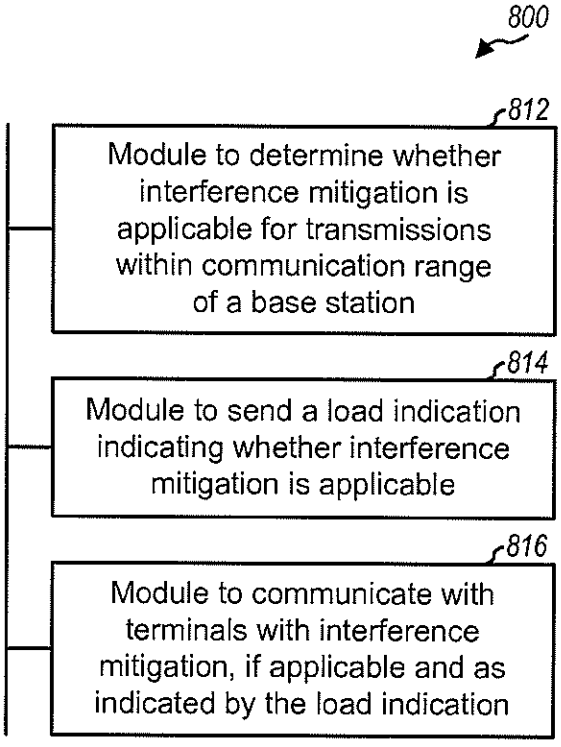


FIG. 8

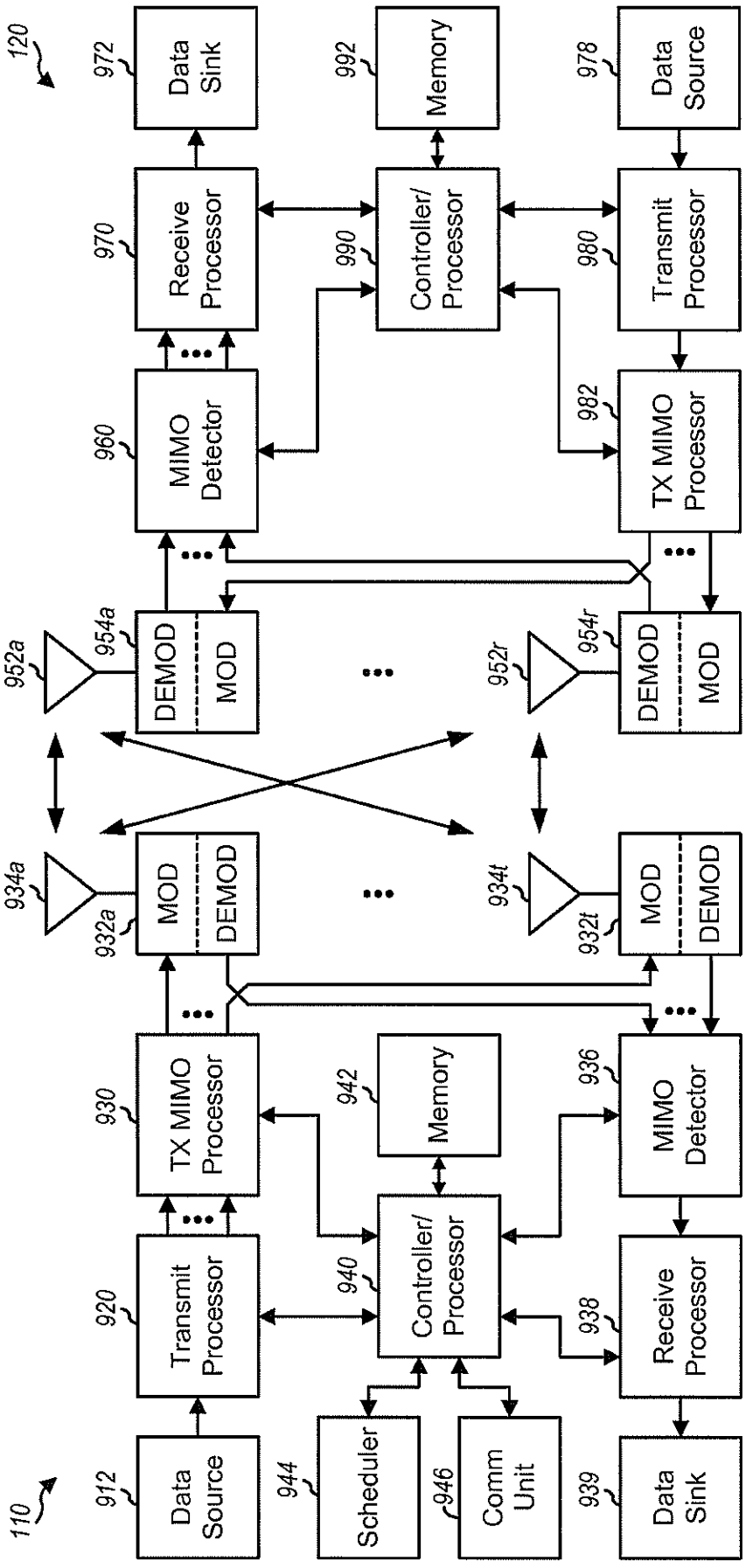

120
972
Data Sink
992
Memory
978
Data Source
970
Receive Processor
990
Controller/ Processor
980
Transmit Processor
960
MIMO Detector
982
TX MIMO Processor
952a
954a
DEMOD
MOD
952r
954r
DEMOD
MOD
934a
932a
MOD
DEMOD
934t
932t
MOD
DEMOD
930
TX MIMO Processor
942
Memory
936
MIMO Detector
920
Transmit Processor
940
Controller/ Processor
938
Receive Processor
110
912
Data Source
944
Scheduler
946
Comm Unit
939
Data Sink

FIG. 9