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(54) Title: MULTIPLY-ACCUMULATE (MAC) UNIT FOR SINGLE-INSTRUCTION/MULTIPLE-DATA (SIMD) INSTRUCTIONS

(57) Abstract: A tightly coupled dual 16-bit multiply-accumulate (MAC) unit for performing single-instruction/multiple-data (SIMD) operations may forward an intermediate result to another operation in a pipeline to resolve an accumulating dependency penalty. The MAC unit may also be used to perform 32-bit X 32-bit operations.



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