

FIG. 1

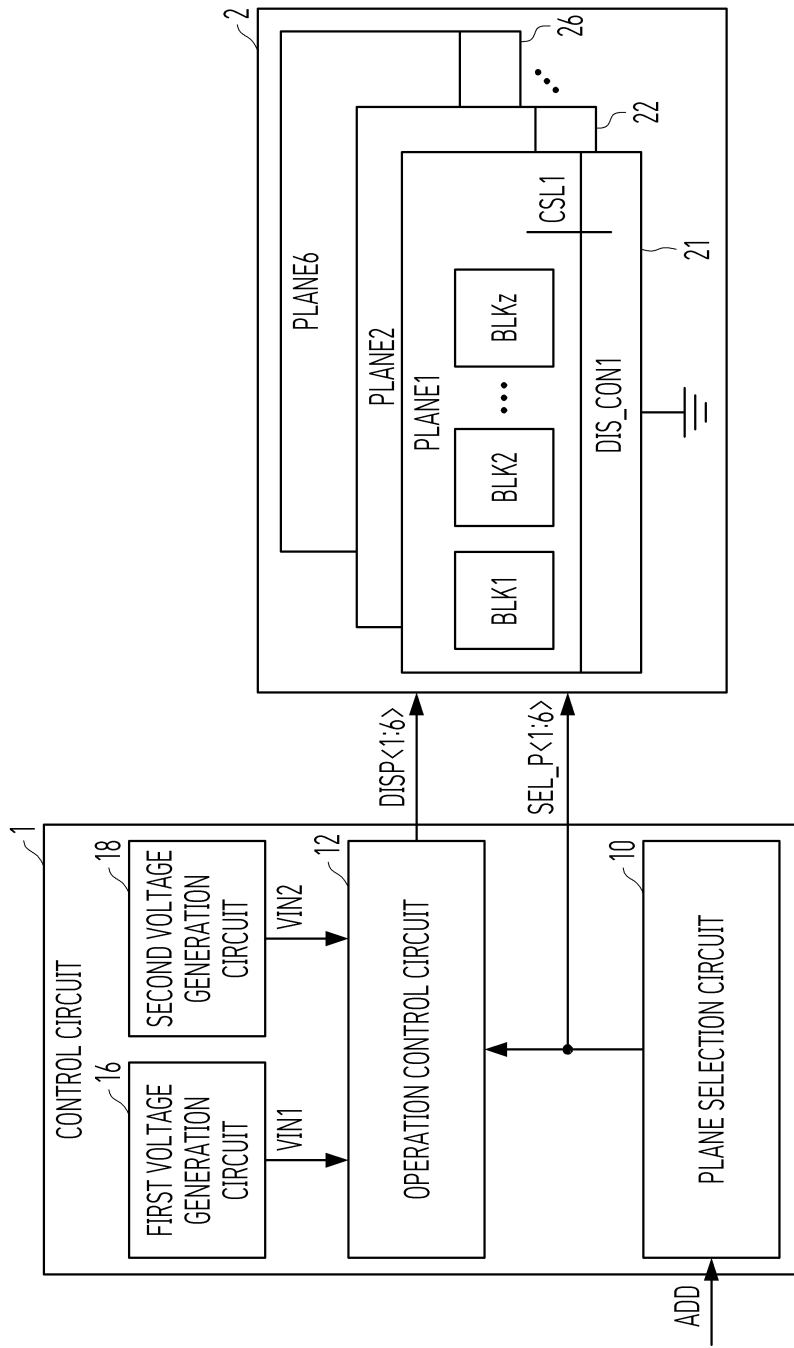


FIG. 2A

2

*DISCHARGE CONTROL CIRCUIT OF FIRST GROUP: DIS_CON1, DIS_CON3

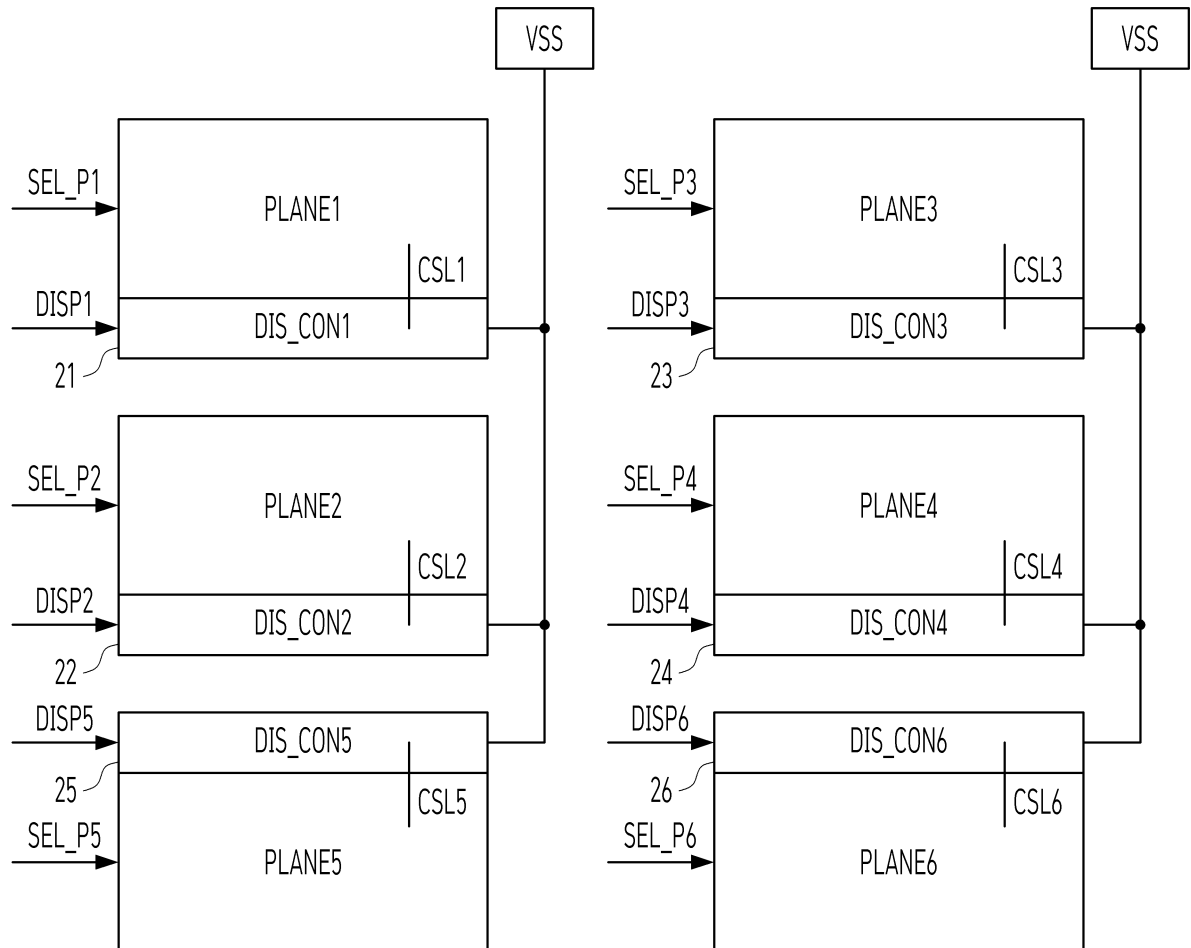
*DISCHARGE CONTROL CIRCUIT OF SECOND GROUP:
DIS_CON2, DIS_CON4, DIS_CON5, DIS_CON6

FIG. 2B

2

*DISCHARGE CONTROL CIRCUIT OF FIRST GROUP:

DIS_CON1, DIS_CON2, DIS_CON3, DIS_CON4

*DISCHARGE CONTROL CIRCUIT OF SECOND GROUP: DIS_CON5, DIS_CON6

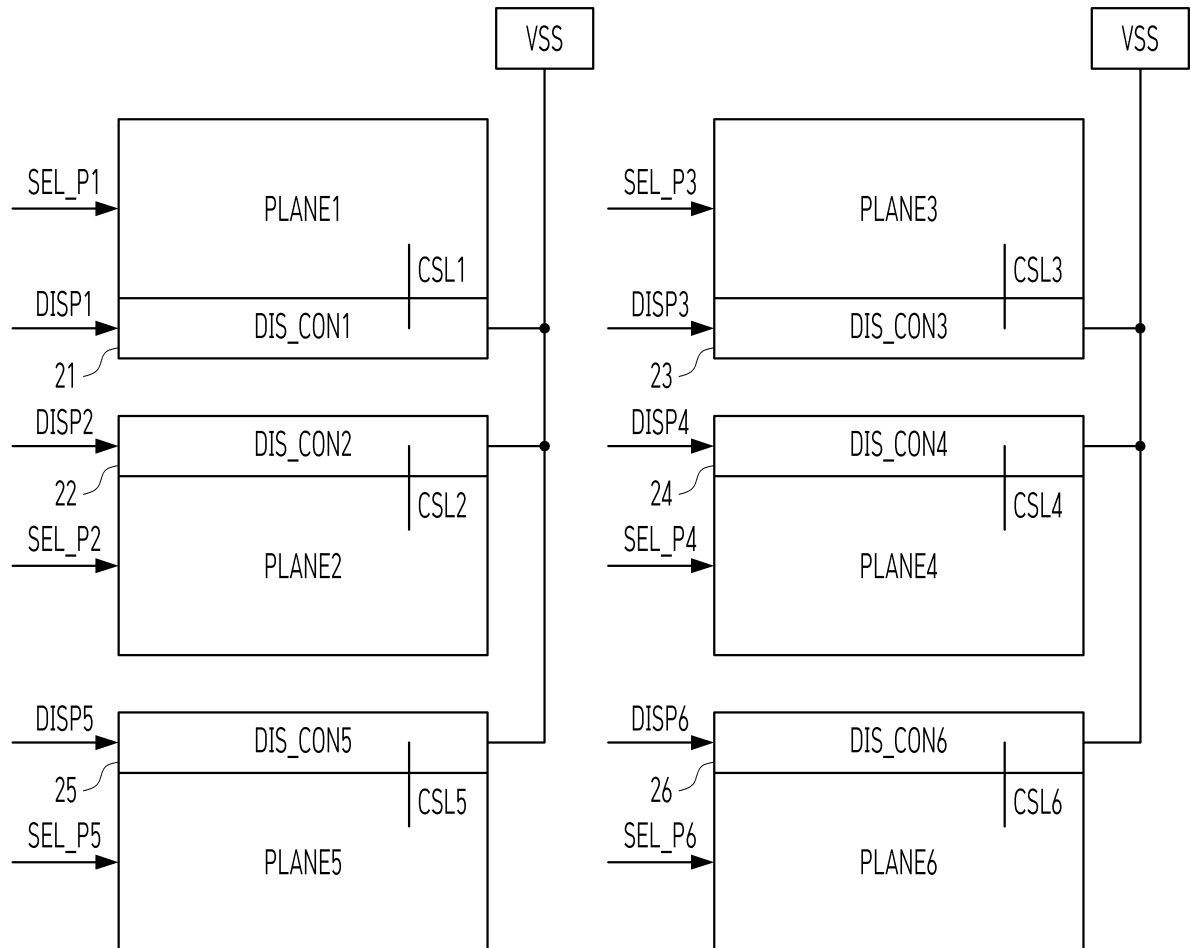


FIG. 3

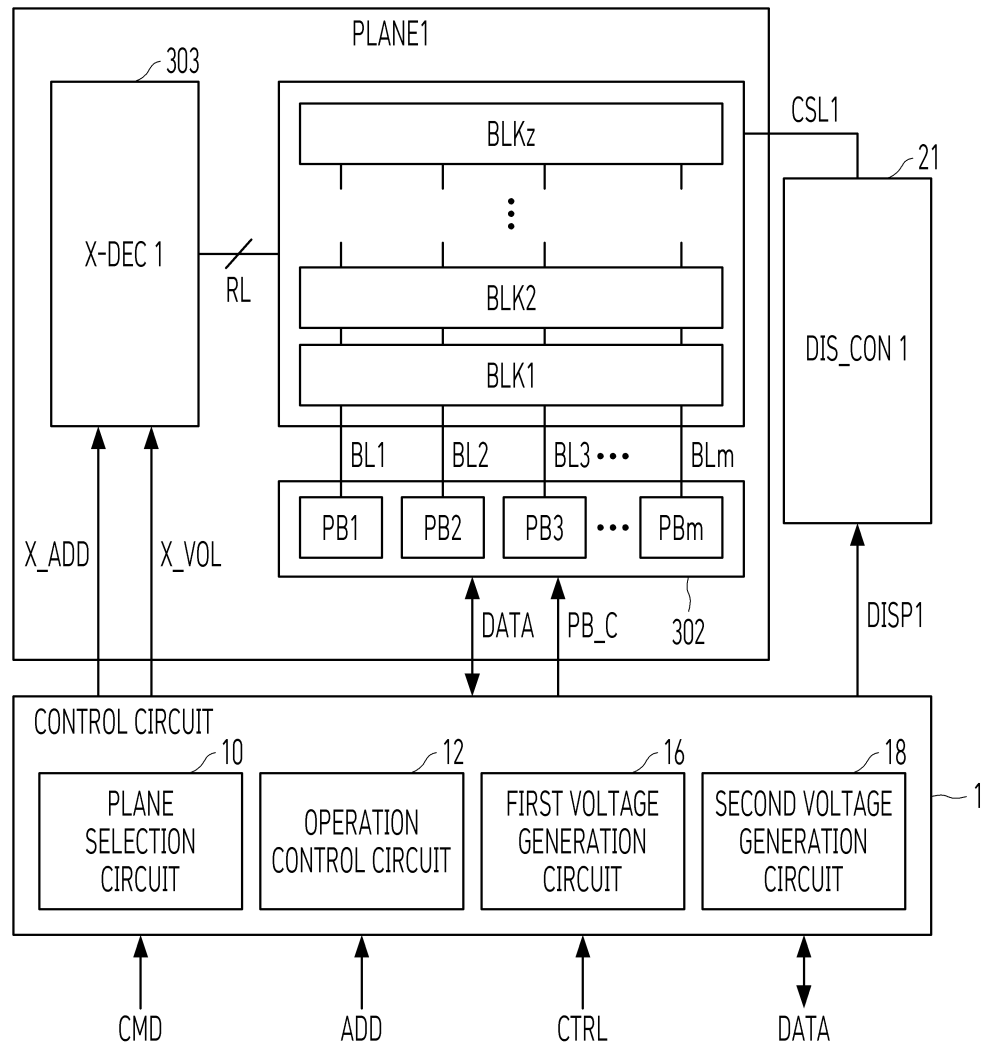


FIG. 4

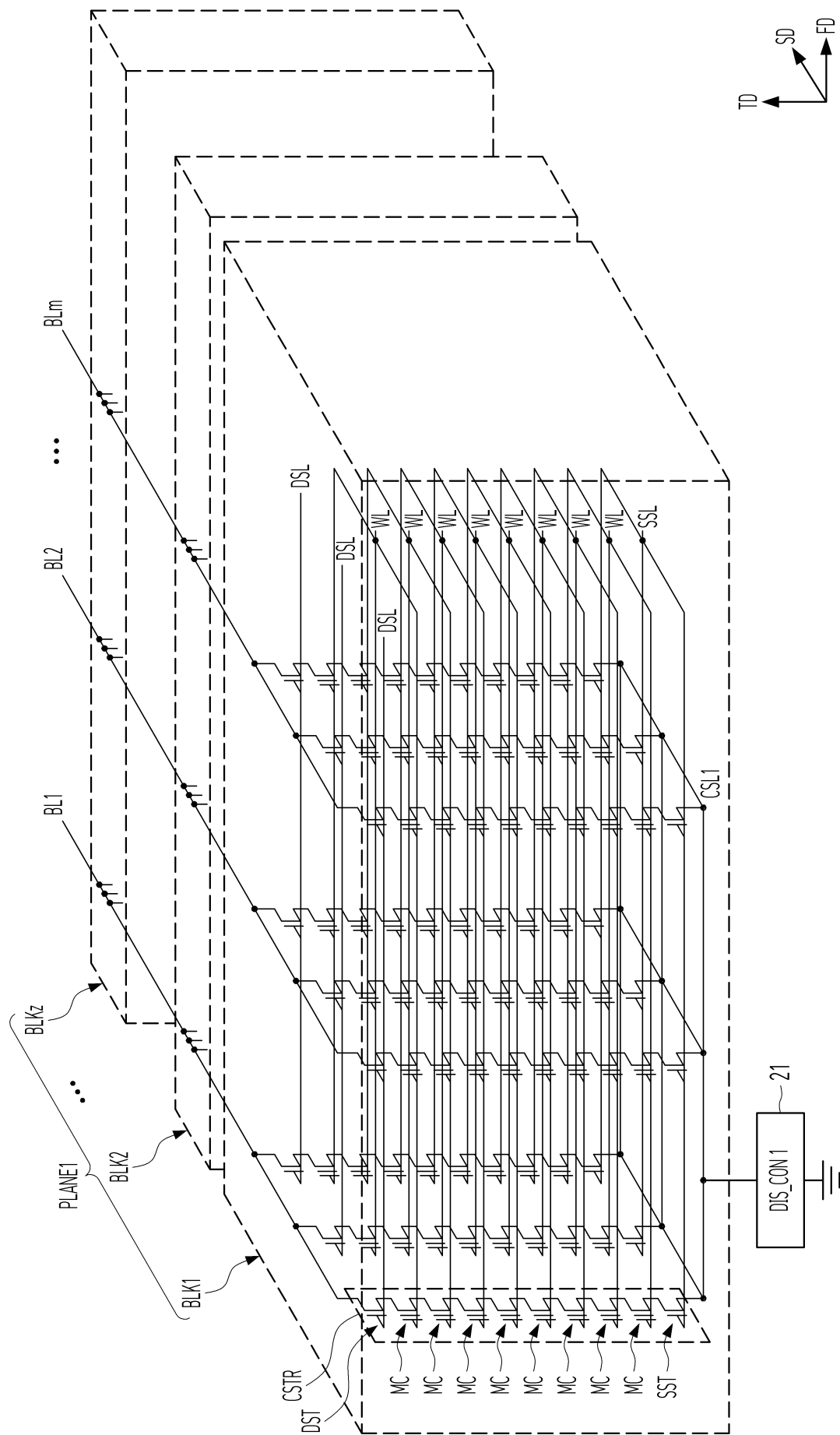


FIG. 5A

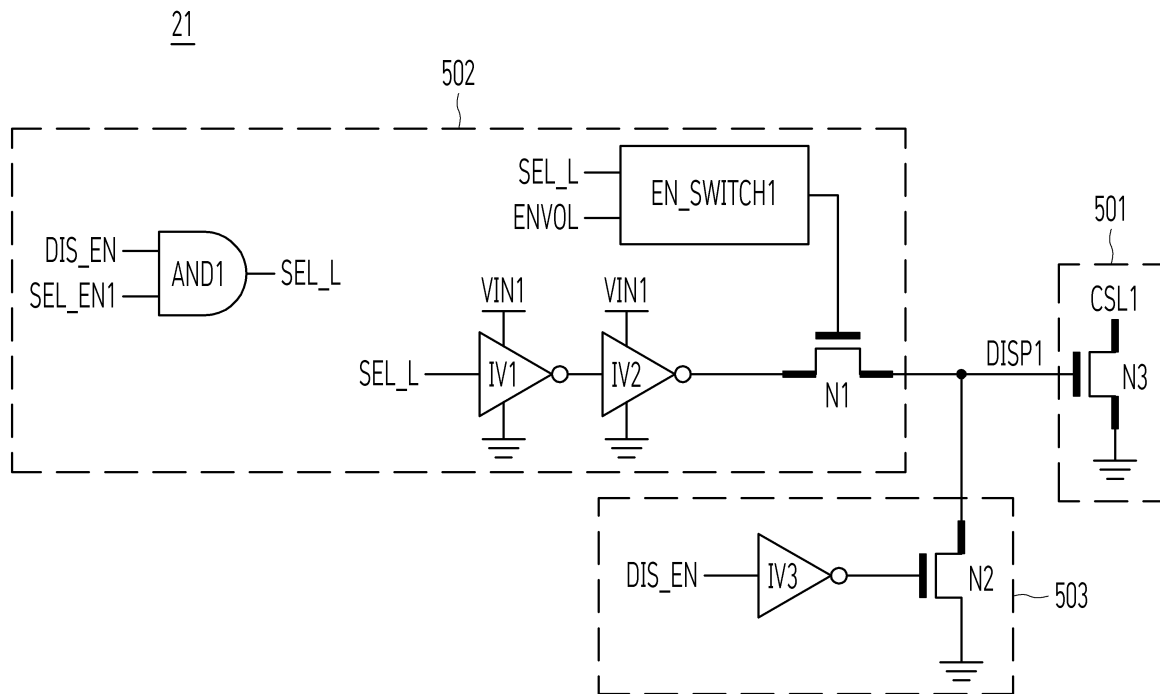


FIG. 5B

