

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
13 February 2003 (13.02.2003)

PCT

(10) International Publication Number
WO 03/013061 A1

(51) International Patent Classification⁷: **H04L 12/18**

Princeton, NJ 08540 (US). **HESSE, John** [US/US]; 827
Stetson Street, Moss Beach, CA 94038 (US).

(21) International Application Number: PCT/US02/23411

(22) International Filing Date: 22 July 2002 (22.07.2002)

(74) Agent: **NOWAK, Keith, D.**; Lieberman & Nowak, LLP,
350 Fifth Avenue, New York, NY 10118 (US).

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
09/919,462 31 July 2001 (31.07.2001) US

(81) Designated States (*national*): AE, AG, AL, AM, AT, AU,
AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU,
CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH,
GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC,
LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW,
MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SD, SE, SG,
SI, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VN,
YU, ZA, ZM, ZW.

(71) Applicant: **INTERACTIC HOLDINGS, LLC** [US/US];
P.O. Box 1459, Princeton, NJ 08542 (US).

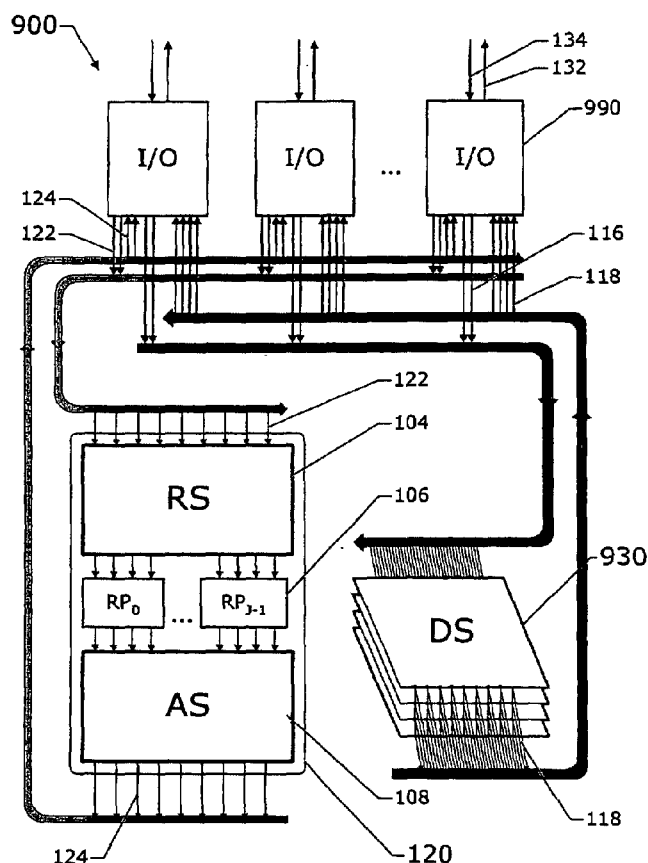
(84) Designated States (*regional*): ARIPO patent (GH, GM,
KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW),
Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),
European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE,

(71) Applicants and

(72) Inventors: **REED, Coke** [US/US]; 62 William Street,

[Continued on next page]

(54) Title: SCALABLE SWITCHING SYSTEM WITH INTELLIGENT CONTROL



PARALLEL DATA SWITCH

(57) Abstract: This invention is directed to a parallel information generation, distribution and processing system (900). This scalable, pipelined control and switching system (900) efficiently and fairly manages a plurality of incoming data streams (132, 134), and applies class and quality of service requirements. The present invention also uses scalable MLML switch fabrics to control a data packet switch (930), including a request-processing switch (104) used to control the data-packet switch (930). Also included is a request processor (106) for each output port, which manages and approves all data flow to that output port, and an answer switch (108) which transmits answer packets from request processors (106) back to requesting input ports.

WO 03/013061 A1



ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, SK,
TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ,
GW, ML, MR, NE, SN, TD, TG).

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

Published:

— *with international search report*