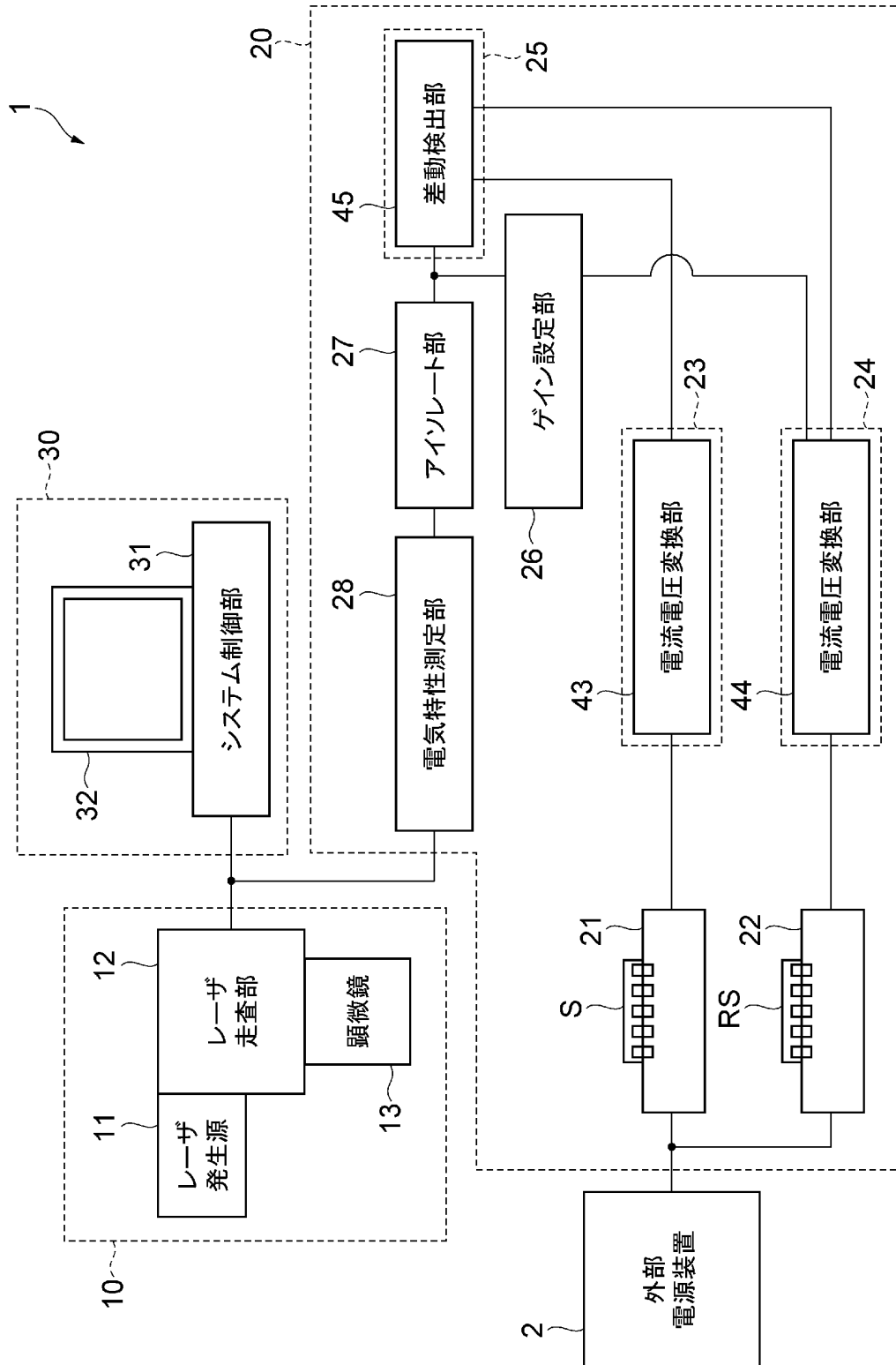
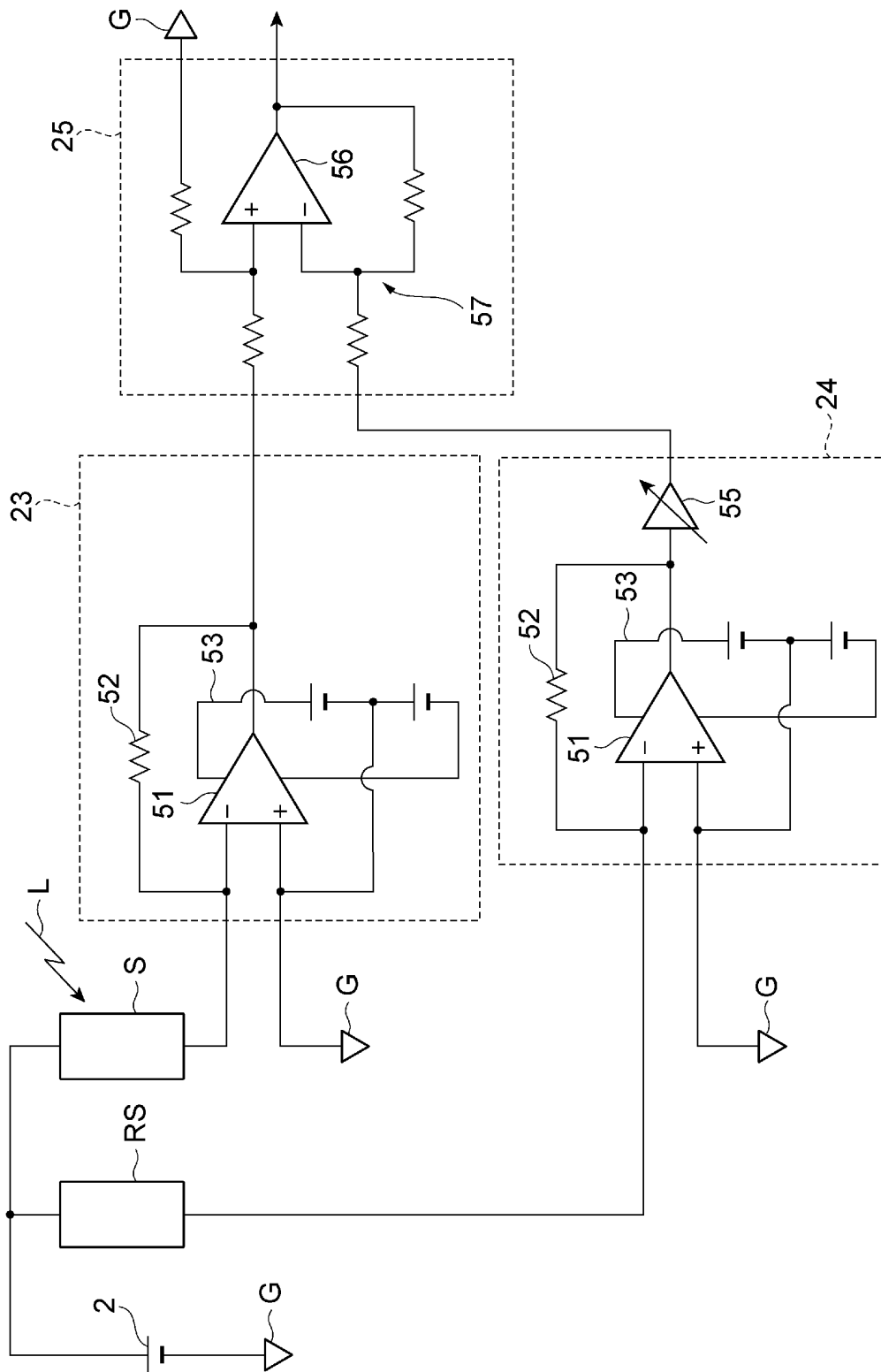


[図1]



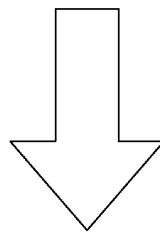
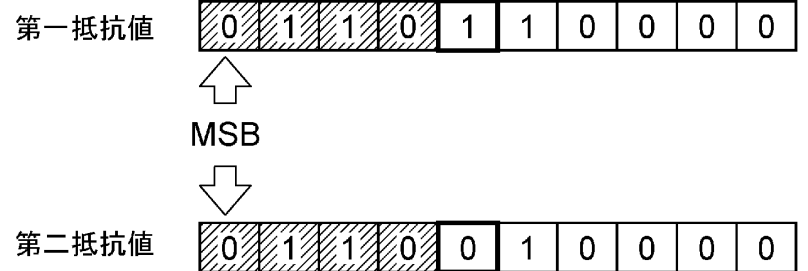
[図2]



[図3]

n=4

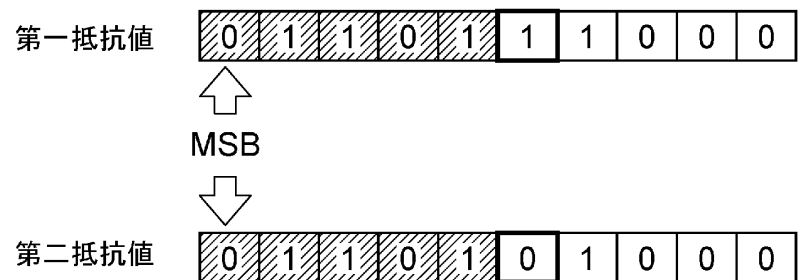
4ビット目のビット値の決定



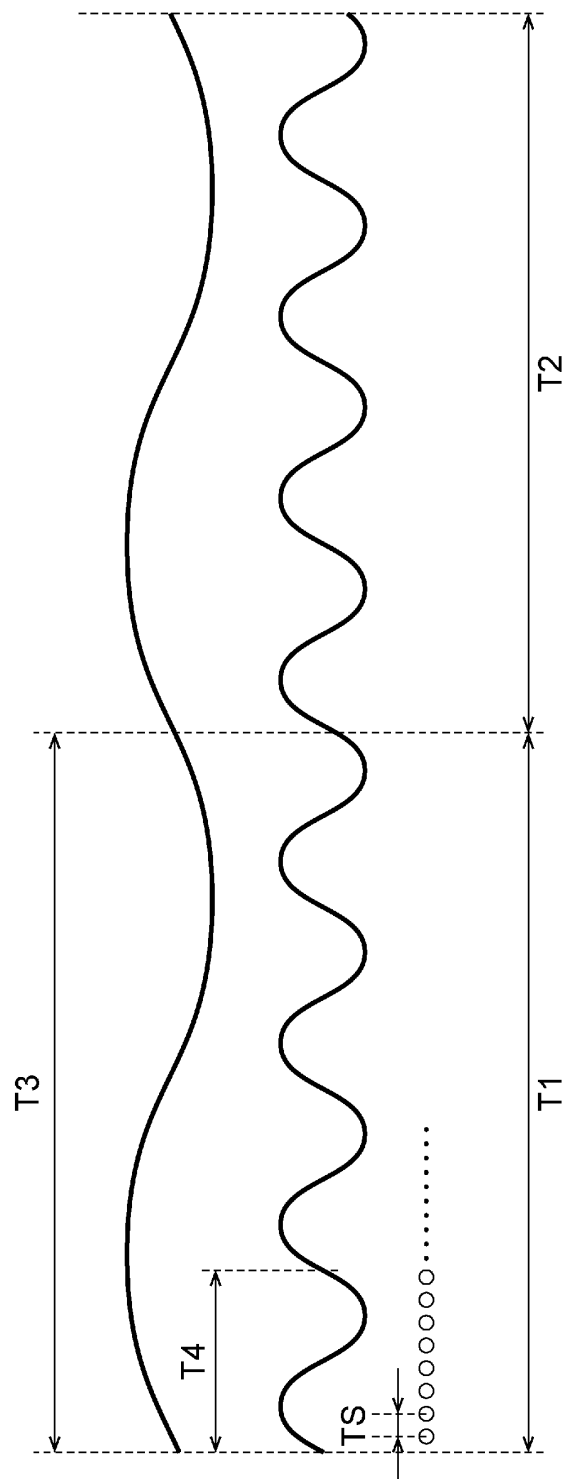
第一抵抗値の方が第二抵抗値よりも
第一処理信号の強度が小さい

n=5

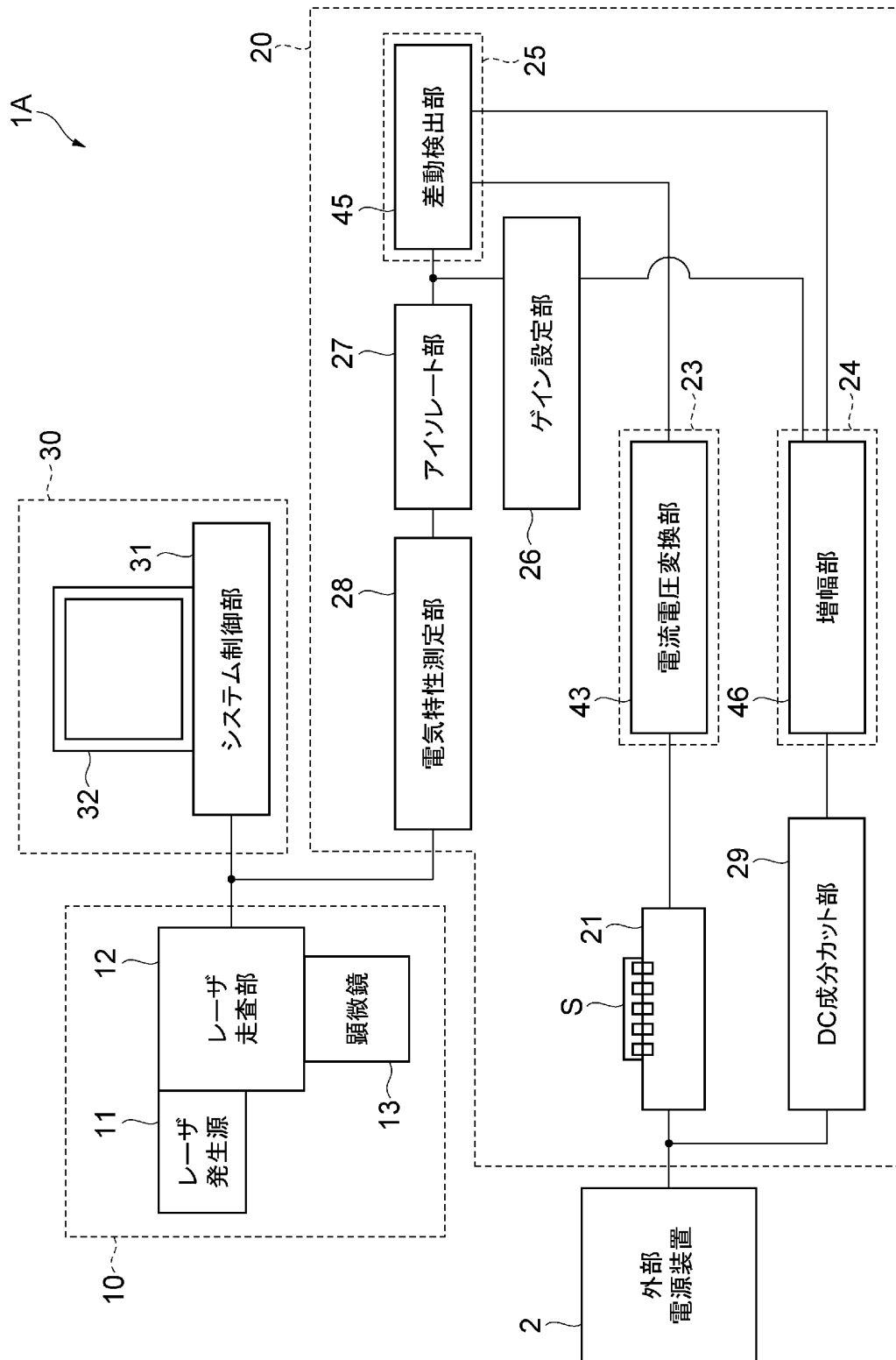
5ビット目のビット値の決定



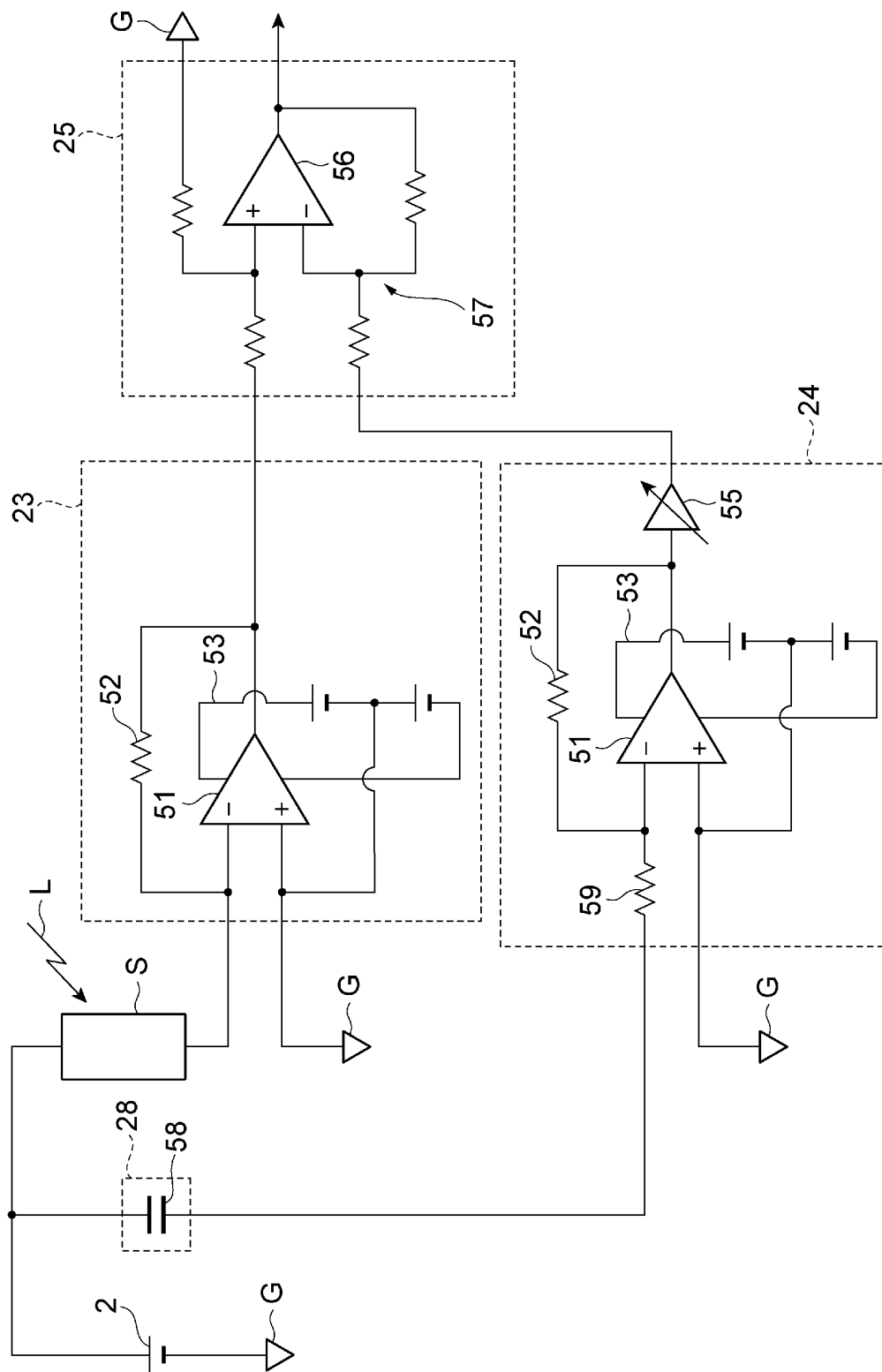
[図4]



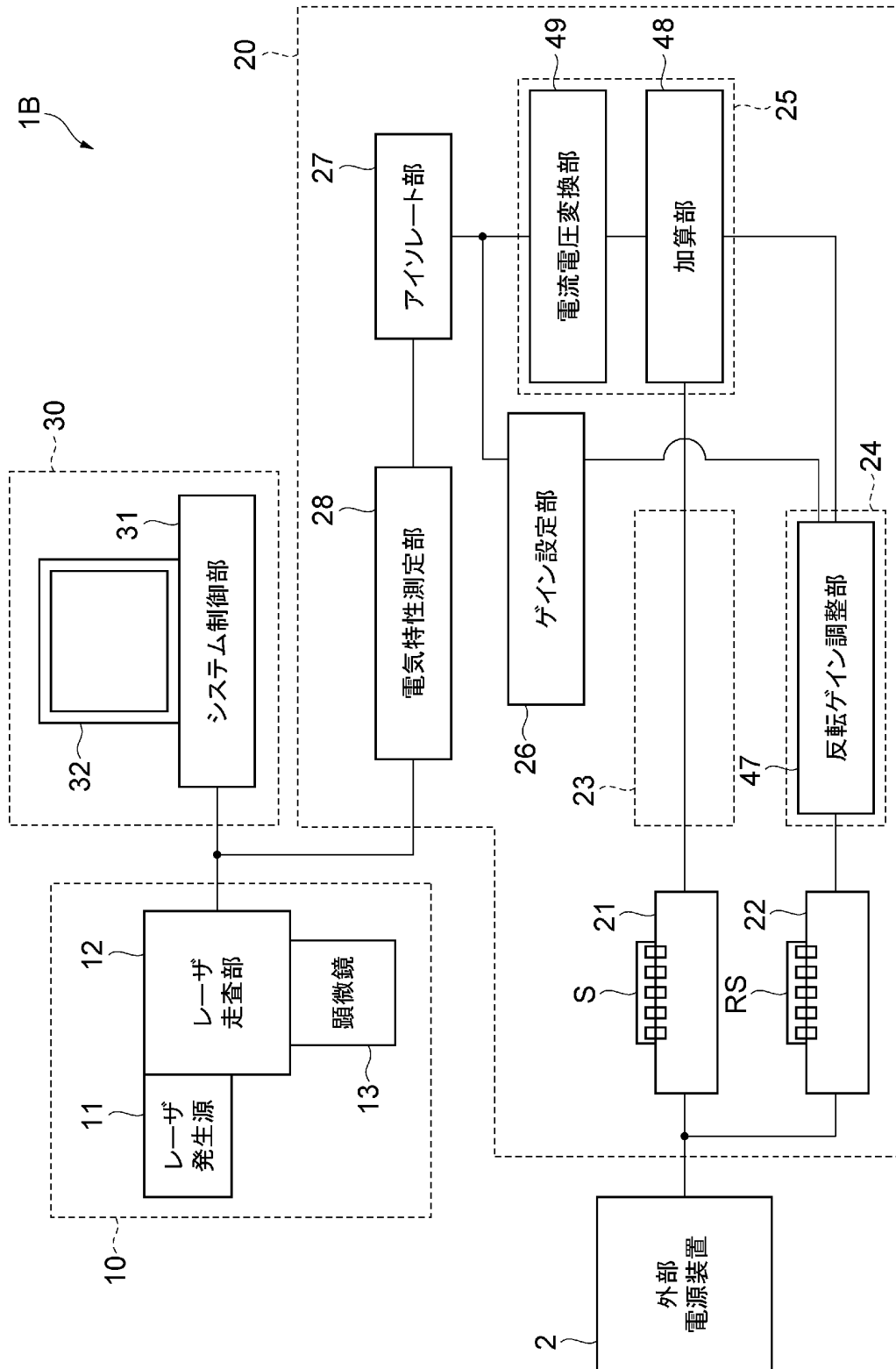
[図5]



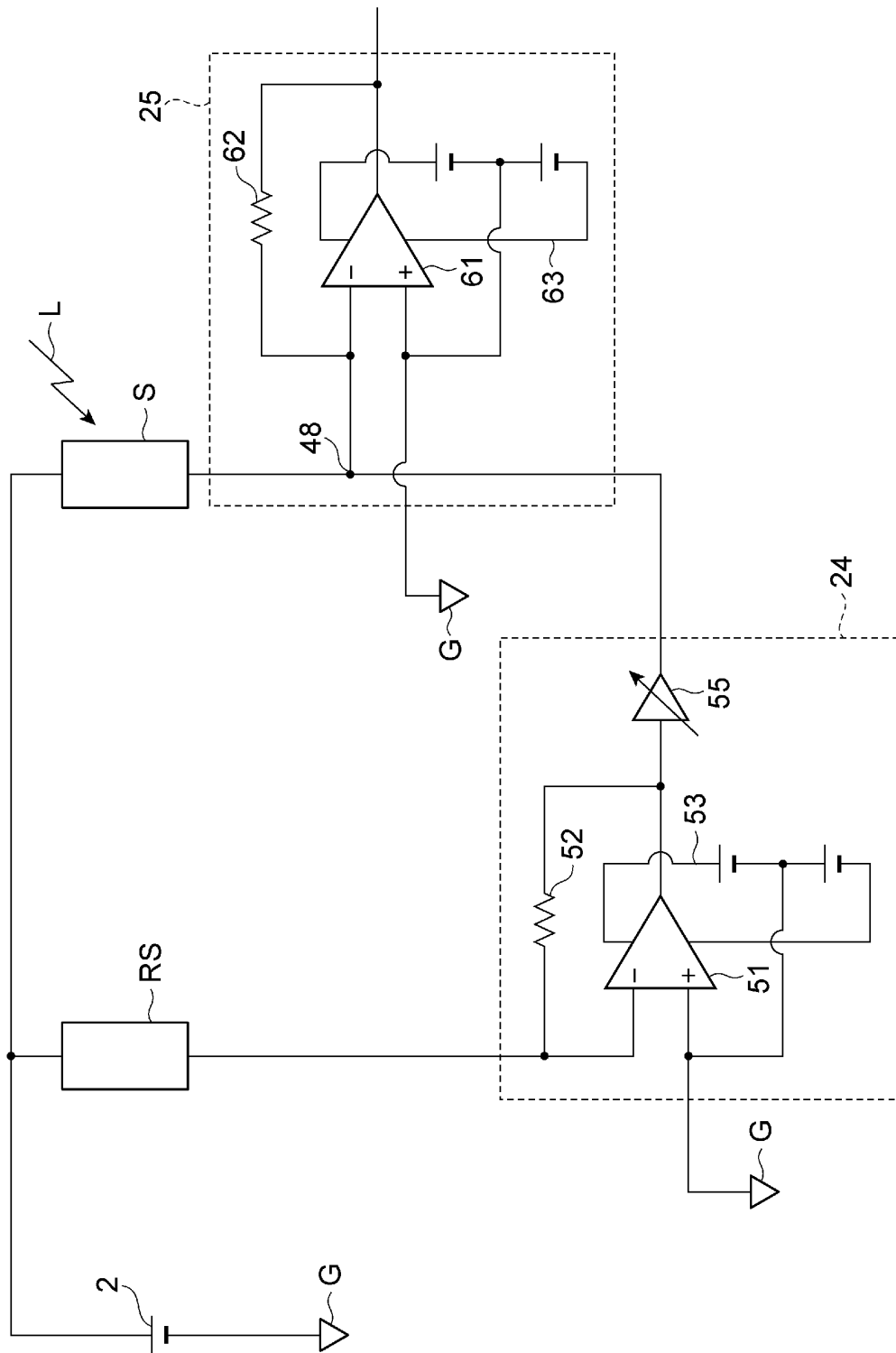
[図6]



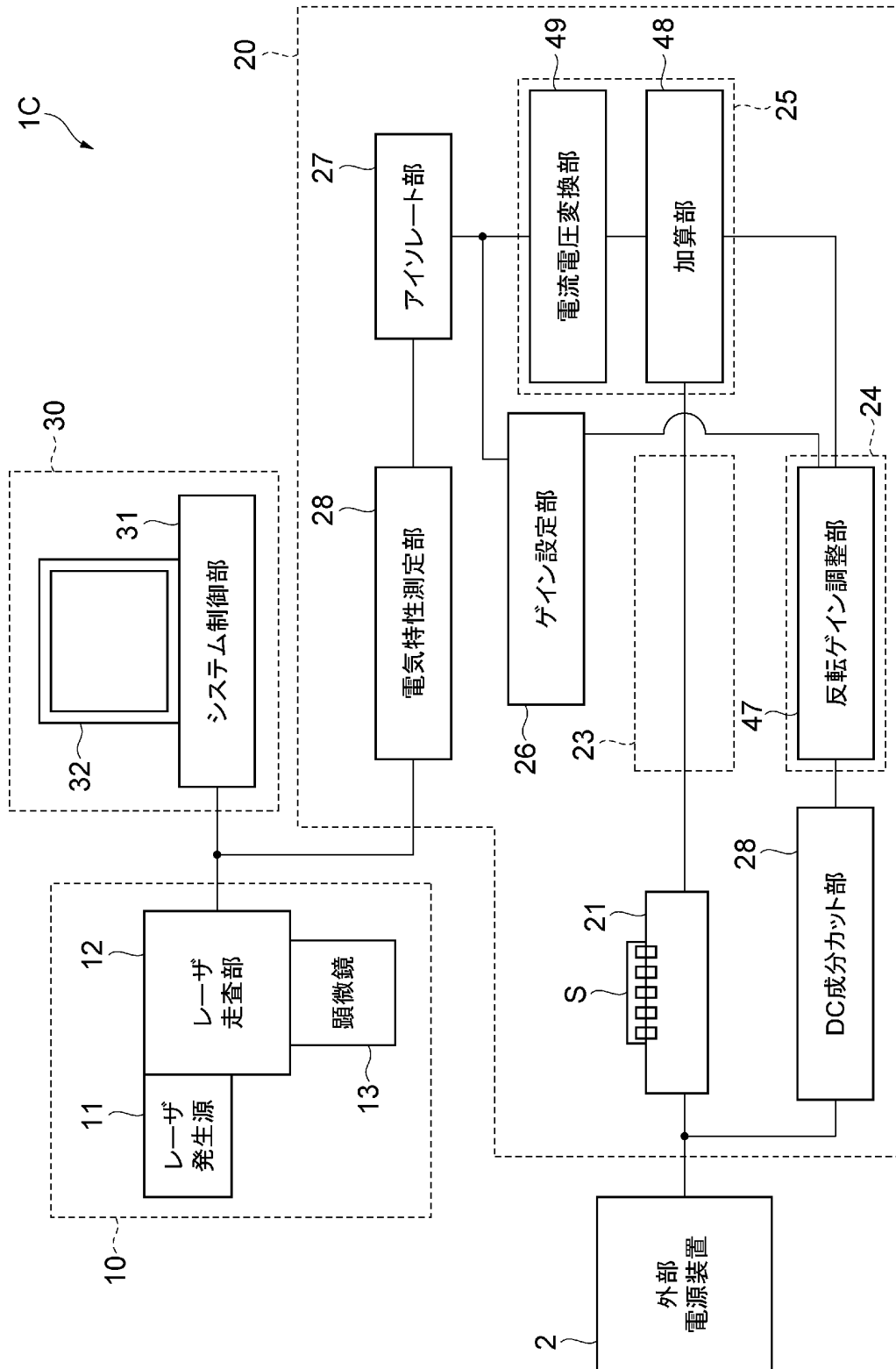
[図7]



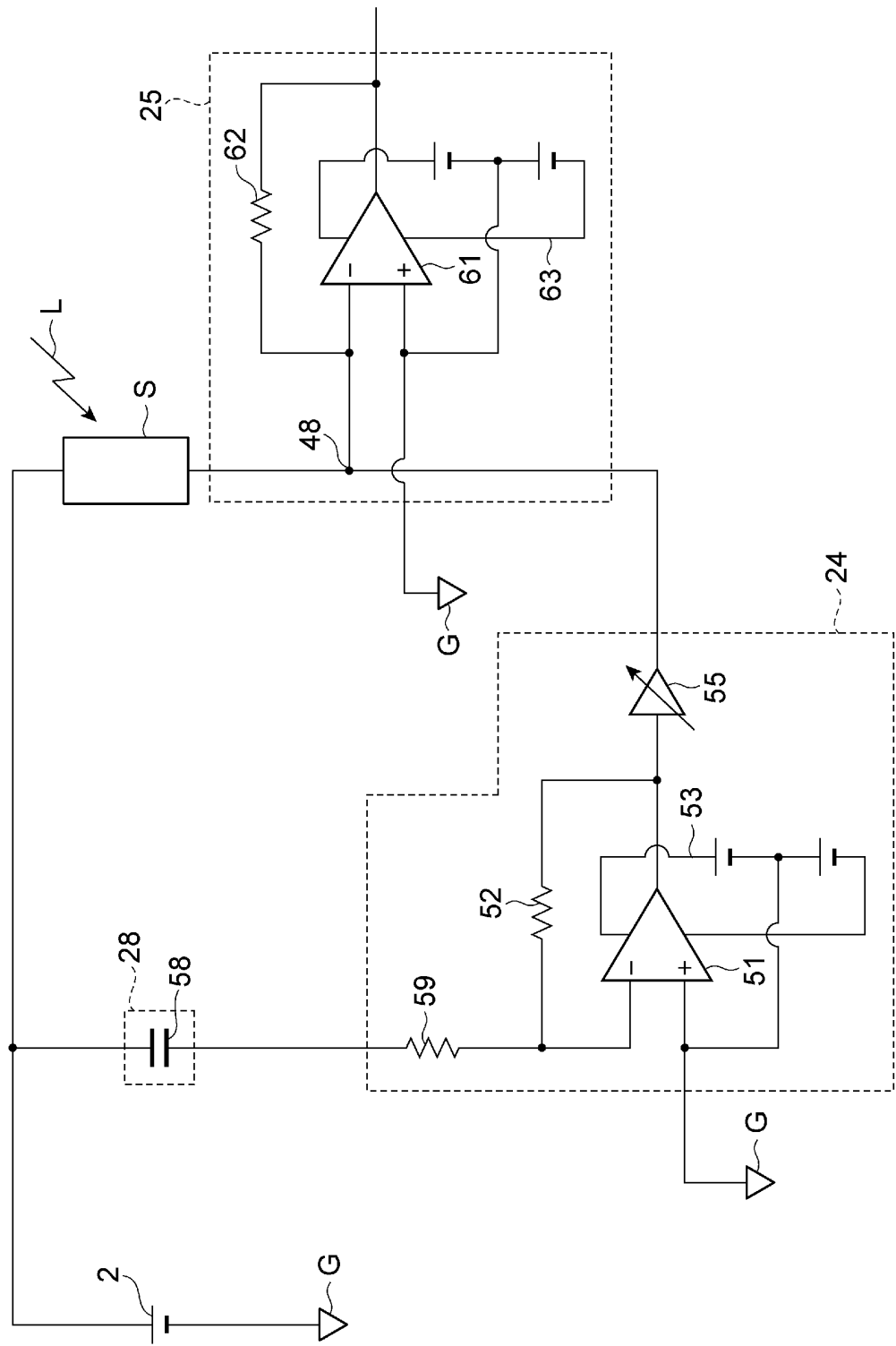
[図8]



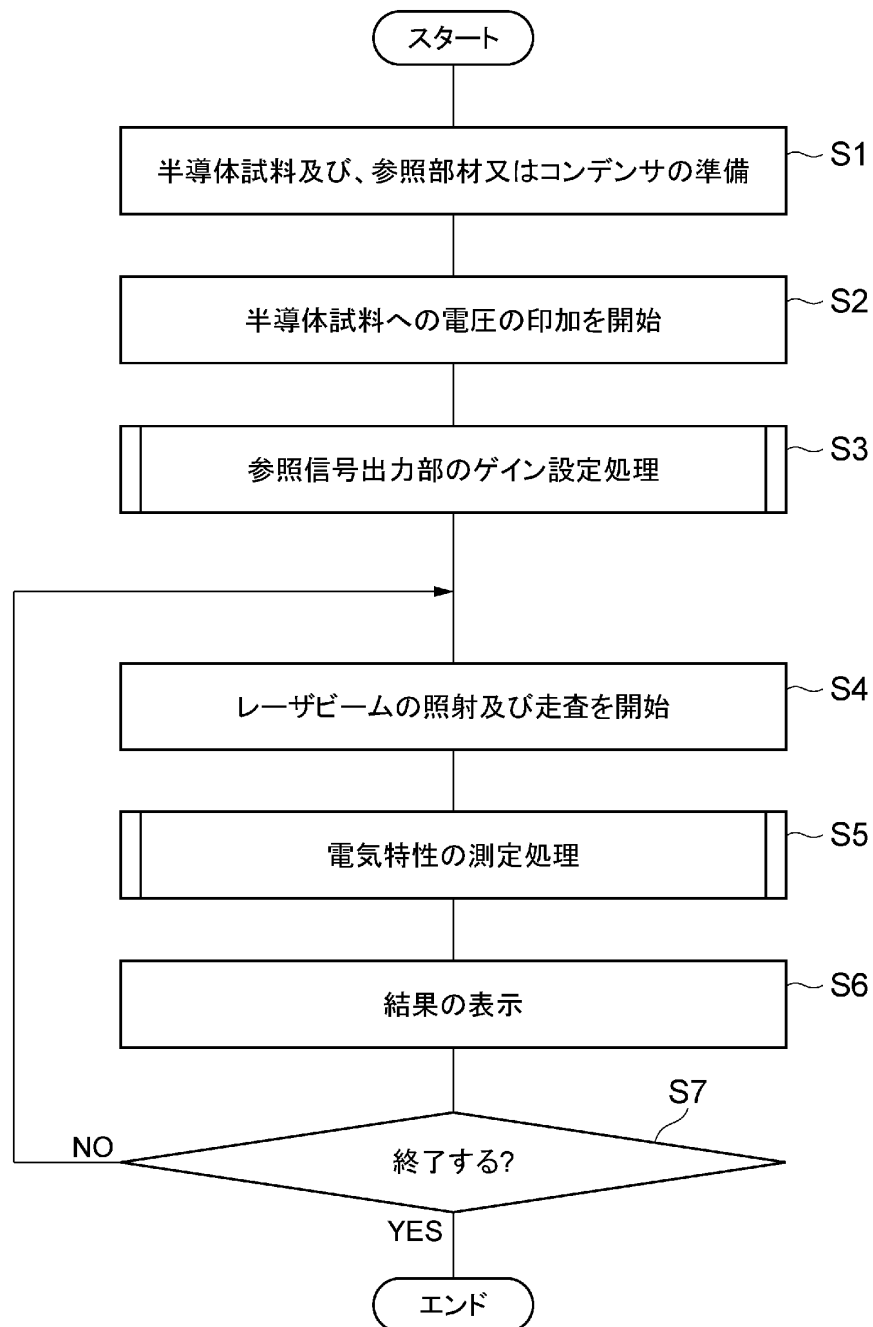
[図9]



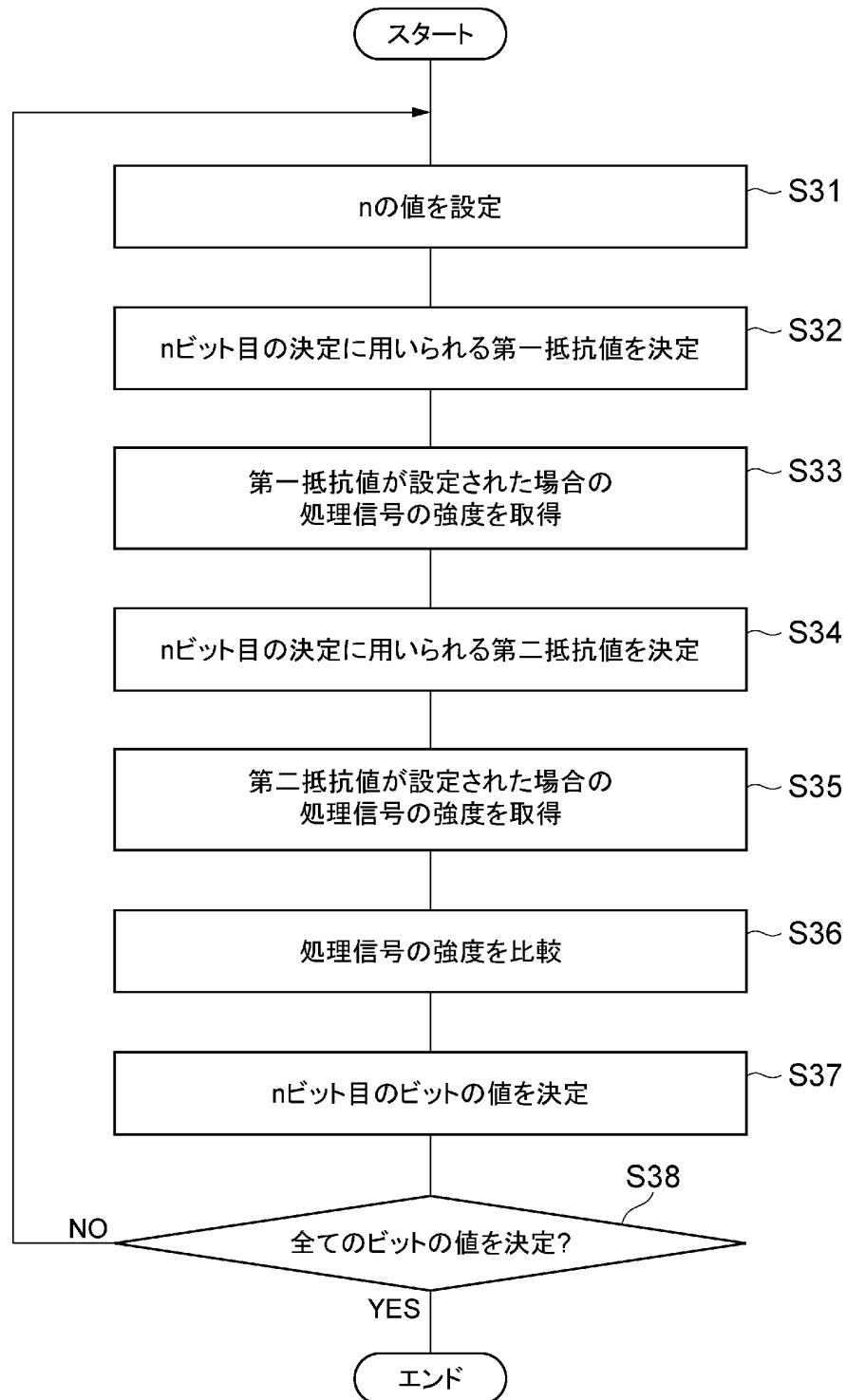
[図10]



[図11]



[図12]



[図13]

