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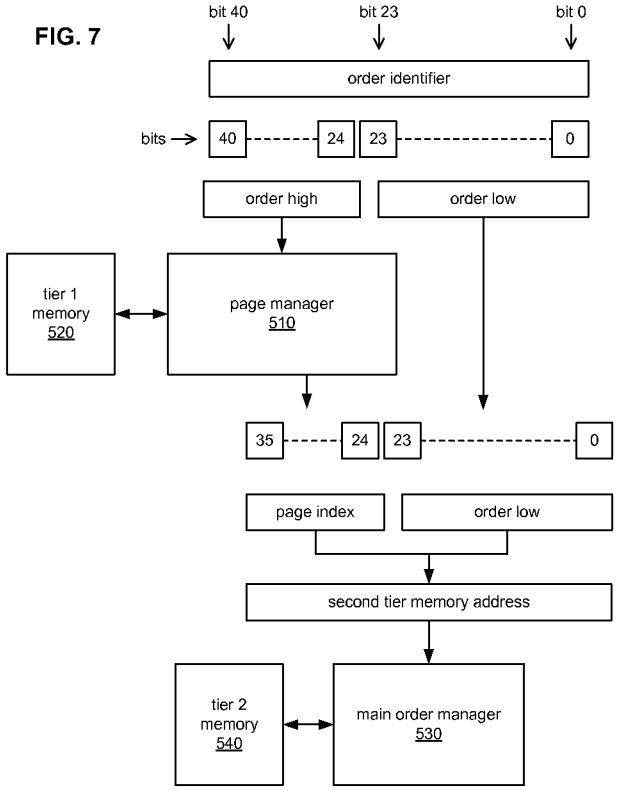
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[Continued on next page]

(54) Title: FPGA MEMORY PAGING

FIG. 7



(57) Abstract: To improve the performance of a market data platform, a market data platform includes a hardware offload solution that allows received financial information to bypass the software portion of the platform and be written directly into memory. The hardware offload solution includes a field programmable gate array (FPGA) that takes over responsibility for some tasks that were previously performed in software. These tasks include performing line arbitrage on financial information feed pairs to ensure that an accurate copy of the financial information feed is obtained and stored in memory as quickly as possible. These tasks also include performing memory paging in order to allow financial orders to be stored and accessed in an efficient manner.

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