Fig. 1A

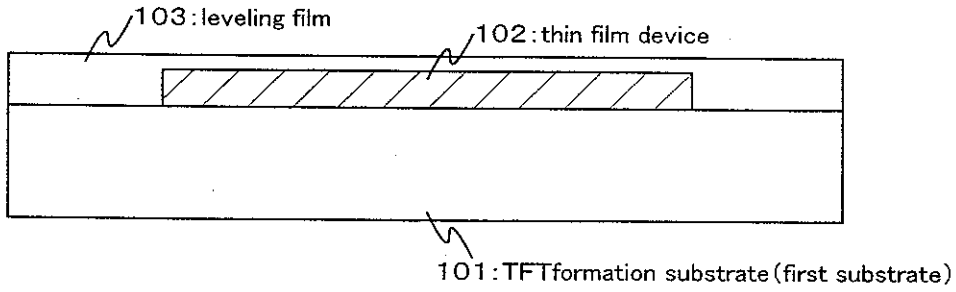


Fig. 1B

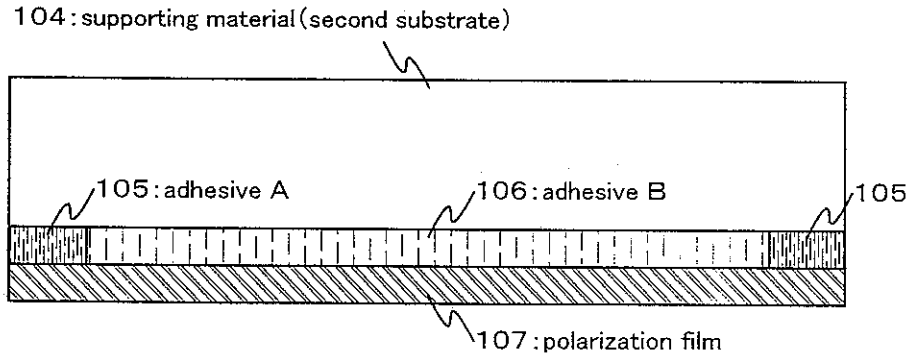


Fig. 1C

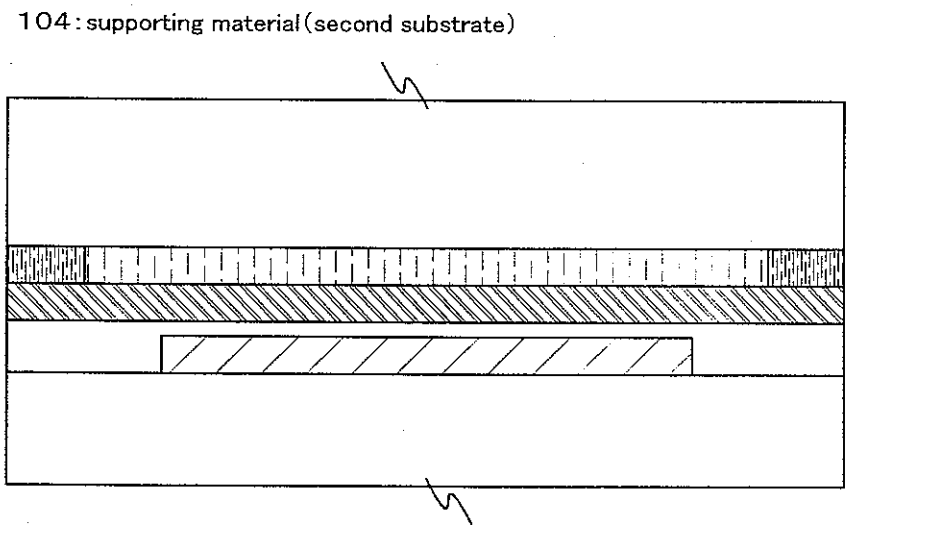


Fig. 1D

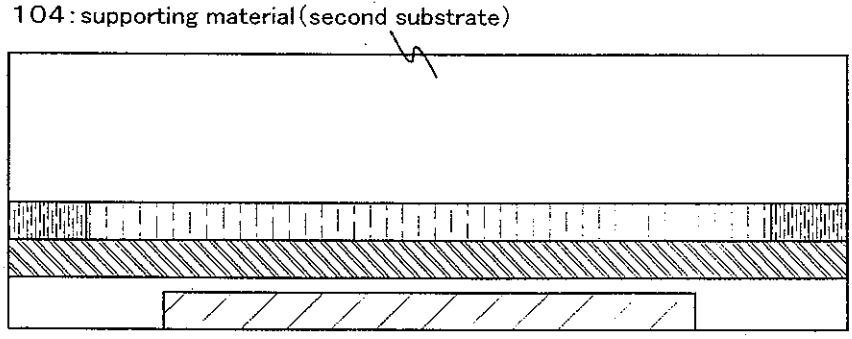


Fig. 2A

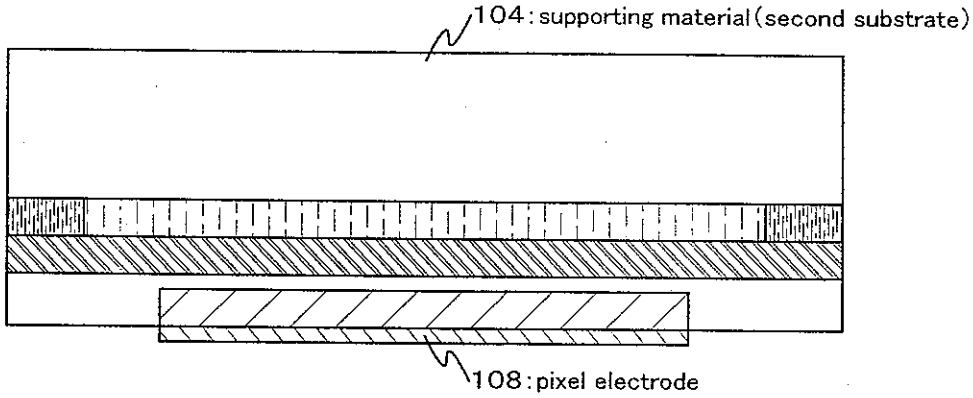

104: supporting material (second substrate)
108: pixel electrode

Fig. 2B

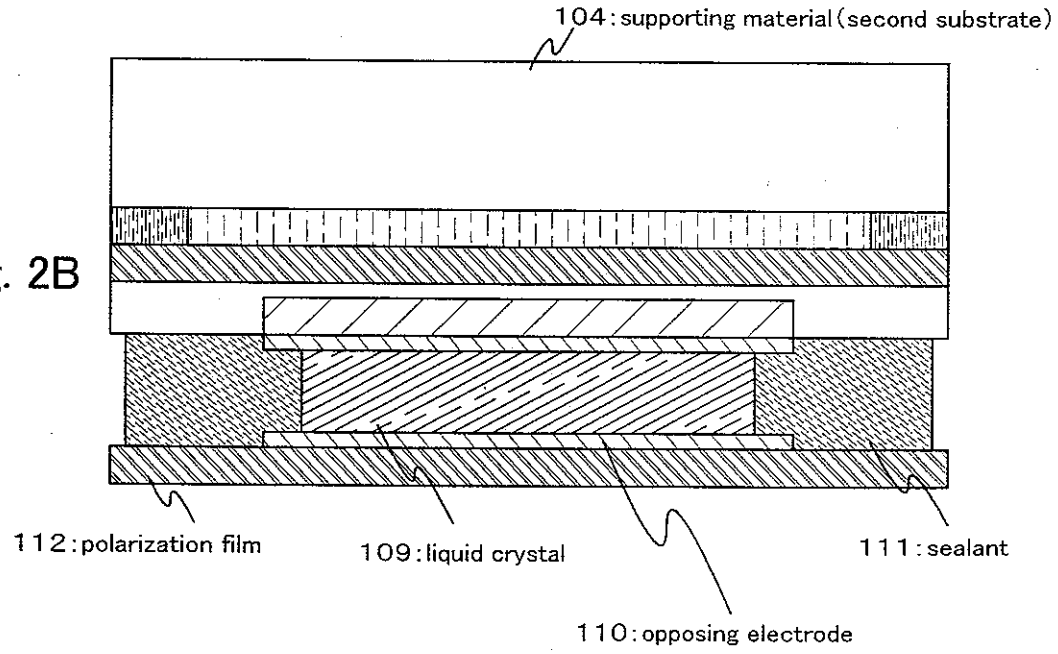

104: supporting material (second substrate)
112: polarization film
109: liquid crystal
110: opposing electrode
111: sealant

Fig. 3A

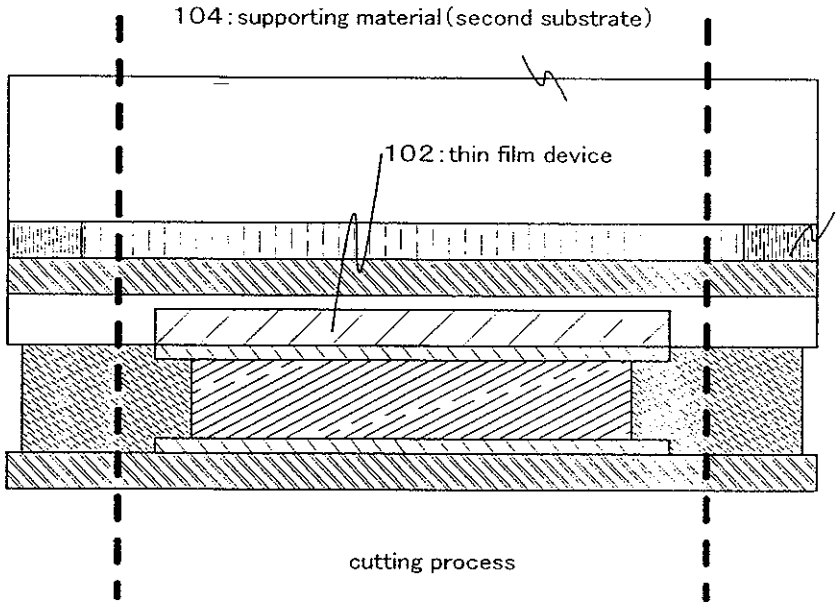


Fig. 3B

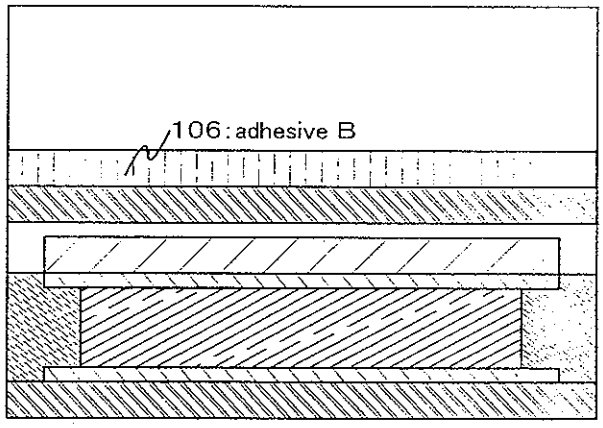


Fig. 3C

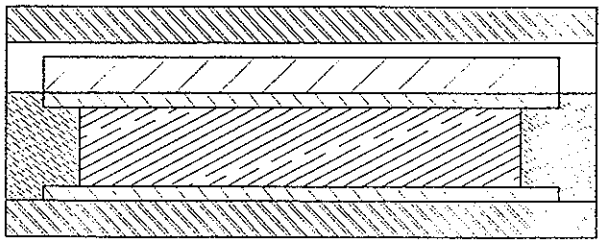


Fig. 4A

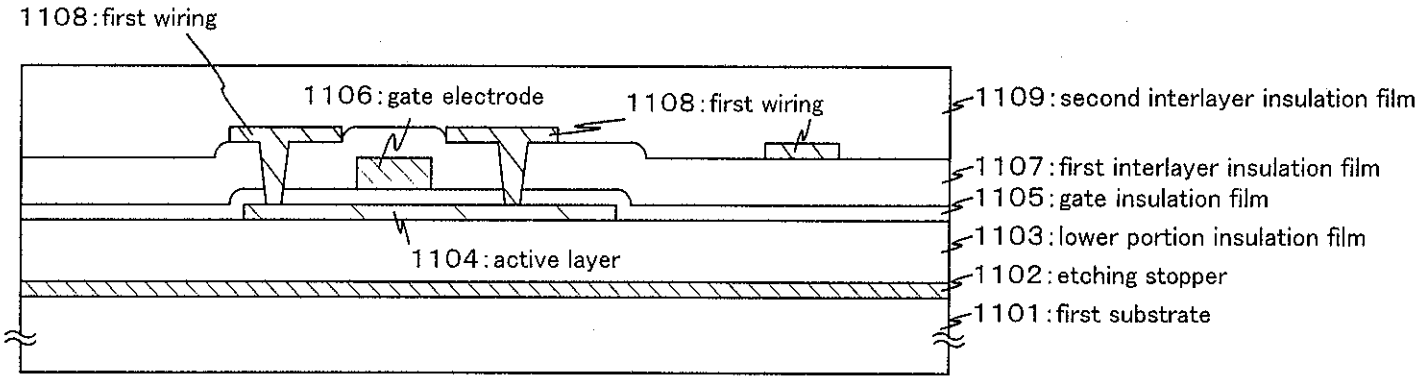
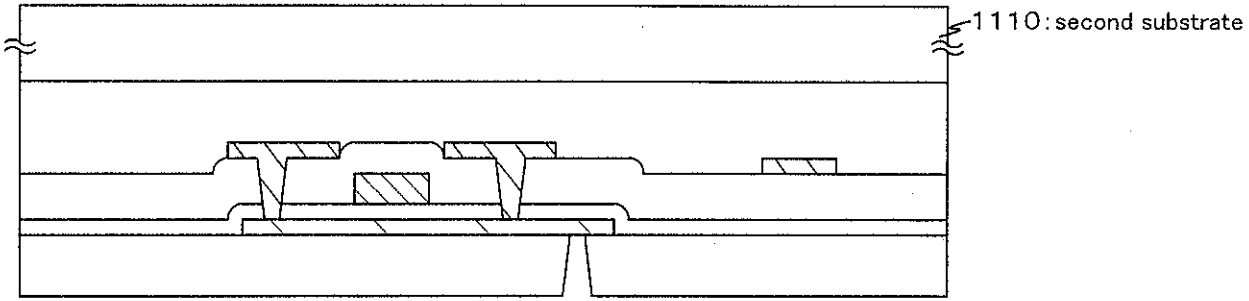

1108:first wiring
1106:gate electrode
1108:first wiring
1109:second interlayer insulation film
1107:first interlayer insulation film
1105:gate insulation film
1104:active layer
1103:lower portion insulation film
1102:etching stopper
1101:first substrate

Fig. 4B


1110:second substrate

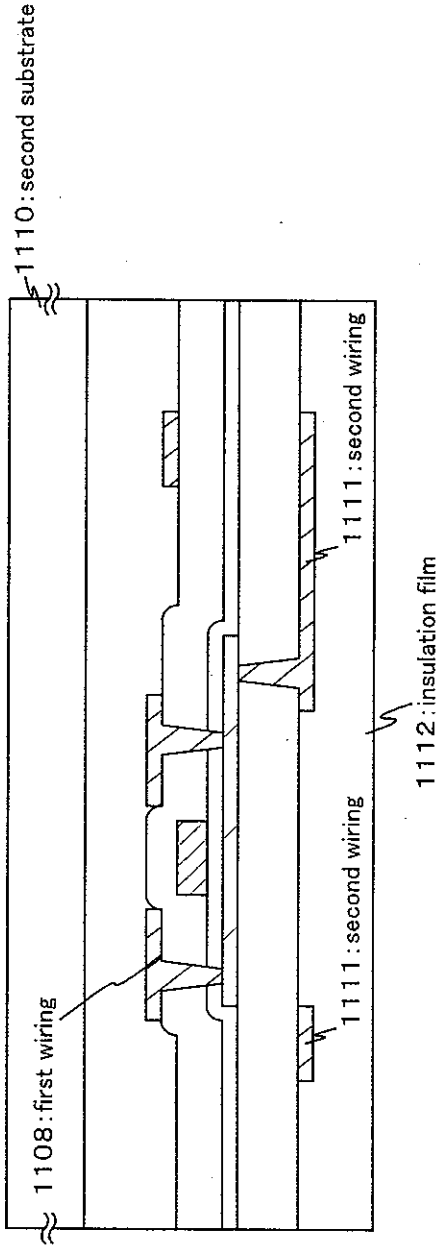

1110: second substrate
1108: first wiring
1111: second wiring
1111: second wiring
1112: insulation film

Fig. 5A

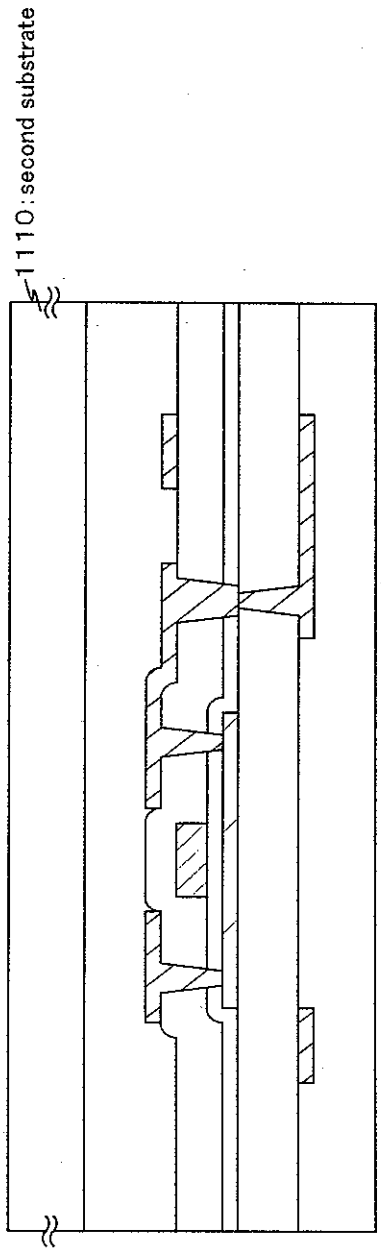

1110: second substrate

Fig. 5B

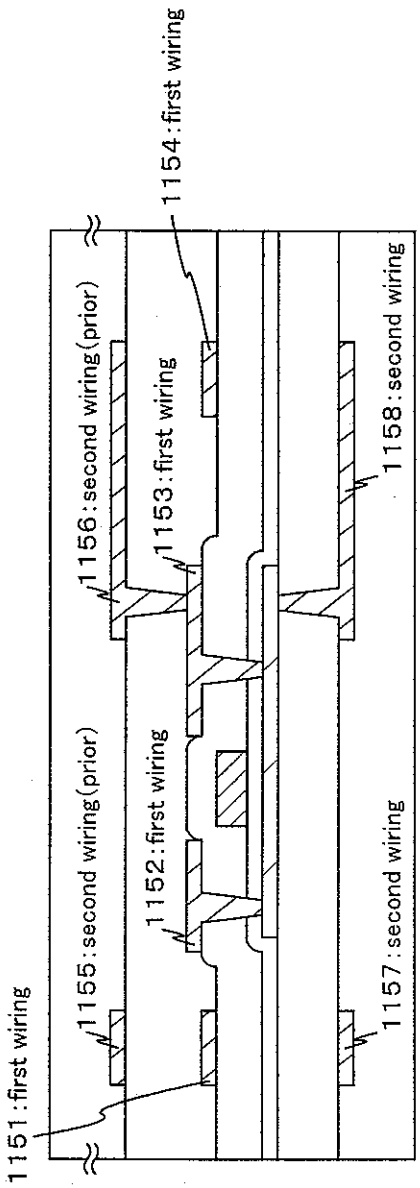

1151 : first wiring
1155 : second wiring (prior)
1152 : first wiring
1156 : second wiring (prior)
1153 : first wiring
1154 : first wiring
1157 : second wiring
1158 : second wiring

Fig. 6

Fig. 7A

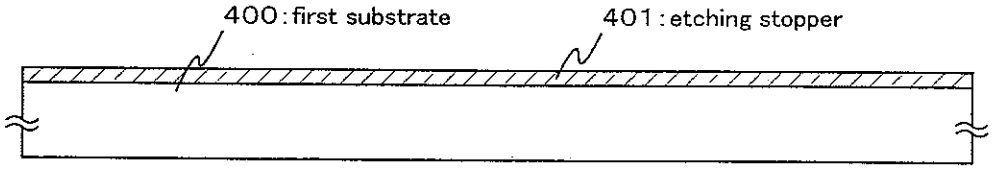


Fig. 7B

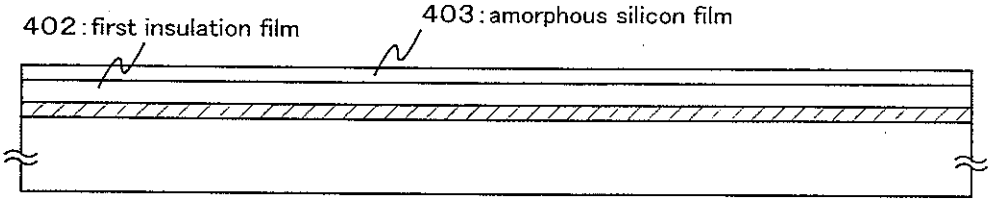


Fig. 7C

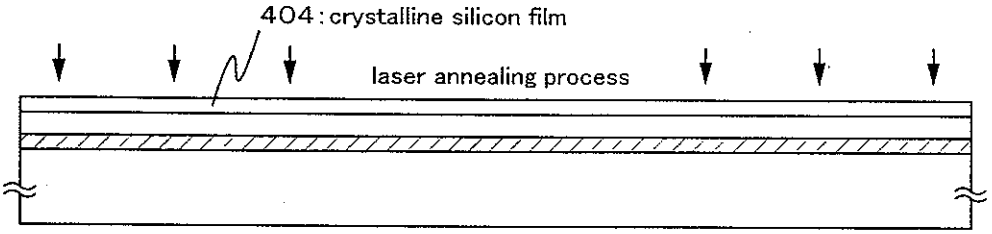


Fig. 7D

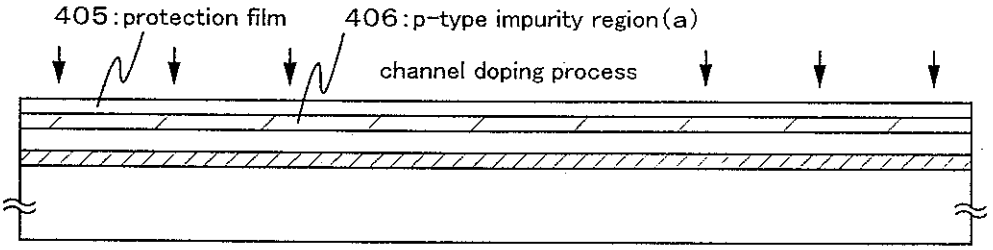


Fig. 7E

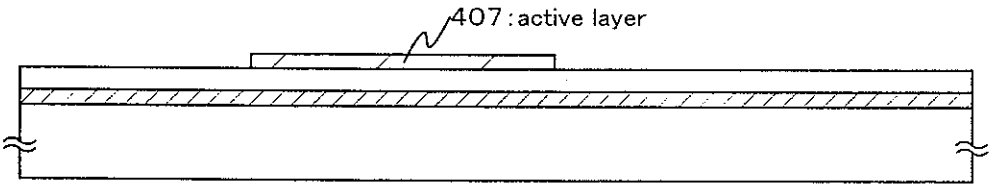


Fig. 7F

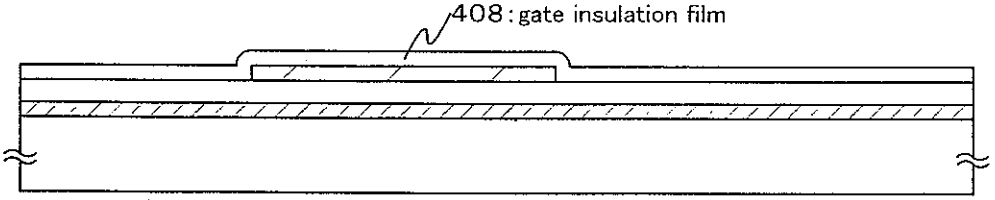


Fig. 8A

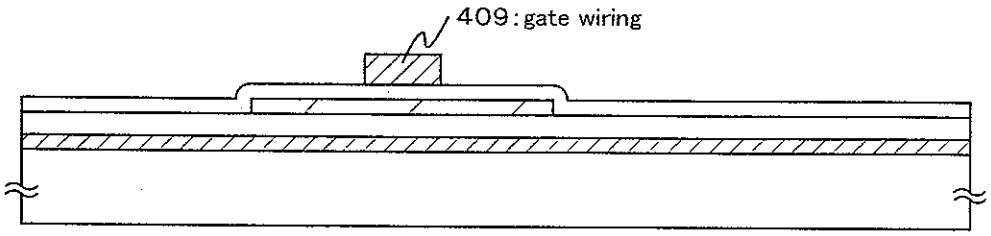


Fig. 8B

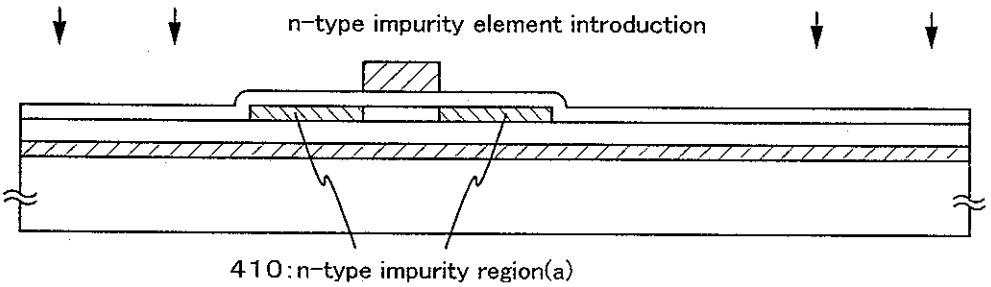


Fig. 8C

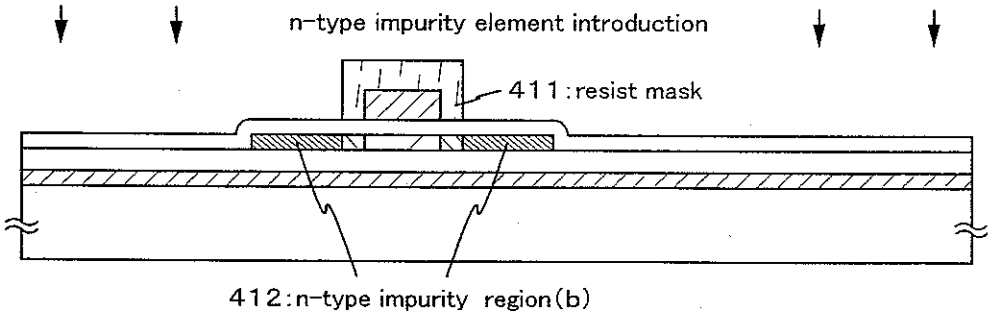


Fig. 8D

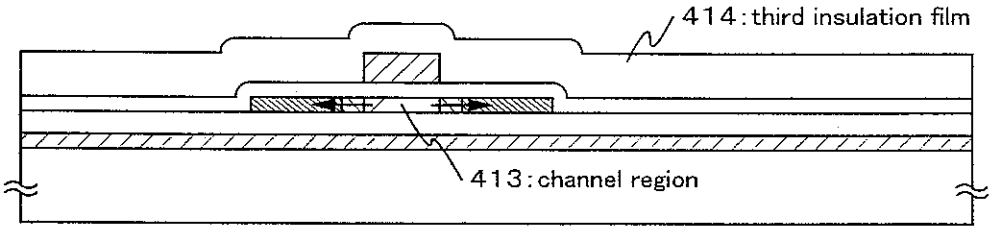


Fig. 9A

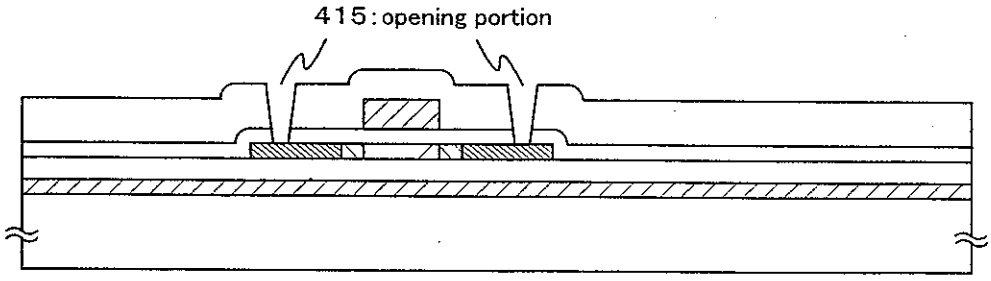


Fig. 9B

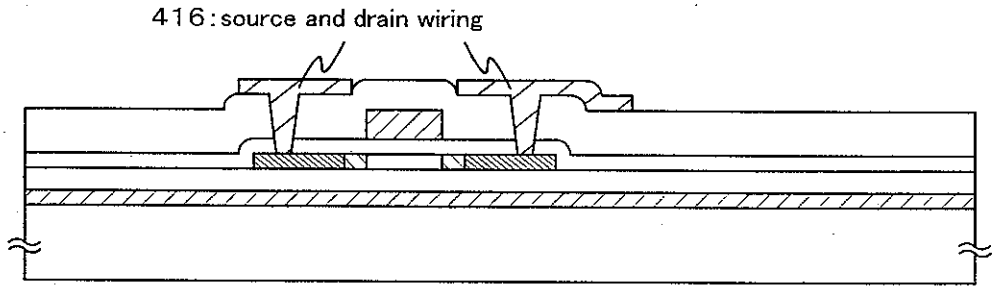


Fig. 9C

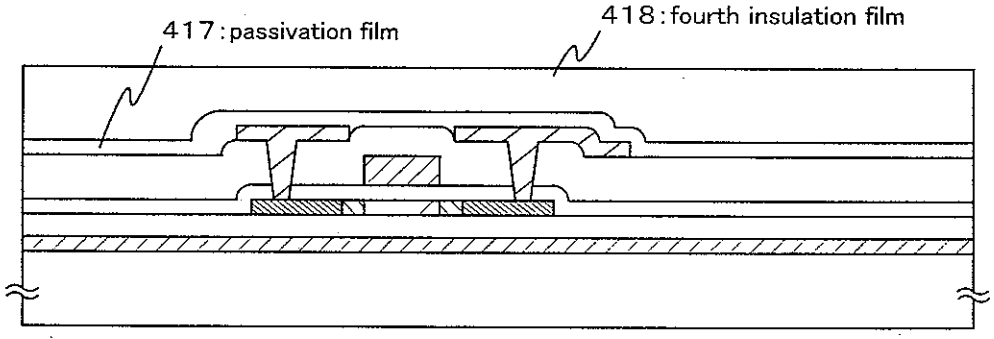


Fig. 9D

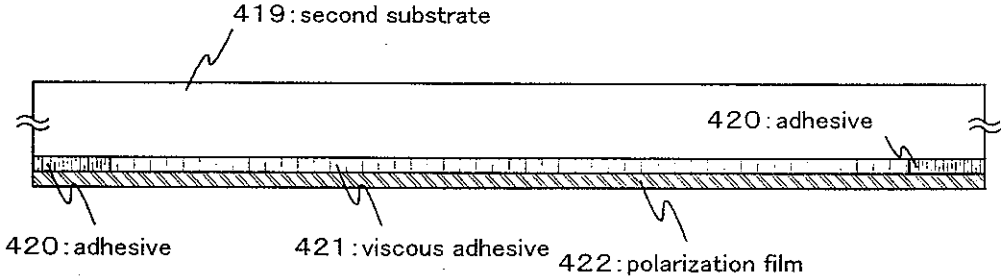


Fig. 10A

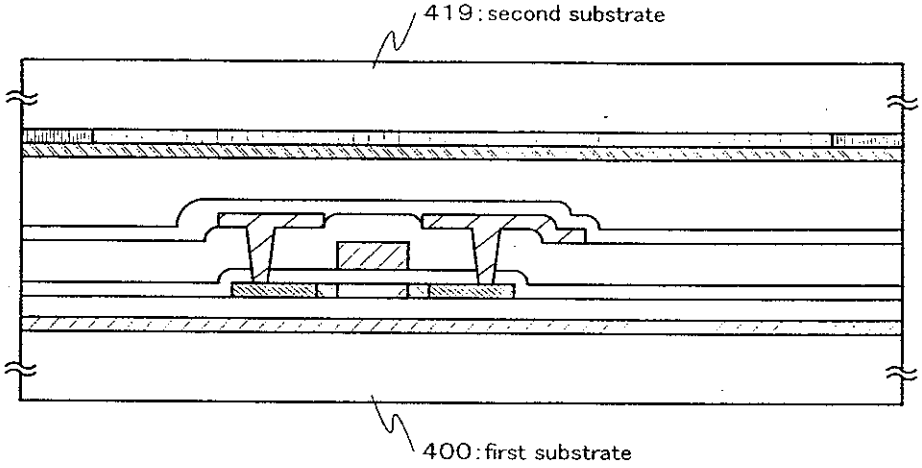


Fig. 10B

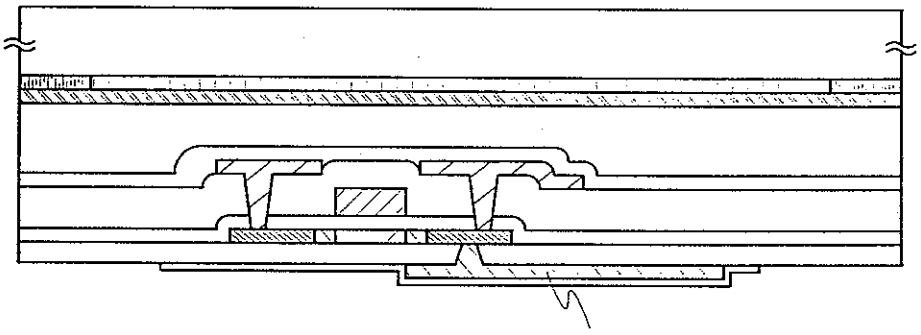


Fig. 10C

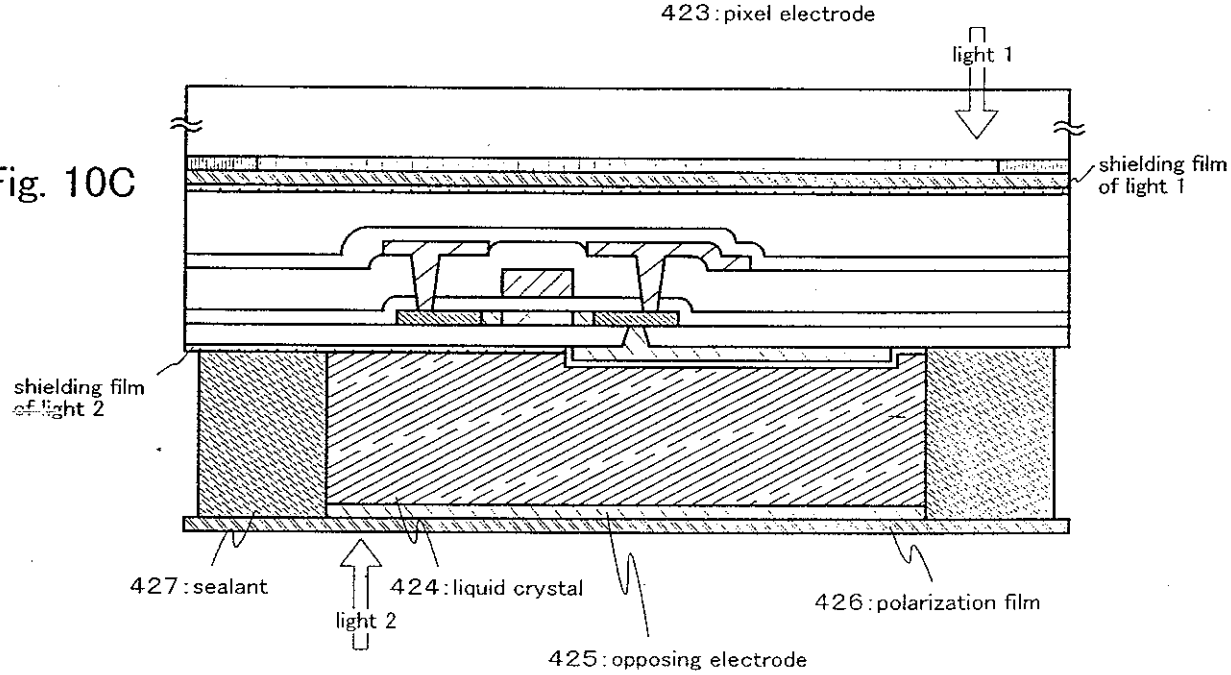


Fig. 11A

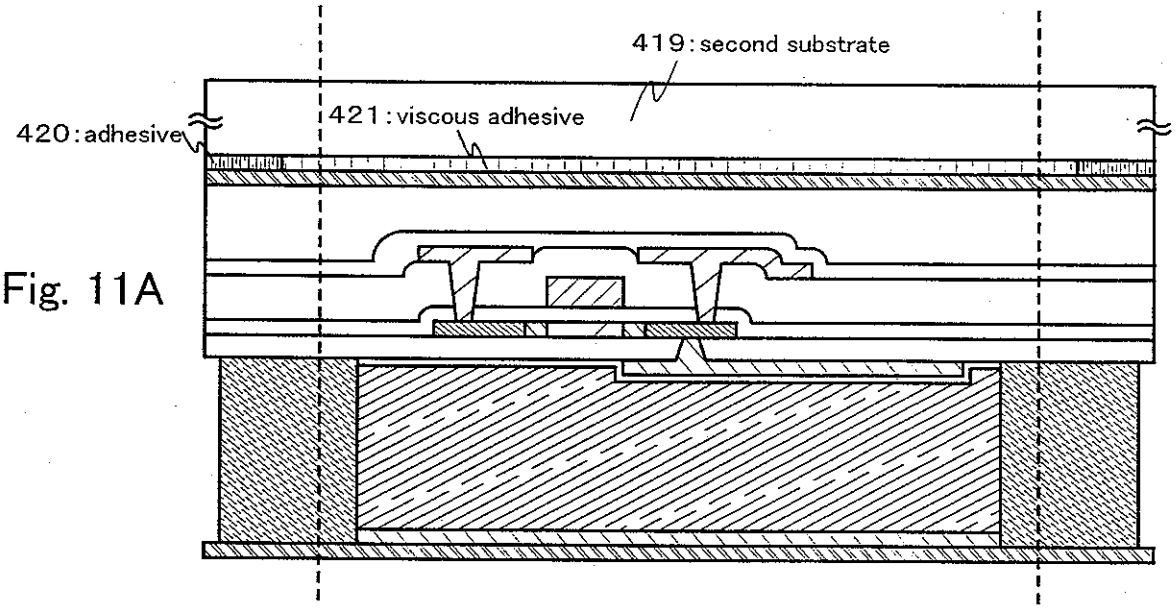
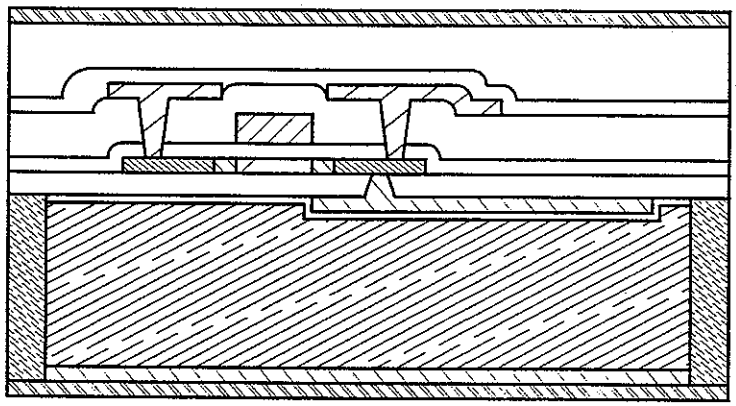


Fig. 11B



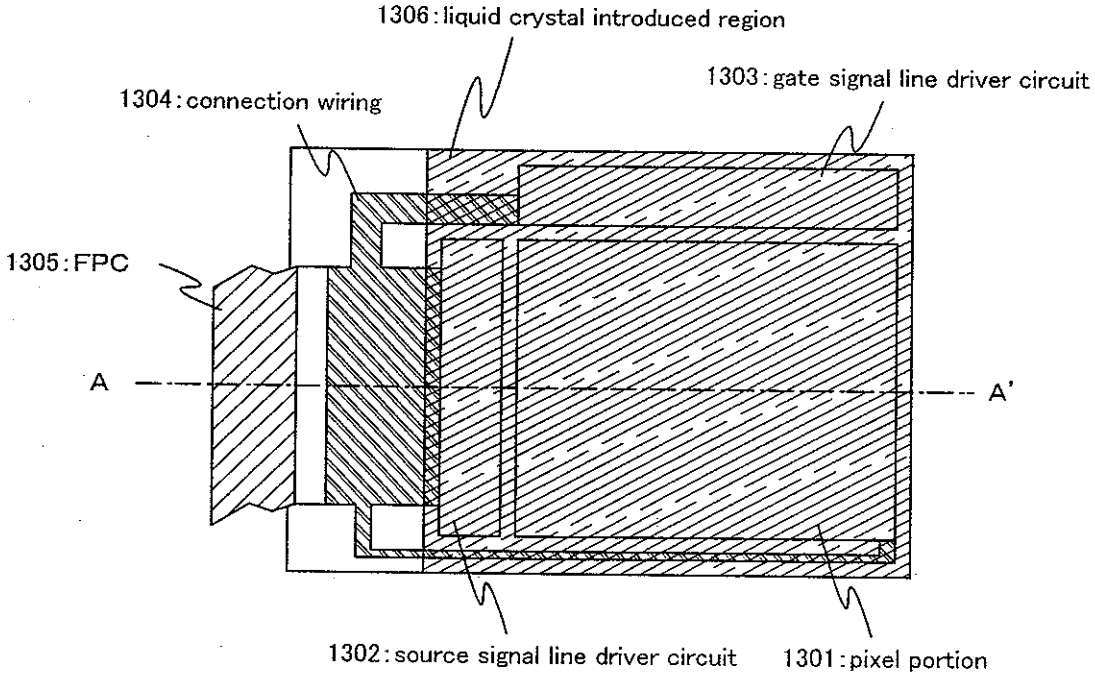

1306: liquid crystal introduced region
1304: connection wiring
1303: gate signal line driver circuit
1305: FPC
A
A'
1302: source signal line driver circuit
1301: pixel portion

Fig. 12

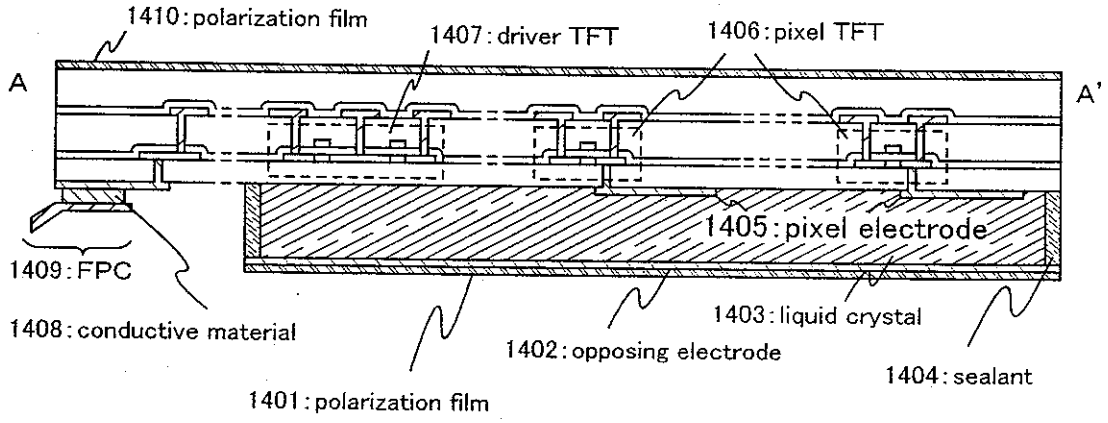

1410: polarization film
1407: driver TFT
1406: pixel TFT
A
A'
1405: pixel electrode
1409: FPC
1408: conductive material
1403: liquid crystal
1402: opposing electrode
1404: sealant
1401: polarization film

Fig. 13

Fig. 14A

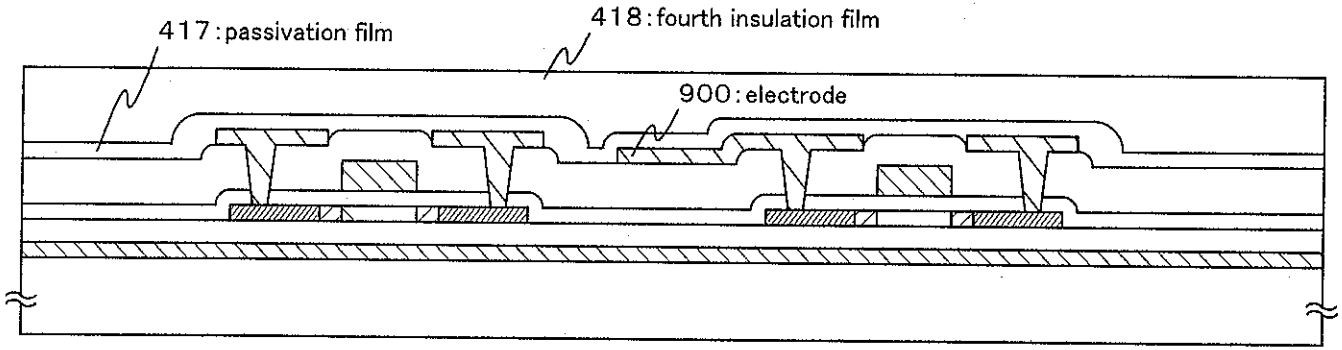


Fig. 14B

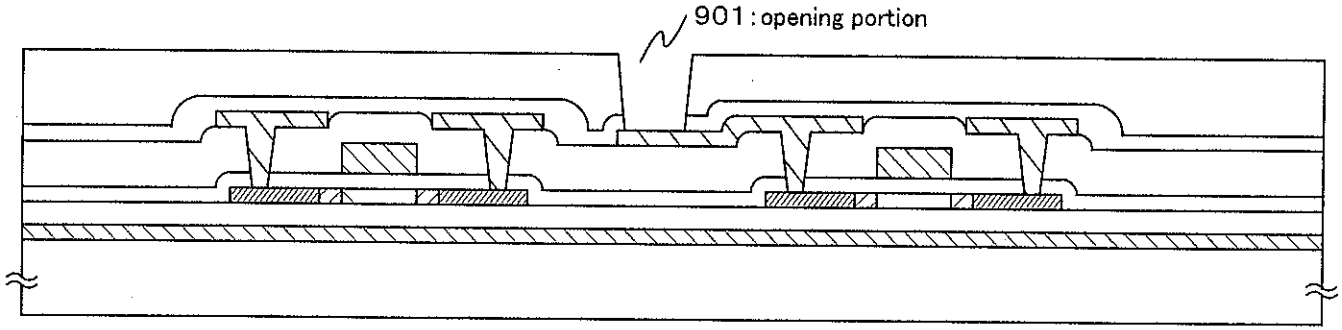
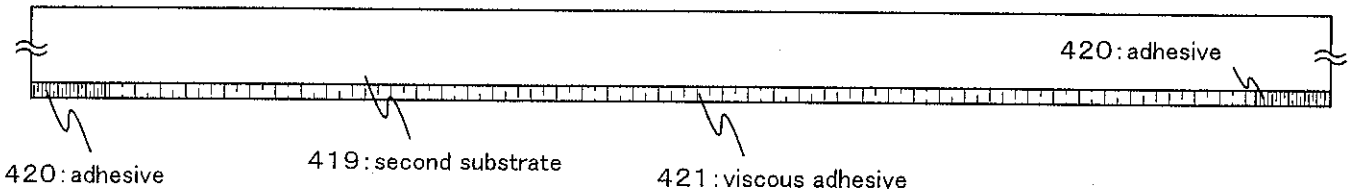


Fig. 14C



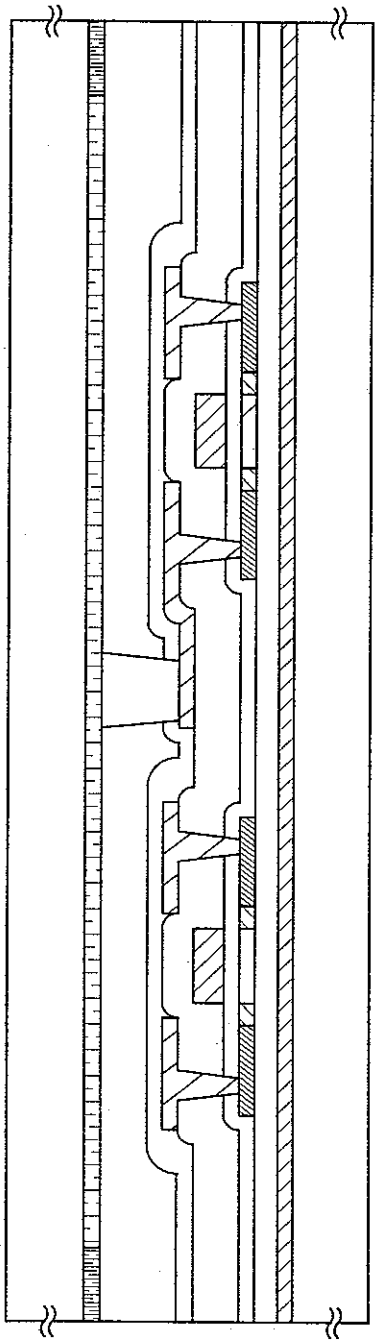


Fig. 15A

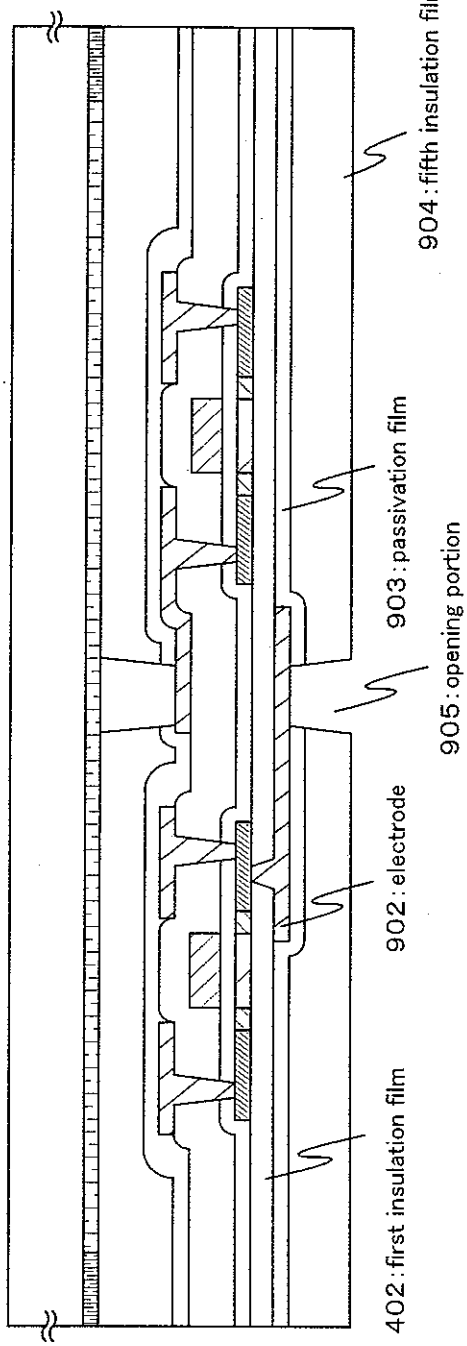


Fig. 15B

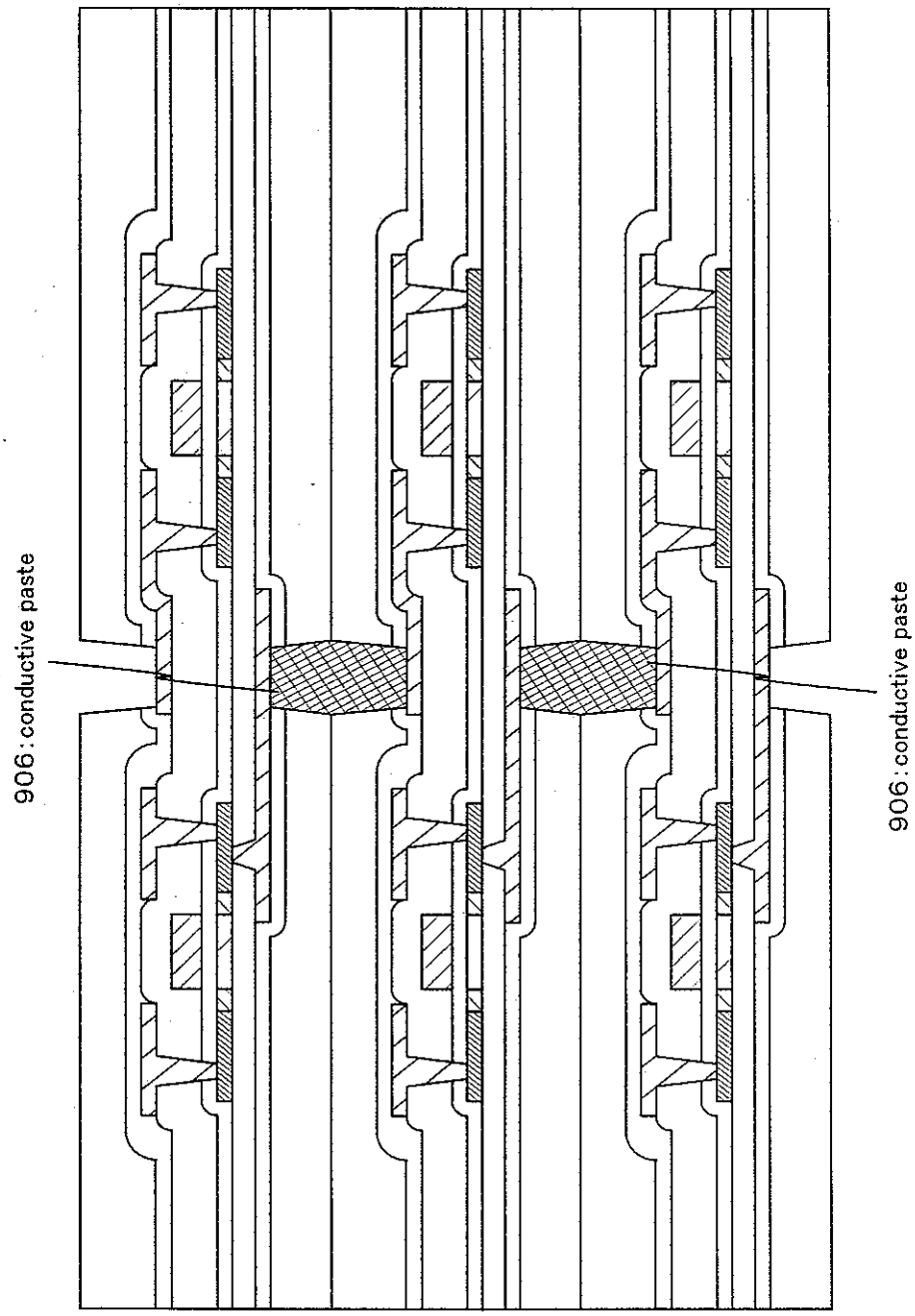


Fig. 16

Fig. 17A

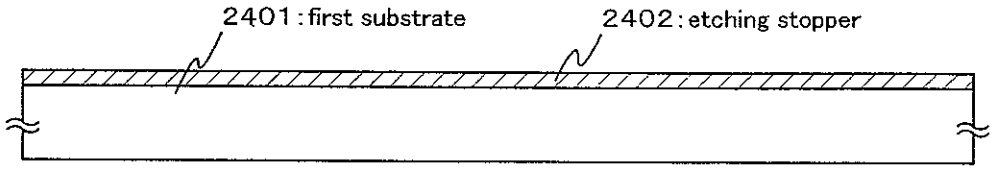


Fig. 17B

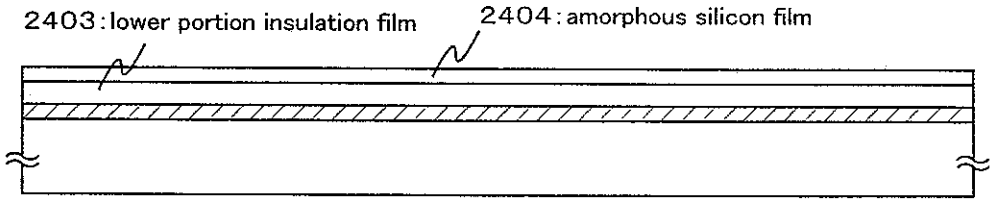


Fig. 17C

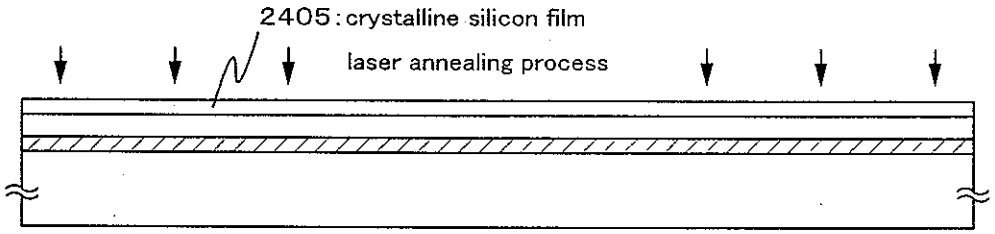


Fig. 17D

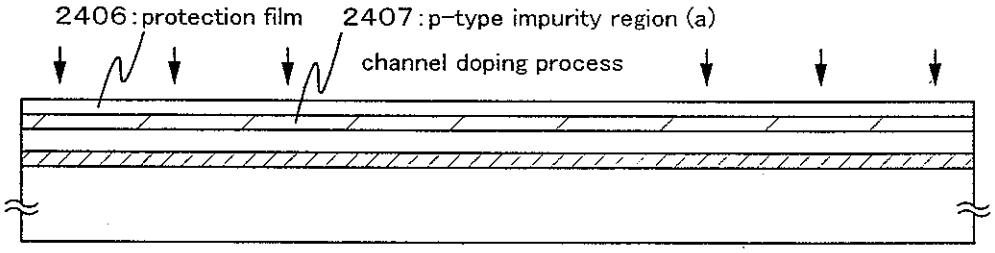


Fig. 17E

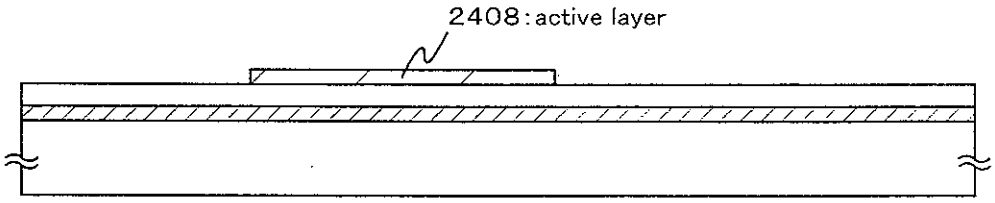


Fig. 18A

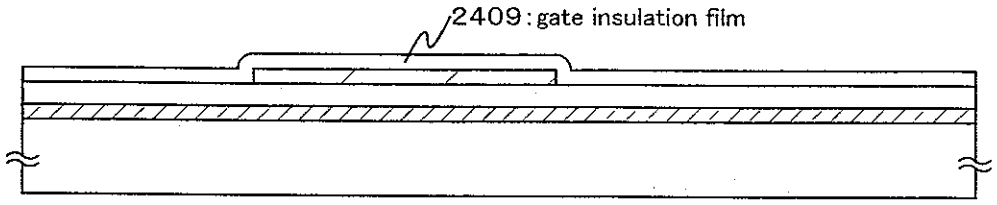


Fig. 18B

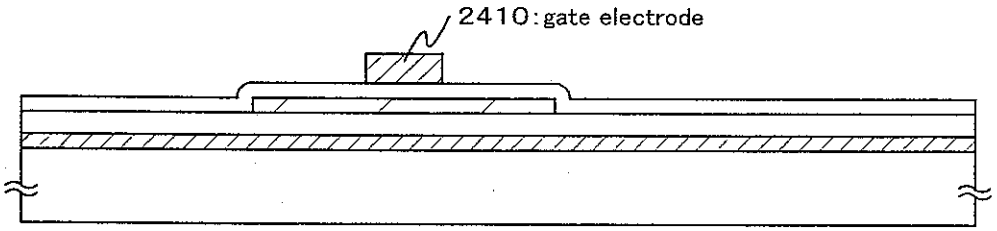


Fig. 18C

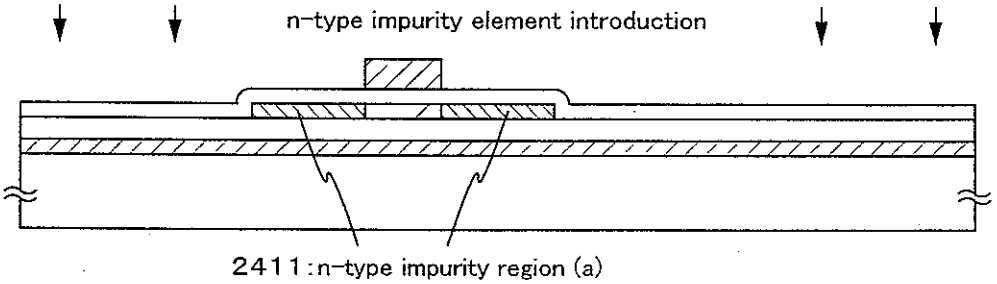


Fig. 18D

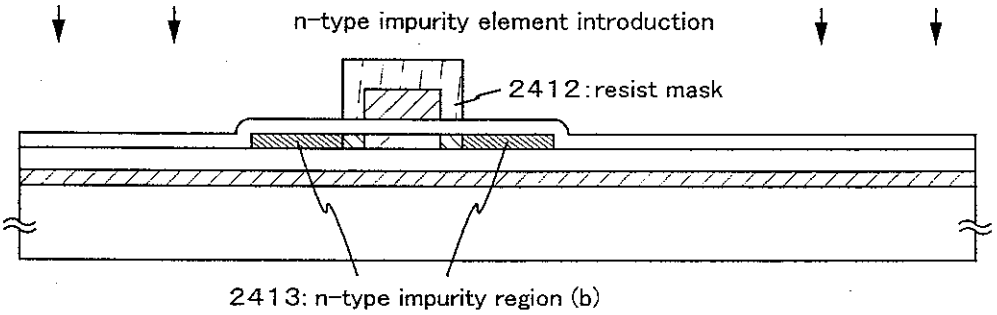


Fig. 19A

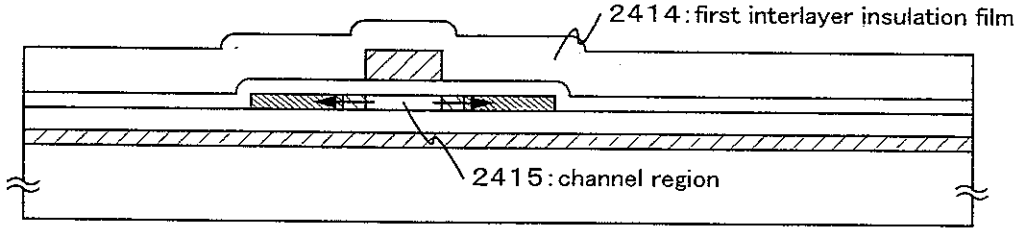


Fig. 19B

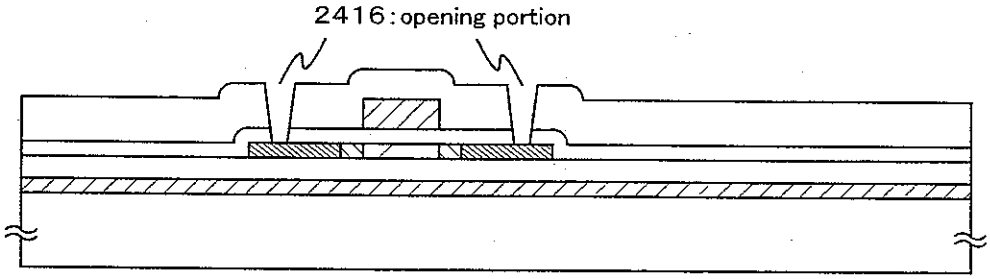


Fig. 19C

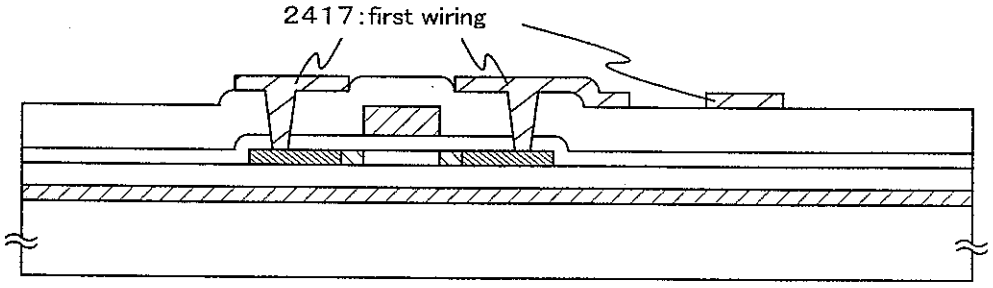


Fig. 19D

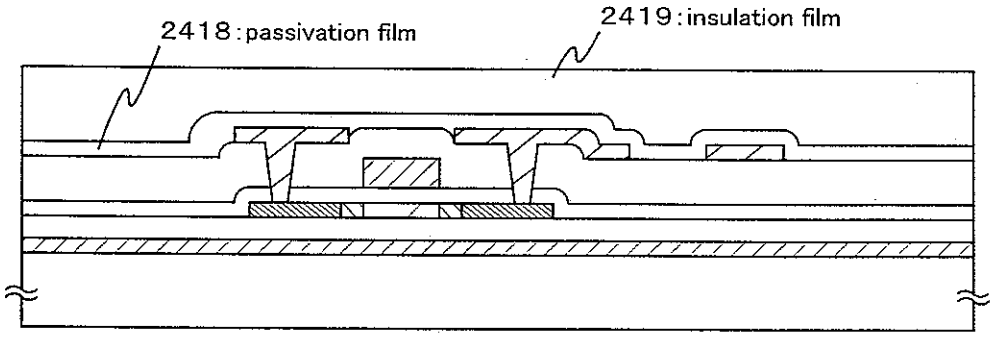


Fig. 20A

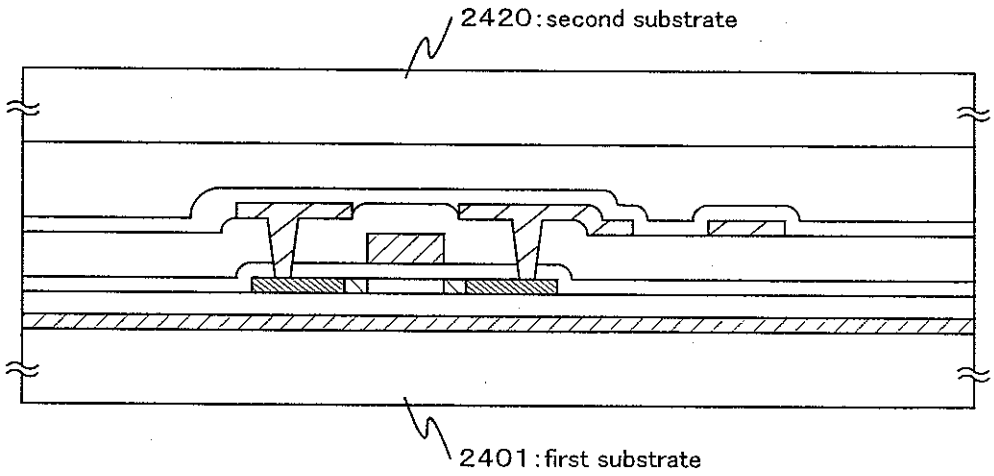


Fig. 20B

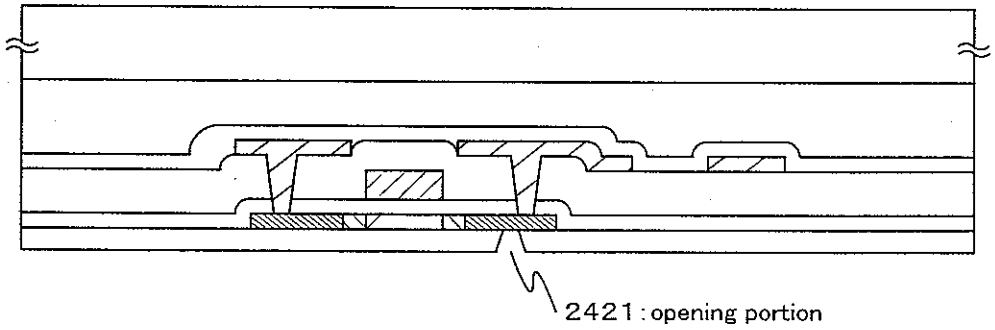
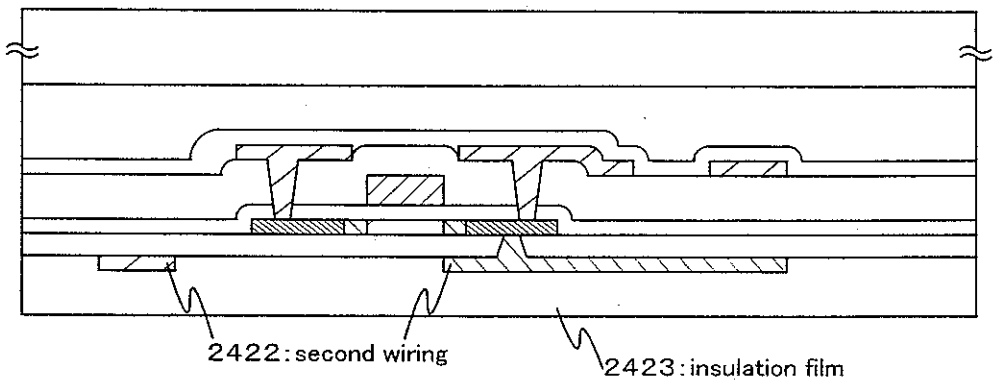


Fig. 20C



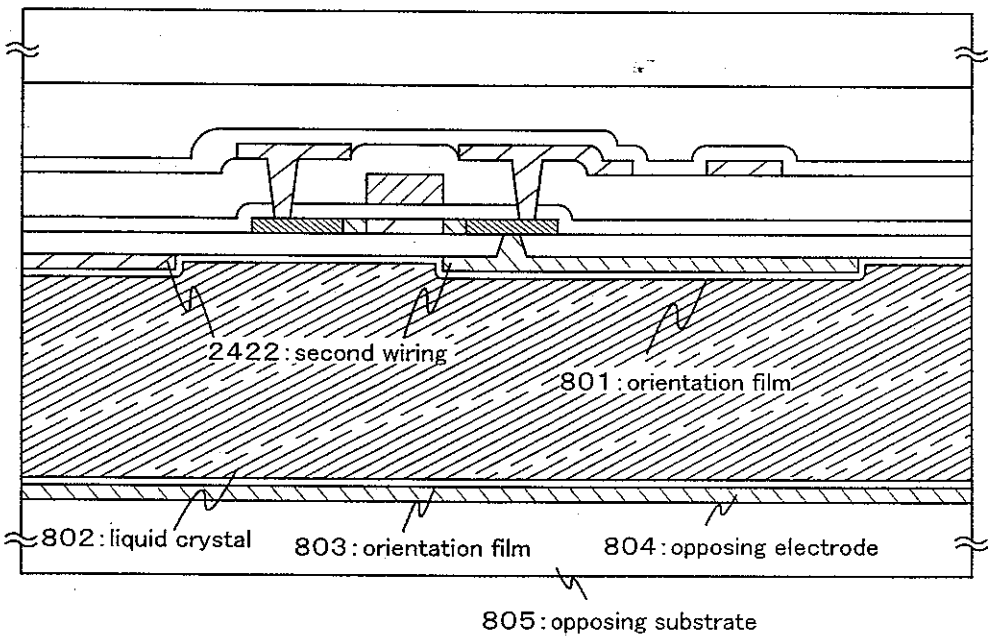

2422 : second wiring
801 : orientation film
802 : liquid crystal
803 : orientation film
804 : opposing electrode
805 : opposing substrate

Fig. 21

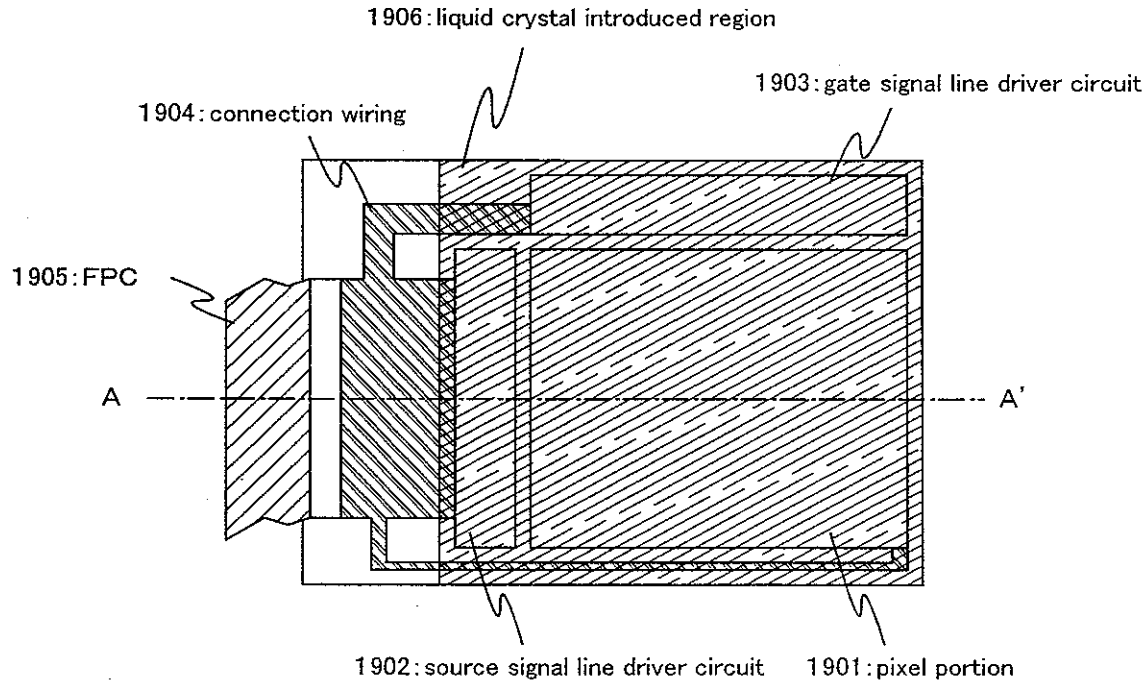

1906: liquid crystal introduced region
1903: gate signal line driver circuit
1904: connection wiring
1905: FPC
A
A'
1902: source signal line driver circuit
1901: pixel portion

Fig. 22

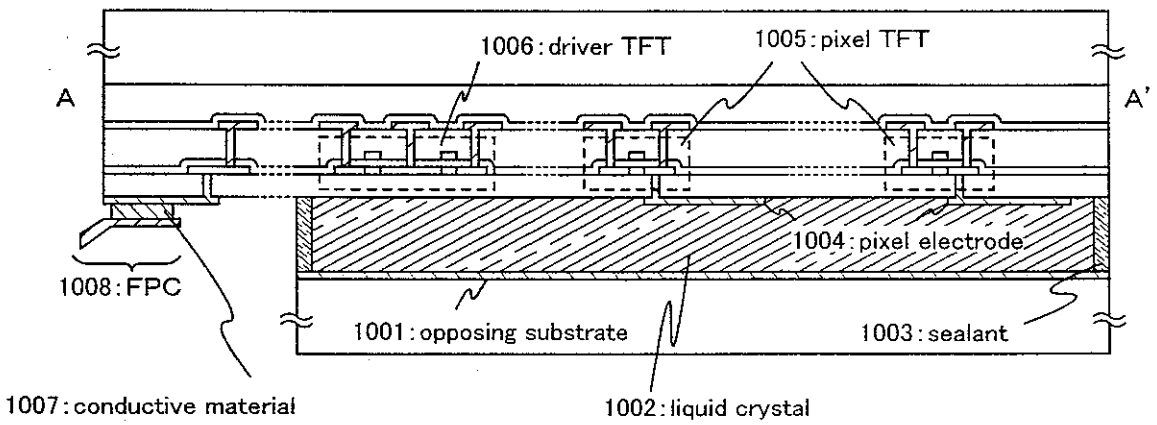

1006: driver TFT
1005: pixel TFT
A
A'
1004: pixel electrode
1008: FPC
1001: opposing substrate
1003: sealant
1007: conductive material
1002: liquid crystal

Fig. 23

Fig. 24A

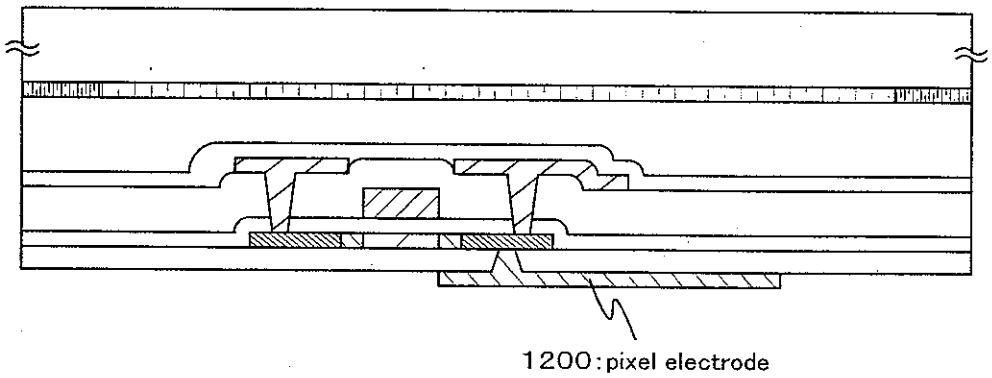


Fig. 24B

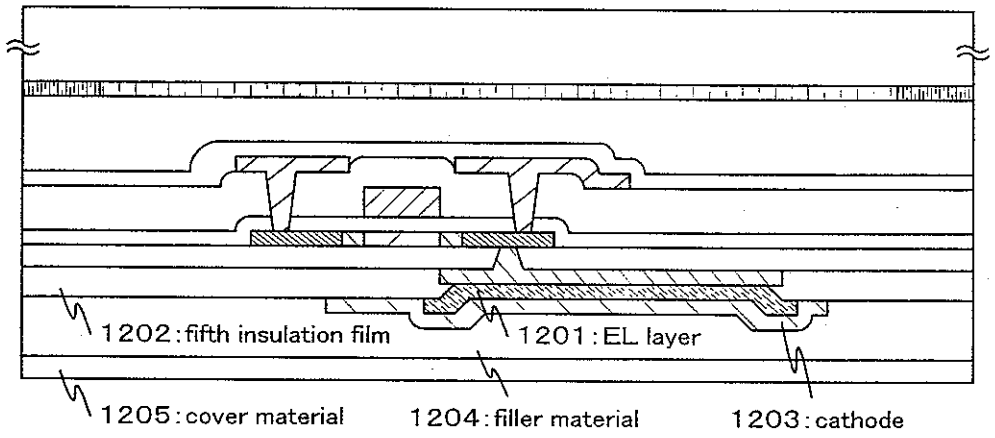
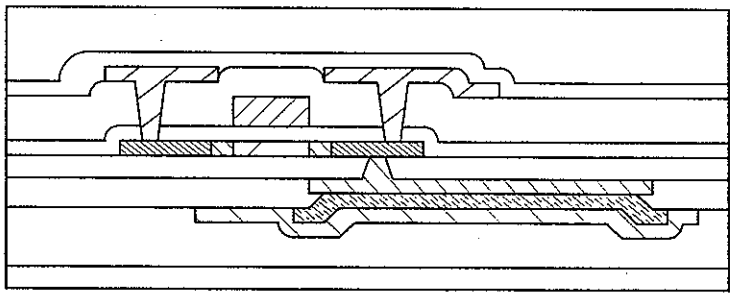


Fig. 24C



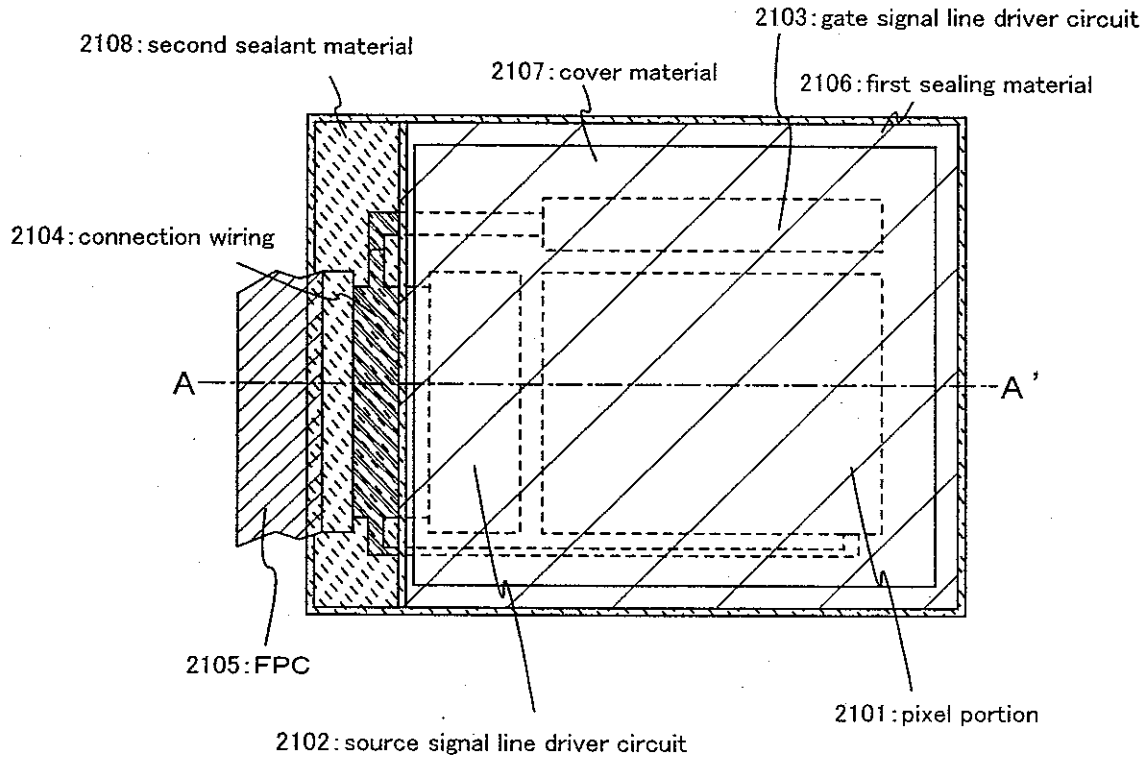

2103: gate signal line driver circuit
2108: second sealant material
2107: cover material
2106: first sealing material
2104: connection wiring
A
A'
2105: FPC
2101: pixel portion
2102: source signal line driver circuit

Fig. 25

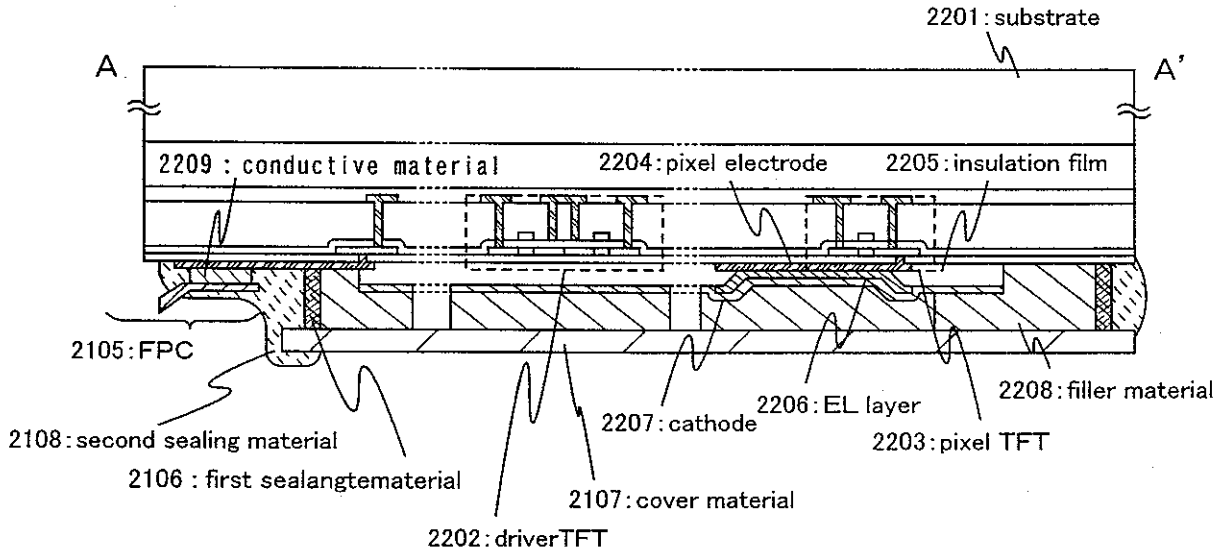

2201: substrate
A
A'
2209 : conductive material
2204: pixel electrode
2205: insulation film
2105: FPC
2208: filler material
2108: second sealing material
2207: cathode
2206: EL layer
2203: pixel TFT
2106 : first sealangtematerial
2107: cover material
2202: driverTFT

Fig. 26

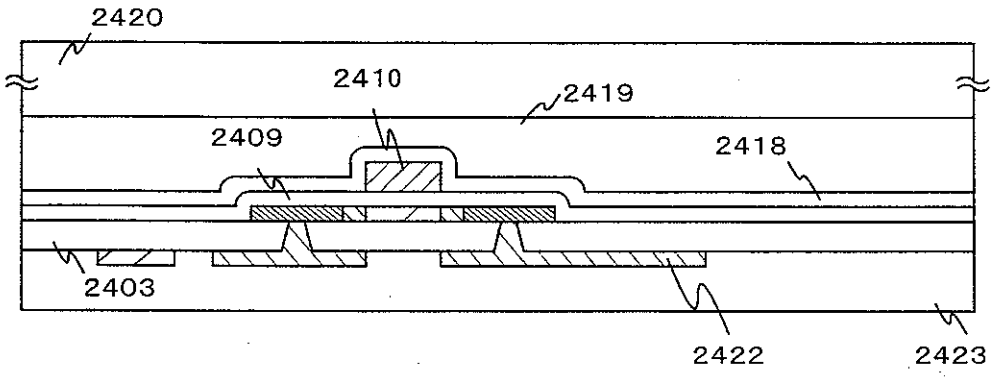


Fig. 27

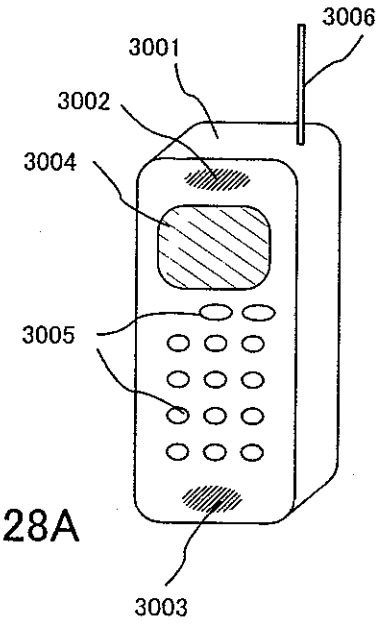


Fig. 28A

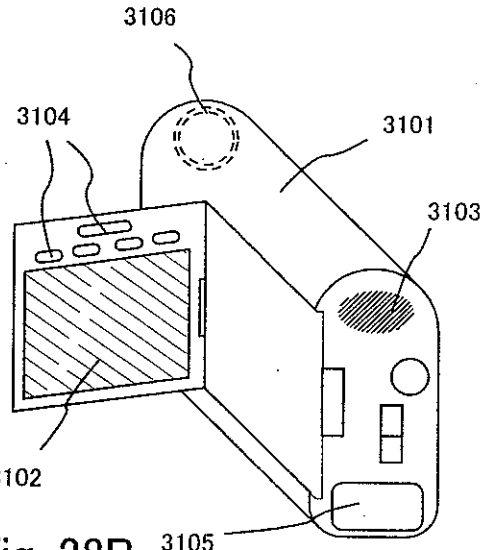


Fig. 28B

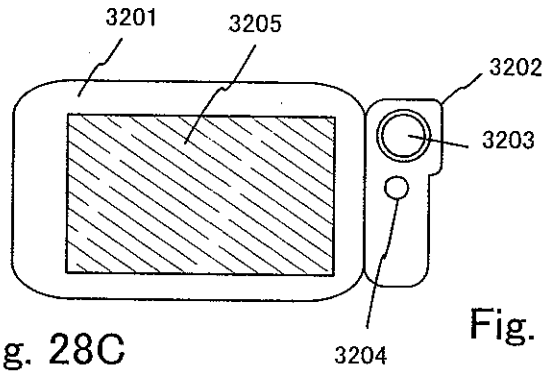


Fig. 28C

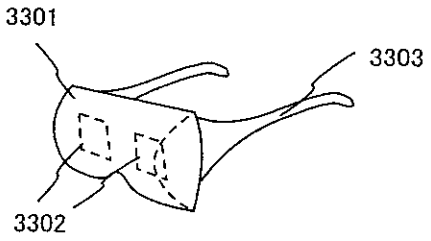


Fig. 28D

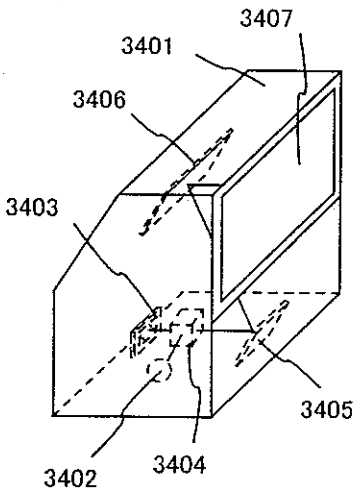


Fig. 28E

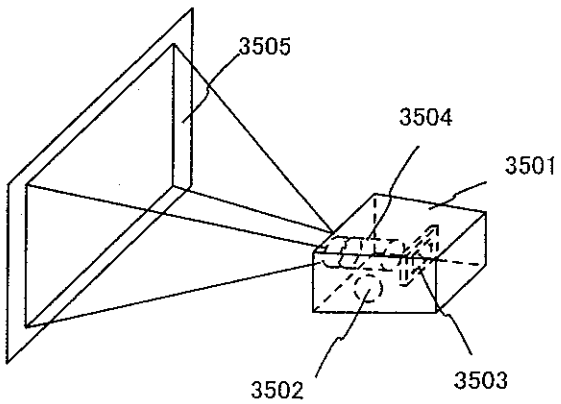


Fig. 28F