TRANSFER ACKNOWLEDGEMENT FOR A MOBILE SCALABLE LINK (MSL) ARCHITECTURE

RELATED APPLICATIONS

[0001] This Application is related to U.S. Patent Application No. TBD, Attorney Docket No. 42P20075, entitled "Providing Additional Channels for an MSL Architecture," having common Inventorship, and filed concurrently herewith.

FIELD

[0002] Embodiments of the invention relate to interprocessor communication, and specifically to a transfer acknowledgement mechanism in a Mobile Scalable Link (MSL) architecture.

BACKGROUND

[0003] Traditional interprocessor communication following the Mobile Scalable Link (MSL) protocol is limited to the channel definitions of the MSL specification. Specifically, four bits are available under the MSL architecture to specify a channel, and all 16 potential channels are defined in MSL2. Additionally, traditional MSL data transfer is unidirectional, providing only for a transmitting device to send data, and not providing a mechanism for the receiving device to acknowledge that the data was properly received.

BRIEF DESCRIPTION OF THE DRAWINGS

[0004] The description of embodiments of the invention includes various illustrations by way of example, and not by way of limitation in the figures and accompanying drawings.

[0005] **Figure 1** is a block diagram of a system employing an interprocessor communication architecture in accordance with an embodiment of the invention.

[0006] Figure 2 is a block diagram of a channel definition of an interprocessor communication specification in accordance with an embodiment of the invention.

[0007] Figure 3 is a block diagram of a communication bus of a system employing an interprocessor communication specification in accordance with an embodiment of the invention.

[0008] Figure 4 is a block diagram of a timing diagram for communication in accordance with an embodiment of the invention.

[0009] Figure 5 is a flow diagram of transmission of an interprocessor message in accordance with an embodiment of the invention.

[0010] Figure 6 is a flow diagram of an acknowledgement of an interprocessor message transfer in accordance with an embodiment of the invention.

[0011] Figure 7 is a block diagram of a portable device having processors with a physical interconnect in accordance with an embodiment of the invention.

[0012] Figure 8 is a block diagram of an electronic device having processors with a physical interconnect and a memory subsystem in accordance with an embodiment of the invention.

DETAILED DESCRIPTION

[0013] Various references herein to an "embodiment" are to be understood as describing a particular feature, structure, or characteristic included in at least one embodiment of the invention. Thus, the appearance of phrases such as "in one embodiment," or "in alternate an embodiment" may describe various embodiments of the invention, and may not necessarily all refer to the same embodiment.

[0014] Figure 1 is a block diagram of one embodiment of a system employing an interprocessor communication architecture. System 120 may include multiple devices,

apparatuses, machines, etc., in communication over communication bus 110. System 120 represents various types of devices that may have multiple devices on an interconnect, including cellular telephones, personal digital assistants (PDAs), wireless communication devices, portable computing devices, etc. System 120 may also represent other devices, for example, digital watches, stereo systems, cameras, etc.

[0015] Communication bus 110 represents one or more signal lines between the various devices of system 120. In one embodiment communication bus 110 may represent the physical interconnect among the various devices. In one embodiment communication bus 110 may include data lines and sideband signal lines. For example, communication bus 110 may be a Mobile Scalable Link (MSL) or MSL2 multi-drop bus.

[0016] Data lines that may be present on communication bus 110 provide a communication interface over which several logical channels may exist. In one embodiment the channels may be multiplexed over the physical lines. Communication may be specified to operate over channels that are broadcast channels and others that are non-broadcast, or device-specific. For example, a protocol may specify two or more header fields, one of which is an address or other identifier (generically, an ID, or a device specific ID), and a channel number. When these fields are placed on the data lines, the channel corresponding to the number is activated, and the identified device(s) are indicated to receive the data payload.

[0017] One or more channel numbers may be defined to be broadcast channels, and one or more channel numbers may be defined to be non-broadcast, or device-specific channels. Besides having a particular channel number definition, in one embodiment a difference between a broadcast channel and a non-broadcast channel is the manner in which the channel is invoked. For example, a broadcast channel may be specified by a broadcast channel number and a broadcast ID, or some identifier defined to indicate that all devices

on communication bus 110 should listen. A non-broadcast channel may be a channel for which a channel number is defined as a device-specific channel, and a device-specific ID is placed on the data lines. Thus, a broadcast channel may provide communication from one device to multiple, or all other devices on communication bus 110, and a non-broadcast channel may provide communication from a transmitting device to a specific device matching the ID number.

[0018] Sideband lines are to be understood as one or more signal lines that may be present between two or more devices, and which may be asserted (set high (logic one) for active-high lines, and set low (logic zero) for active-low lines) to provide information between two or more communicating devices independent of, in conjunction with, in parallel with, the data lines. Sideband lines may be lines to indicate, for example, a ready to send signal, a wait signal, a system clock line, etc.

[0019] In one embodiment communication bus 110 may be controlled through arbitration. In an arbitration scheme, a single device at a time may have control of the bus. Specifying that a device has "control" of the bus may refer to the data lines and the side band lines. Alternatively, specifying that a device controls the bus may refer simply to the device having the right to communicate on one or more of the physical lines of communication bus 110. In one embodiment arbitration may be used to designate a master and a slave device on the bus. The master may have the right to place a message (e.g., an ID, a channel number, a data byte, etc.) on the data lines. The master device is thus the device designated with an opportunity to transmit. A slave does not have a right to place a message on the data lines while a master controls the bus. A slave may thus be a device designated to receive, or a device that is not receiving communication, but should wait until a communication is sent to the device, or the device has an opportunity to transmit. However, in one embodiment one or more sideband signals may be asserted by a receiving

device or a slave device even when the bus is controlled by a master. The devices may alternate control of the bus, or the data lines, for each to have an opportunity to transmit.

[0020] Thus, device 100 may control the bus, and may be considered the master. In one embodiment device 100 represents a subsystem, a circuit, a processor, etc. As a processor, device 100 may represent a general-purpose processor in system 120, an applications processor (e.g., a device that has access and/or control of one or more application level programs/code executions on system 120), etc. In one embodiment a high-level processor in system 120 may begin as the bus master, and may generate the clock signal for communication bus 110. The high-level processor may alternate as a slave on communication bus 110 during operation of system 120. More or fewer devices may be present in system 120 than those shown.

[0021] Devices 101-103 represent other circuits, subsystems, processors, logic arrays, peripherals, external servers, etc. Each device 101-103 may represent the master device at some point of operation/execution of system 120. At other times, these devices represent slave devices attached to communication bus 110. If Figure 1 were to be considered a snapshot in execution of system 120, these devices may currently be considered to be the slave devices, and would receive a message and/or remain idle until such time as they receive a message and/or transmit.

[0022] In one embodiment communication bus 110 may support a broadcast channel and a non-broadcast channel, with the broadcast channel being specified by a particular channel number and a broadcast ID, and the non-broadcast channel being specified by another, different particular channel number and a device-specific ID. In one embodiment the broadcast ID is hexadecimal "F". In one embodiment the device-specific ID is a number of the range 0-7. In one embodiment the number of channels may be, for example, 8, 16, 128, etc. The number of channels may depend on the number of bits used to specify the

channel number. For example, a channel number identifier of one nibble (four bits) has the potential to identify 16 different channels. Suppose that all potentially available channels are defined as either broadcast or non-broadcast channels. If all channels are defined, there would traditionally be no channels reserved for future use.

[0023] In one embodiment, a system with all potential channels defined as either broadcast or non-broadcast channels may employ a mechanism to define more channels within the same address space. For example, if a broadcast channel is defined, a device-specific ID may be used in conjunction with the broadcast channel. Similarly, a broadcast ID may be used with a non-broadcast channel. Thus, whether a channel is a broadcast channel or a non-broadcast channel could in one embodiment be changed from being a function of the specified channel number to a function of the ID number placed on the data channels. A number of extra broadcast channels up to the number of protocol-specified non-broadcast channels may be created and employed for uses other than what may be defined in the specification/protocol definition. Similarly, a number of extra non-broadcast channels up to the number of broadcast channels may be created and employed for uses other than what may be defined in the specification/protocol definition.

[0024] **Figure 2** is a block diagram of one embodiment of a channel definition of an interprocessor communication specification. In one embodiment an interprocessor communication definition, may define channels that employ a device-specific address and channels that employ a broadcast address. In one example, MSL defines 16 channels, with channels 8, 9, and 15 dedicated to broadcast use, and the remaining channels for device-specific message transfer. Traditional MSL does not allow for channels beyond the 16 defined channels.

[0025] In one embodiment a device-specific address is employed with one of channels 8, 9, and/or 15 to provide an additional device-specific message transfer mechanism. In

Figure 2 these are specified as reserved for future use (RFU) channels. These channels are reserved because the MSL specification does not define them. Because they are not defined, a system designer may define these channels for a custom, specific application or use.

[0026] In one embodiment a broadcast address is employed with one of channels 0-7 and/or 10-14 to provide an additional broadcast message transfer mechanism. Again, these channels may be custom defined for system broadcast information. For example, custom system status, messages, etc., may be useful for certain devices in a system.

[0027] Because these channels are not defined in traditional MSL, a system using one or more RFU channels may still be backwards compatible with a traditional system. For example, because a broadcast address is defined as being used with broadcast channel 8, if an address other than a broadcast address is specified for the broadcast channel, the devices in the system may ignore the message. The device-specific address used may be for a device that will listen on the broadcast channel and decode the message when its own device ID is the address specified on the broadcast channel.

[0028] **Figure 3** is a block diagram of one embodiment of a communication bus of a system employing an interprocessor communication specification. Applications processor 310 may be a processor, subsystem, gate array, etc., that may perform application level functions in a system. Applications processor 310 may include an interface to facilitate interprocessor communication between applications processor 310 and one or more other processors, devices, subsystems, etc., for example, devices 320. Devices 320 may represent subsystems and/or drivers that control peripherals, circuits, hardware, etc.

[0029] In one embodiment applications processor 310 is a bus master, meaning applications processor 310 controls one or more portions of the physical interconnection lines between itself and devices 320. Applications processor 310 may include an interface

to the physical interconnection with the devices 320. Similarly, devices 320 may include interfaces to the physical interconnection. The interfaces may include one or more of a pin, a receive circuit, a drive circuit, a pull-up/pull-down circuit, a buffer, etc., to couple the devices/processor to the physical interconnect. The physical interconnection may include multiple signal lines, and may include a clock signal CLK 331, a strobe signal STB 332, a wait signal WAIT 333, data lines DAT[N:0] 334, and an arbitration signal LREQ# 335. While these signals are shown, more or fewer signal lines may be present in a system.

[0030] In one embodiment DAT[N:0] 334 represents multiple signal lines (e.g., 4, 8) over which interprocessor communication may be transmitted. The other signal lines may be referred to as sideband signal lines. In one embodiment a master device controls DAT[N:0] 334. The master device may also control one or more sideband lines. As represented by LREQ# 335, control of the bus may be arbitrated, meaning control will change, and a device that is at one point the master may be a slave on the bus at another point in time. In one embodiment sideband signals, e.g., WAIT 333 may be asserted by a slave device on the bus.

[0031] In one embodiment a channel may be specified by placing one bit of a nibble on each of four data lines 334 of DAT[3:0]. In another embodiment data lines 334 may include eight data lines DAT[7:0], and a full byte may be placed on the lines. Alternatively, a system could be designed to have each line divided into timeslots, and each of data lines DAT[N:0] 334 may represent use different data channels separately. The system may operate to have a channel number, device ID, and data payload placed on DAT[N:0] 334. In one embodiment a channel may be activated when a channel number and an identifier is placed on DAT[N:0]. The identifier may be decoded at the receiving device(s) to determine whether the receiving device is an intended receiver.

[0032] In a system where 2^N channels are defined, with some broadcast channels and some non-broadcast channels, a device-specific ID may be specified on a broadcast channel to provide an additional device-specific message transfer mechanism. Similarly, a broadcast ID may be specified on a device-specific channel to provide an additional broadcast message transfer mechanism. Thus, a standard channel definition may be enhanced to create a modified channel definition to include message transfer with an identifier other than one defined in the standard. Thus, a decoder may receive as valid a message that has an identifier other than one expected on the channel according to the specification.

[0033] **Figure 4** is a timing diagram for one embodiment of interprocessor communication as described above. CLK represents the clock pulses in the timing diagram. The shared interprocessor communication bus may include a clock signal to synchronize devices on the bus at least as far as communication on the bus is concerned. In one embodiment DATA[3:0] may include a device ID field 411, channel number field 412, and data field 413. Additional fields and/or different field sizes may also be supported.

[0034] A broadcast ID or a device-specific address/ID may be placed in device ID field 411 to indicate the intended message audience. A broadcast ID may indicate the message should be decoded by more than a single slave device. A device-specific ID may indicate the message should be decoded only by the specified intended recipient. For example, in Figure 4, a device-specific ID of "1" indicates the device with address/identifier "1" is the intended recipient of the message to be transmitted on DATA[3:0]. In one embodiment a device ID of "F" may indicate a broadcast message, which assumes that fewer than 2^N, or 16 in this example, devices may be supported in the system.

[0035] Channel number 412 may be specified to be any of the channels defined for the system. In one embodiment assume that all 2^N channels potential channels are defined in

the interprocessor communication definition, where N represents the number of bits/lines used to represent the channel number. Further suppose that at least one channel is defined as a broadcast channel, which is defined to have a corresponding broadcast ID placed in device ID field 411. In one embodiment channel 8 shown in channel number 412 represents a defined broadcast channel. Thus, a broadcast channel is specified with a device-specific ID of 1. Because the channel and device ID are mismatched according to the protocol, devices on the bus may ignore the message. Because the message is addressed to device 1, device 1 may be operable to decode the message, even though the message may be technically improper according to the standard protocol definition. In this way an additional channel may be supported.

[0036] In one embodiment the protocol associated with the system and timing diagram of Figure 4 may be unidirectional, or transmit only, meaning the receiving device has no way in the standard protocol definition to acknowledge transfer. The standard acknowledgement mechanism may be defined by the protocol as the receiving device arbitrating for control of the bus, establish ownership of the bus, and then transmit an acknowledge cycle to the original sending device. If there are multiple devices in the platform/system also requesting the bus, this process could produce a substantial delay in acknowledging the transfer. In the case of needing to re-transmit the message for failed reception, the process may extend even further. Thus, standard acknowledgement may be inefficient in terms of performance, power, and timing.

[0037] In one embodiment a transmitting device, or master (the device with established ownership) may follow transfer of a data message with transfer of an acknowledge message. The data message may follow standard communication protocol specifications using a data channel to a particular device. The acknowledge message may be transmitted by placing a device-specific ID in device ID field 411, and a broadcast channel in channel

number field 412. One or more nibbles of data may be placed in data field 413. In one embodiment the value of the data placed in data field 413 during an acknowledge is a "Don't Care". In one embodiment a specific acknowledge value may be employed. During the acknowledge message, the receiving device may assert or de-assert one of the sideband signals to indicate whether the message was received.

[0038] For example, in the example of Figure 4, the channel/control strobe 420 may be asserted while the transmitting device specifies a device ID of "1" and a broadcast channel "8." Data 413 is a Don't Care, and the receiving device asserts the WAIT signal 430 during the data payload of the transmitting device's message. Depending on system implementation, asserting WAIT 430 during the message may indicate the message should be resent. Alternatively, asserting WAIT 430 during the message may indicate the message was properly received. The original transmitting device may sample WAIT signal 430 at time 440 to "read" or "receive" the acknowledge message of the original receiving device.

[0039] Sampling time may depend on system implementation. There may be one or more nibbles of data payload during which time the original receiving device has to acknowledge a message transfer from the original transmitting device. Two nibbles of data are shown merely for purposes of illustration, and is not to be interpreted as being required or limiting.

[0040] **Figure 5** is a flow diagram of one embodiment of transmission of an interprocessor message. In one embodiment multiple processors, subsystems, peripherals, etc. are interconnected on a bus. Interprocessor communication may be facilitated through the use of messages across the bus. A message may be prepared for transmission, 502. A device with ownership/control of the bus and/or the data lines may be permitted to transmit a

message. The intended receiver or receivers may act as slaves on the bus while the transmitting device is the master.

[0041] It is determined whether the message is broadcast or non-broadcast, 510. For example, a transmitting module on a transmitting device may determine that a broadcast message is being sent. In another example, a processor that generates the message for transmission indicates to a transmitter that the message is either broadcast or non-broadcast. A broadcast message is intended for multiple receivers, and may not indicate a specific receiving device. A non-broadcast message is intended for one or more specified receivers, which may be identified by a device ID. If the message is a broadcast message, it is determined whether the message will be transmitted on a broadcast channel, 520. For example, the communication protocol employed by the transmitting device to engage in interprocessor communication may specify certain message types to be transmitted on certain defined channels, according to a standard protocol specification/definition. If the broadcast message is a broadcast message that matches with a definition of a broadcast channel, the message may be transmitted according to the standard specification, 522.

[0042] If the message is a broadcast message that does not match a standard specification definition, a broadcast ID and a non-broadcast channel may be specified, 524. The message may then transmitted, 526.

[0043] If the message is not a broadcast message, it is a non-broadcast message, and it is determined whether the message will be transmitted on a standard device-specific channel, 530. For example, a transmitter may access a look-up table, a storage location, etc., that indicates a channel to use for a particular message. A device-specific channel may be one that is defined in the specification for transfer of a particular message type to a device specified by a device ID. If the non-broadcast message is a message that matches with a

definition of a device-specific channel of the specification, the message may be transmitted according to the standard specification, 522.

[0044] If the message is a device-specific message that does not match a standard specification definition, a device-specific ID and a broadcast channel may be specified, 534. The message may then transmitted, 536.

[0045] **Figure 6** is a flow diagram of one embodiment of an acknowledgement of an interprocessor message transfer. A message may be transferred from a transmitting device to a receiving device over a data channel, 602. This may be a message that complies with the channel and transfer definitions of an interprocessor communication specification. A data transfer on a data channel may involve specifying the data channel and a device-specific ID.

[0046] In one embodiment transmission may be unidirectional, and a mechanism for acknowledging the transfer is not provided in the specification. A device-specific ID may be specified on a channel that is traditionally a broadcast channel, 604, as described above. The device-specific ID may cause the message to be received and decoded only by the intended receiver, even though messages transferred on the specified broadcast channel would normally be listened to and decoded by all devices on the interconnection.

[0047] The first transmission may be according to the protocol specification, and the second transmission may be a device-specific communication on a channel not defined for device-specific transfer. In one embodiment the receiver may assert a sideband signal, e.g., a flow control signal line, to indicate an acknowledgement to the transmitting device. It is determined whether a sideband signal is asserted, 610. For example, the master may test/determine for the condition/state of the sideband line (e.g., asserted or not asserted) to receive an indication from the original receiving device whether the message was

received. Asserted means that a signal line is driven high for an active-high signal line, and driven low for an active-low signal line.

[0048] If the sideband signal is asserted, the original receiving device may be indicating to the original transmitting device that the message was not properly received, and the message should be resent, 614. If the sideband signal is de-asserted, the original receiving device may be indicating to the original transmitting device that the message was properly received, and the message may be thus acknowledged, 612. These indications could be switched, in that an asserted signal may indicate a properly received signal, and a de-asserted signal may indicate the message should be resent.

[0049] The various operations of Figures 5 and 6 in the various embodiments may be performed utilizing the structures described herein. Additionally, machine-accessible and/or executable sequences of instructions/code may be used to perform operations described in Figures 5 and 6. Thus, the operations described may be implemented with firmware, software, or a combination of firmware and software. The software and/or firmware content may provide instructions to result in hardware, a machine, an electronic device, etc., executing/performing various operations, including some or all of the functions/features described above.

[0050] Instructions to cause or result in a machine/electronic device/hardware to perform the operations may be received via an article of manufacture. An article of manufacture may include a machine accessible medium having content to provide the instructions. A machine accessible medium includes any mechanism that provides (i.e., stores and/or transmits) information/content in a form accessible by a machine (e.g., computing device, electronic device, electronic system/subsystem, etc.). For example, a machine accessible medium includes recordable/non-recordable media (e.g., read only memory (ROM), random access memory (RAM), magnetic disk storage media, optical storage media, flash

memory devices, etc.), as well as electrical, optical, acoustical or other form of propagated signals (e.g., carrier waves, infrared signals, digital signals, etc.), etc.

[0051] Figure 7 is a block diagram of one embodiment of a portable device having processors with a physical interconnect. Portable device 700 may represent various devices, hardware units, electronic structures, etc. For example, portable device 700 may be a cell phone, a smartphone, a personal digital assistant (PDA), a personal computing device, etc. Portable device 700 may include a computing platform on which computing hardware operates. The computing platform may include the computing hardware and/or operating systems, hardware drivers, and/or applications to provide the functionality of portable device 700. In one embodiment applications processor 710 may be a circuit, processor, subsystem, etc., on the host platform. In one embodiment applications processor 710 may be the computing platform of portable device 700. Applications processor 710 may be one or more hardware components, and/or including a general purpose processor.

[0052] Applications processor 710 may include MSL interface 721 that provides an interface to physical interface 730 to radio subsystem 740. Radio subsystem 740 likewise may include MSL interface 722 to interface to physical interface 730. For example, MSL interfaces 721-722 may include pins, circuits, buffers, line drivers, or other hardware/packaging to provide access from applications processor 710 and radio subsystem 740 to physical interface 730 between the devices. Physical interface may represent a high throughput communication bus/interface for data transfer between applications processor 710 and radio subsystem 740.

[0053] In one embodiment radio subsystem 740 represents a peripheral device on portable device 700. Radio subsystem 740 may include signal processor 741 to provide radio

communication processing functionality to radio subsystem 740. In one embodiment signal processor 741 may be an embedded processor on portable device 700.

[0054] Physical interface 730 in one embodiment may be a communication bus based on or derived from the Mobile Scalable Link (MSL) or MSL2 standard specification/definition/protocol. Channels may be defined, or assigned particular functions/uses in the standard. For example, in one embodiment physical interface 730 may include multicast or broadcast channel(s) and device-specific channel(s), as defined/assigned by the standard. Multicast data may be understood as corresponding to a message/a transfer occurring on a multicast channel. Likewise, broadcast data or a broadcast transfer may be understood as corresponding to a message/transfer on a broadcast channel, and device-specific data/transfer may refer to a message transfer on a device-specific (or non-broadcast, non-multicast) channel.

[0055] A broadcast channel may be understood to be a subset of types of multicast channel in that a multicast channel may be defined to transfer a message to multiple recipients simultaneously. A broadcast channel may be defined to transfer the message simultaneously to all potential recipients, or all devices on physical interface 730. A device-specific channel may be defined to transfer a message to a single, specified recipient. Of all devices on physical interface 730, it may be that the only device to which a device-specific message is transferred is the device corresponding to the device-specific identifier/address indicated in activating the device-specific channel.

[0056] In one embodiment a broadcast message may be made on a device-specific channel on physical interface 730 as discussed above. Namely, a broadcast ID may be used to invoke/activate the device-specific channel on physical interface 730 and the message transferred. Similarly, a device-specific message may be transferred on a broadcast

channel on physical interface 730 by, for example, invoking/activating the broadcast channel with the device-specific ID and the message transferred.

[0057] In one embodiment physical interface 730 may include one or more control signals/lines. In one embodiment one or more control signals may be activated/controlled by a device that does not have ownership of the data bus on which the broadcast and device-specific channels are operated. A flow control signal line may be present, such as a wait or equivalent control signal to provide a mechanism for a receiving device to indicate to the transmitting device that it is presently unable to receive the transmitted message. For example, the signal may be asserted when a buffer overflow occurs. In one embodiment a control signal may be used to implement an acknowledge mechanism. For example, by using a device-specific address on a broadcast channel as described above, a transmitting device may provide a time period for the receiving device to indicate whether a message was received, or whether the message should be resent. If a transmitting device follows a normal (e.g., according to the standard protocol) data transfer with a modified transfer (e.g., mismatching the device ID with the channel type), this modified transfer may be decoded at the receiver indicated by the device ID that the receiver should activate the control signal to indicate an acknowledge. The specific acknowledge mechanism may be up to individual design, and one example may include asserting the control signal to indicate a negative acknowledgement, meaning the message should be resent.

[0058] **Figure 8** is a block diagram of one embodiment of an electronic device having processors with a physical interconnect and a memory subsystem. Electronic device 800 represents various devices, machines, hardware units, electronic structures, etc. For example, electronic device 800 may be a cell phone, a smartphone, a personal digital assistant (PDA), a digital data organizer, a personal computing device, etc. Electronic device 800 may include a computing platform on which computing hardware operates.

The computing platform may include the computing hardware and/or operating systems, hardware drivers, and/or applications to provide the functionality of electronic device 800. In one embodiment applications processor 810 represents a circuit, processor, subsystem, etc., on the host platform. In one embodiment applications processor 810 represents the computing platform of electronic device 800. Applications processor 810 may be one or more hardware components, and/or including a general purpose processor.

[0059] Applications processor 810 may include MSL interface 821 that provides an interface to physical interface 830 to peripheral controller 840. Peripheral controller 840 likewise may include MSL interface 822 to interface to physical interface 830. For example, MSL interfaces 821-822 may include pins, circuits, buffers, line drivers, or other hardware/packaging to provide access from applications processor 810 and peripheral controller 840 to physical interface 830 between the devices. Physical interface 830 may represent a high throughput communication bus/interface for data transfer between applications processor 810 and peripheral controller 840.

[0060] In one embodiment peripheral controller 840 represents a variety of peripheral devices/subsystems on electronic device 800. For example, peripheral controller 840 may represent components of a user interface (e.g., a liquid crystal display (LCD), a touchscreen, a keyboard or equivalent, etc.). Peripheral controller 840 may include processor 841 to provide processing functionality to peripheral controller 840. In one embodiment processor 841 may be an embedded processor on electronic device 800.

[0061] Physical interface 830 in one embodiment may be a communication bus based on or derived from the Mobile Scalable Link (MSL) or MSL2 standard specification/definition/protocol. Channels may be defined, or assigned particular functions/uses in the standard. For example, in one embodiment physical interface 830 may include multicast or broadcast channel(s) and device-specific channel(s), as

defined/assigned by the standard. Multicast data may correspond to a message/a transfer occurring on a multicast channel. Likewise, broadcast data or a broadcast transfer may correspond to a message/transfer on a broadcast channel, and device-specific data/transfer may refer to a message transfer on a device-specific (or non-broadcast, non-multicast) channel.

[0062] A broadcast channel may be a subset of types of multicast channel in that a multicast channel may be defined to transfer a message to multiple recipients simultaneously. A broadcast channel may transfer the message simultaneously to all potential recipients, or all devices on physical interface 830. A device-specific channel may transfer a message to a single, specified recipient. Of all devices on physical interface 830, it may be that the only device to which a device-specific message is transferred is the device corresponding to the device-specific identifier/address indicated in activating the device-specific channel.

[0063] In one embodiment a broadcast message may be made on a device-specific channel on physical interface 830 in accordance with that discussed above. Namely, a broadcast ID may be used to invoke/activate the device-specific channel on physical interface 830 and the message transferred. Similarly, a device-specific message may be transferred on a broadcast channel on physical interface 830 by, for example, invoking/activating the broadcast channel with the device-specific ID and the message transferred.

[0064] In one embodiment physical interface 830 may include one or more control signals/lines. In one embodiment one or more control signals may be activated/controlled by a device that does not have ownership of the data bus on which the broadcast and device-specific channels are operated. A flow control signal line may be present, such as a wait or equivalent control signal to provide a mechanism for a receiving device to indicate

to the transmitting device that it is presently unable to receive the transmitted message. For example, the signal may be asserted when a buffer overflow occurs.

[0065] In one embodiment a control signal may be used to implement an acknowledge mechanism. For example, by using a device-specific address on a broadcast channel as described above, a transmitting device may provide a time period for the receiving device to indicate whether a message was received, or whether the message should be resent. If a transmitting device follows a normal (e.g., according to the standard protocol) data transfer with a modified transfer (e.g., mismatching the device ID with the channel type), this modified transfer may be decoded at the receiver indicated by the device ID that it should activate the control signal to indicate an acknowledge. The specific acknowledge mechanism may be dependent upon individual design, and one example may include asserting the control signal to indicate a negative acknowledgement, meaning the message should be resent.

[0066] In one embodiment electronic device may include memory subsystem 850. Memory subsystem 850 may include a memory controller, logic, one or more memory devices, etc. In one embodiment memory subsystem 850 may include non-volatile storage 851, which represents memory that retains its information/values even when power is disabled to electronic device 800. For example, non-volatile storage 851 may include read only memory (ROM), Flash, programmable ROM (PROM), electronic PROM (EPROM), etc.

[0067] Besides what is described herein, various modifications may be made to embodiments of the invention without departing from their scope. Therefore, the illustrations and examples herein should be construed in an illustrative, and not a restrictive sense. The scope of the invention should be measured solely by reference to the claims that follow.

CLAIMS

What is claimed is:

1. A method for acknowledging data transfer, comprising:

transmitting a message over a communication interface, the communication interface having a broadcast channel and a device-specific channel assigned in a standard communication interface specification, the broadcast channel defined in the specification for data transfer with a broadcast identifier to multiple devices, the device-specific channel defined in the specification for data transfer to a specific device, the message transmitted on the device-specific channel with a device-specific identifier;

subsequently transmitting a different message over the communication interface with the device-specific identifier on the broadcast channel;

determining the state of a control signal; and

determining, based at least in part on the state of the control signal, whether to retransmit the message.

2. A method according to claim 1, wherein the communication interface specification standard comprises the Mobile Scalable Link 2 (MSL2) standard.

3. A method according to claim 2, wherein the broadcast channel comprises one of channel 8, 9, or 15.

4. A method according to claim.3, wherein the broadcast channel comprises channel 8.

5. A method according to claim 1, wherein the control signal comprises a flow control line of the communication interface.

6. A method according to claim 5, wherein the flow control line comprises a wait signal line.

7. A method according to claim 1, wherein the message is to be retransmitted if the state of the control line is asserted.

8. A method according to claim 7, wherein the control line being asserted comprises the control line being at a high logic level when the state is tested.

9. An article of manufacture comprising a machine-accessible medium having content to provide instructions to result in a machine performing operations including:

transmitting a device-specific message over a communication bus, the communication bus having a broadcast channel and a non-broadcast channel defined in a communication interface standard specification, the broadcast channel defined in the specification with a broadcast channel number and a broadcast address, the non-broadcast channel defined in the specification with a non-broadcast channel number and a device-specific address, the device-specific message transmitted on the non-broadcast channel with a device-specific address;

subsequently transmitting a different device-specific message with the device-specific address on the broadcast channel;

determining the state of a sideband signal; and

determining whether to retransmit the signal based, at least in part, on the state of the control line.

10. An article of manufacture according to claim 9, wherein the communication interface specification standard comprises the Mobile Scalable Link 2 (MSL2) standard.

11. An article of manufacture according to claim 9, wherein the sideband signal comprises a control line of the communication bus.

12. An article of manufacture according to claim 11, wherein the control line comprises a wait signal line.

13. A method for acknowledging data transfer, comprising:

receiving a message over a communication interface, the communication interface having a broadcast channel and a device-specific channel assigned in a standard communication interface specification, the broadcast channel defined in the specification for data transfer with a broadcast identifier to multiple devices, the device-specific channel defined in the specification for data transfer to a specific device, the message received on the device-specific channel with a device-specific identifier;

subsequently receiving a different message over the communication interface with the device-specific identifier on the broadcast channel; and

changing the state of a control signal to indicate an acknowledge of the received message to the transmitting device.

14. A method according to claim 13, wherein the communication interface specification standard comprises the Mobile Scalable Link 2 (MSL2) standard.

15. A method according to claim 14, wherein the broadcast channel comprises one of channel 8, 9, or 15.

16. A method according to claim 13, wherein the control signal comprises a sideband control line of the communication interface.

17. A method according to claim 16, wherein the control line comprises a wait signal line.

18. A method according to claim 13, wherein the state of the control signal is changed from de-asserted to asserted to indicate the message was not properly received.

19. A method according to claim 18, wherein the state of the control line is changed from a low logic level to a high logic level to change the state from de-asserted to asserted.

20. An article of manufacture comprising a machine-accessible medium having content to provide instructions to result in a machine performing operations including:

receiving a device-specific message over a communication bus, the communication bus having a broadcast channel and a non-broadcast channel defined in a communication interface standard specification, the broadcast channel defined in the specification with a broadcast channel number and a broadcast address, the non-broadcast channel defined in the specification with a non-broadcast channel number and a device-specific address, the device-specific message received on the non-broadcast channel with a device-specific address;

subsequently receiving a different device-specific message with the device-specific address on the broadcast channel; and

changing the state of a control signal to indicate an acknowledge of the received message to the transmitting device.

21. An article of manufacture according to claim 20, wherein the communication interface specification standard comprises the Mobile Scalable Link 2 (MSL2) standard.

22. An article of manufacture according to claim 20, wherein the sideband signal comprises a control line of the communication bus.

23. An article of manufacture according to claim 22, wherein the control line comprises a wait signal line.

24. An apparatus to acknowledge an interprocessor data transfer, comprising:

a data line having a defined broadcast channel and a defined device-specific channel, the broadcast channel defined in a standard protocol definition to be activated with a broadcast channel number and a broadcast address, the device-specific channel defined in the protocol definition to be activated with a device-specific channel number and a device-specific address, the data line to have a data message transferred on the data line by the device-specific channel being activated with the device-specific channel number and a device-specific address of a device to receive the data message, and to have

a subsequent an acknowledge message transferred on the data line by the broadcast channel being activated with the broadcast channel number and the device-specific address of the receiving device; and

a sideband signal line to be asserted by the receiving device in response to the acknowledge message to indicate whether the receiving device received the data message correctly.

25. An apparatus according to claim 24, wherein the acknowledge message comprises the broadcast channel number, the device-specific address, and two bytes of data.

26. An apparatus according to claim 25, wherein the two nibbles of data comprise a Don't Care data byte.

27. An apparatus according to claim 24, wherein the signal line is asserted to indicate the receiving device did not receive the data message correctly.

28. A system comprising:

a master device;

a slave device;

a communication bus interconnecting the master device and the slave device, the master device to control the communication bus, the communication bus to include a data line and a control signal line, the data line defined in an interprocessor protocol standard to transfer only broadcast data on a broadcast channel, the master to transmit non-broadcast data to the slave on the broadcast channel according to a modified data line definition, the modified data line definition to allow non-broadcast data transfer on the broadcast channel, the non-broadcast data transfer to transfer an acknowledge message from the master device to the slave device, and the control signal line to indicate the acknowledge from the slave device to the master device; and

a non-volatile storage coupled with the master device to store a code sequence to be executed by the master device.

29. A system according to claim 28, wherein the modified data line definition comprises defining the acknowledge message on the broadcast channel as activating the broadcast channel with the broadcast channel number, and a device-specific identifier rather than a broadcast identifier, and the control signal line being asserted in response to the acknowledge message to indicate a negative acknowledgement.

30. A system according to claim 29, wherein the interprocessor protocol standard comprises the Mobile Scalable Link 2 (MSL2) standard, the broadcast channel comprises one of channels 8, 9, or 15, and the control signal line comprises a flow control wait signal line.