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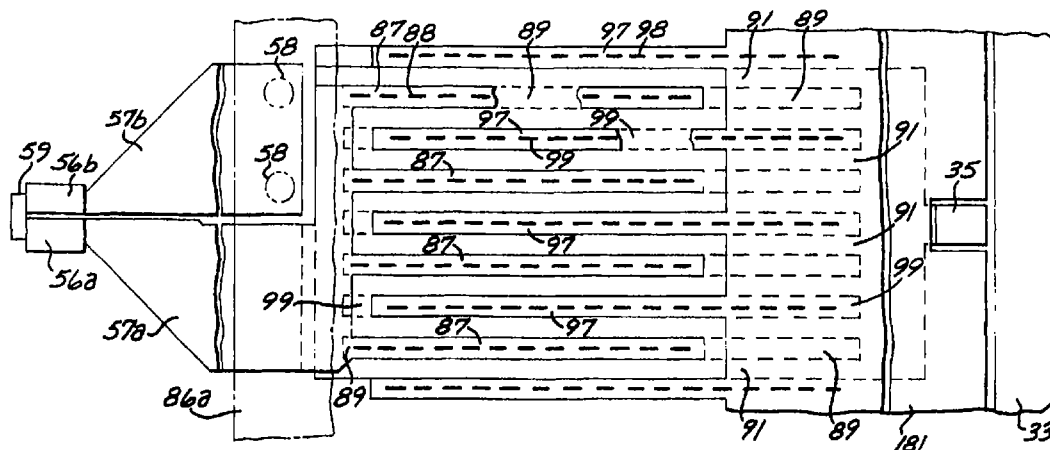
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- (71) Applicant: **HEWLETT-PACKARD COMPANY** [US/US], M/S 20BN, 3000 Hanover Street, Palo Alto, CA 94304-1112 (US)
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- (72) Inventors: **TORGERSON, Joseph M.**; 24901 Hidden Valley Rd, Philomath, OR 97370 (US). **BROWNING, Robert N.K.**, 1715 NW Menlo Drive, Corvallis, OR 97330 (US). **MACKENZIE, Mark H.**, 1715 NW Menlo Drive, Corvallis, OR 97330 (US) **BOYD, Patrick V.**; 35262 Riverside Drive, Albany, OR 97321 (US)
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(54) Title: ENERGY BALANCED PRINthead DESIGN



(57) Abstract: A narrow ink jet printhead (100A, 100B) having efficient FET drive circuits (85) that are configured to compensate for parasitic resistances of power traces (86a, 86b, 86c, 86d, 181). The ink jet printhead further includes ground busses (181) that overlap active regions of the FET drive circuits

WO 02/060692 A1