## CLAIMS

What is claimed is:

1. An antenna array, comprising:

a plurality of subarrays, wherein a feed point and at least one radiating element are disposed on each subarray, the feed point is disposed beside the at least one radiating element on each subarray, the plurality of subarrays are arranged in a first direction and a second direction, the first direction is perpendicular to the second direction, the feed points of the plurality of subarrays are located on a same straight line in the first direction, and the feed points of the plurality of subarrays are located on a same straight line in the second direction; and in the first direction, subarrays to which feed points on a same straight line belong are located in a same row, and in the second direction, subarrays to which feed points on a same straight line belong are located in a same column; and

each subarray has a phase center, phase centers of subarrays in at least one row of subarrays, of which the feed points are on a same straight line, in the antenna array are not on a same straight line, and/or phase centers of subarrays in at least one column of subarrays, of which the feed points are on a same straight line, in the antenna array are not on a same straight line.

2. The antenna array according to claim 1, wherein the antenna array comprises N subarrays, a same quantity of radiating elements are disposed on each of the N subarrays, a feed point of at least one of the N subarrays is different from a feed point of another subarray of the N subarrays, and N is an integer greater than or equal to 2.

3. The antenna array according to claim 1 or 2, wherein the antenna array comprises at least two types of same-type subarrays, and a same quantity of radiating elements are disposed on subarrays of one type of the same-type subarrays.

4. The antenna array according to any one of claims 1 to 3, wherein

the antenna array comprises at least one first subarray, at least two radiating elements are disposed on the first subarray, and the at least two radiating elements are arranged in a straight line; and

a feed point of the first subarray is located between the two radiating elements; or

the feed point of the first subarray is located on a side that is of a radiating element at an end of the first subarray and that is away from a remaining radiating element.

5. The antenna array according to any one of claims 1 to 4, wherein the antenna array comprises at least one second subarray, one radiating element is disposed on the second subarray, and a feed point of the second subarray is located beside the radiating element.

6. The antenna array according to any one of claims 1 to 5, wherein in the first direction, spacings between feed points of every two adjacent subarrays are equal, and/or in the second direction, spacings between feed points of every two adjacent subarrays are equal.

7. The antenna array according to any one of claims 1 to 6, wherein the antenna array comprises a dummy element, and the dummy element is a non-feed radiating element.

8. An apparatus, comprising:

the antenna array according to any one of claims 1 to 7,

a feeder cable, and

a circuit bearing board, wherein the feeder cable is configured to feed a subarray in the antenna array, and the antenna array and the feeder cable are disposed on the circuit bearing board.

9. The apparatus according to claim 8, further comprising:

at least one radio frequency integrated circuit chip, wherein the radio frequency integrated circuit chip is disposed on the circuit bearing board, the radio frequency integrated circuit chip comprises at least two radio frequency transmitting and receiving channels, the at least two radio frequency transmitting and receiving channels are configured to respectively feed at least two subarrays in the antenna array through the feeder cables, and the radio frequency transmitting and receiving channels are connected to the subarrays in a one-to-one manner.

10. The apparatus according to claim 9, wherein

the apparatus comprises a power divider/combiner and at least two radio frequency integrated circuit chips, the power divider/combiner is separately connected to the at least two radio frequency integrated circuit chips through at least two power divider lines, the at least two power divider lines have a same length, and the power divider lines are connected to the radio frequency integrated circuit chips in a one-to-one manner.

11. The apparatus according to claim 10, wherein the circuit bearing board is a package substrate; and

the apparatus further comprises:

a printed circuit board, wherein the package substrate is disposed on the printed circuit board and is connected to the printed circuit board, and the power divider/combiner is disposed on the printed circuit board.

12. The apparatus according to claim 10, wherein the circuit bearing board is a printed circuit board; and

the power divider/combiner is disposed on the printed circuit board.

13. An apparatus comprising:

the antenna array according to any one of claims 1 to 7;

a feeder cable; and

at least one radio frequency integrated circuit chip, wherein the antenna array and the feeder cable are disposed

on a packaging layer of the radio frequency integrated circuit chip, the radio frequency integrated circuit chip comprises at least two radio frequency transmitting and receiving channels, the at least two radio frequency transmitting and receiving channels are configured to respectively feed at least two subarrays in the antenna array through the feeder cables, and the radio frequency transmitting and receiving channels are connected to the subarrays in a one-to-one manner.

14. The apparatus according to claim 13, wherein the apparatus comprises a power divider/combiner and at least two radio frequency integrated circuit chips; and

the power divider/combiner is separately connected to the at least two radio frequency integrated circuit chips through at least two power divider lines, the at least two power divider lines have a same length, and the power divider lines are connected to the radio frequency integrated circuit chips in a one-to-one manner.

15. The apparatus according to claim 13 or 14, wherein the apparatus further comprises:

a printed circuit board, wherein both the radio frequency integrated circuit chip and the power divider/combiner are disposed on the printed circuit board.

16. A wireless communication device, comprising:

the antenna array according to any one of claims 1 to 7, or the apparatus according to any one of claims 8 to 1