FIG. 1

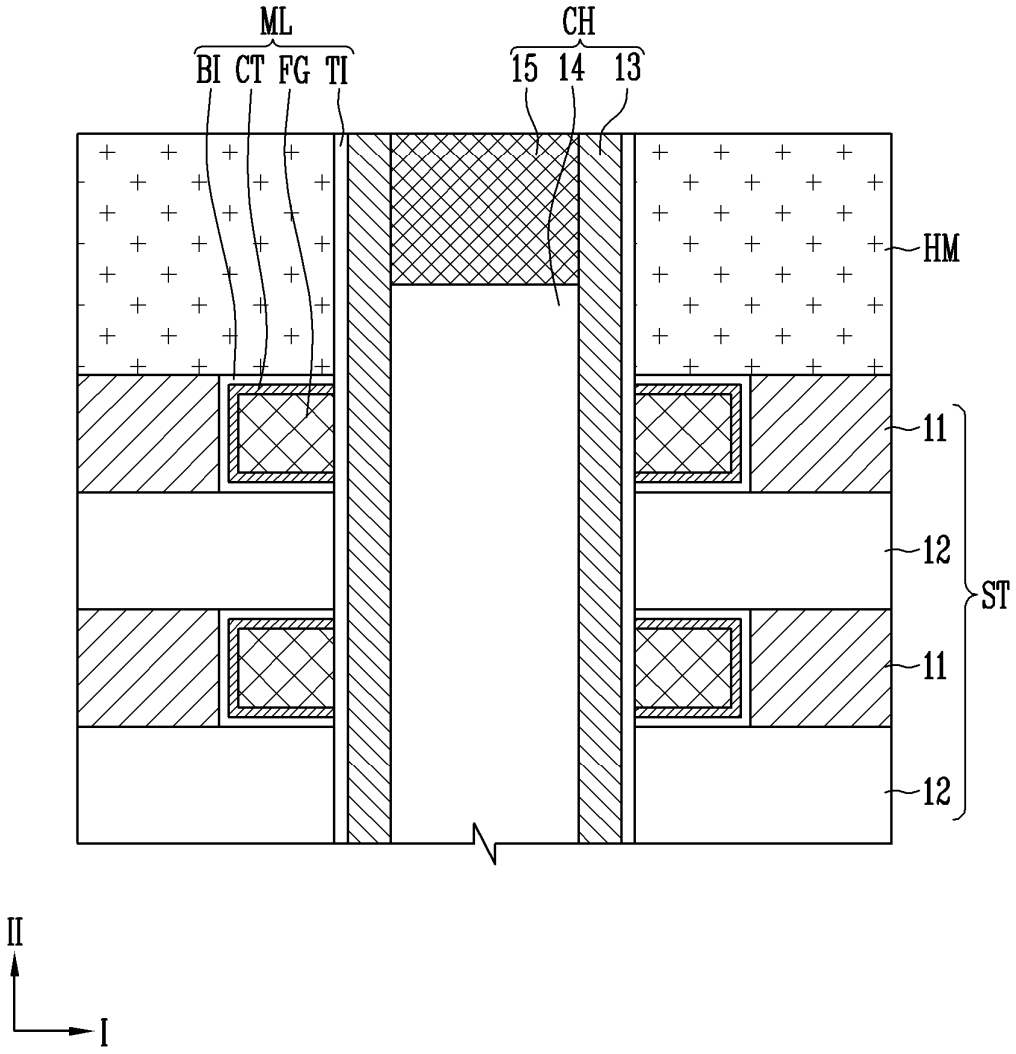


FIG. 2A

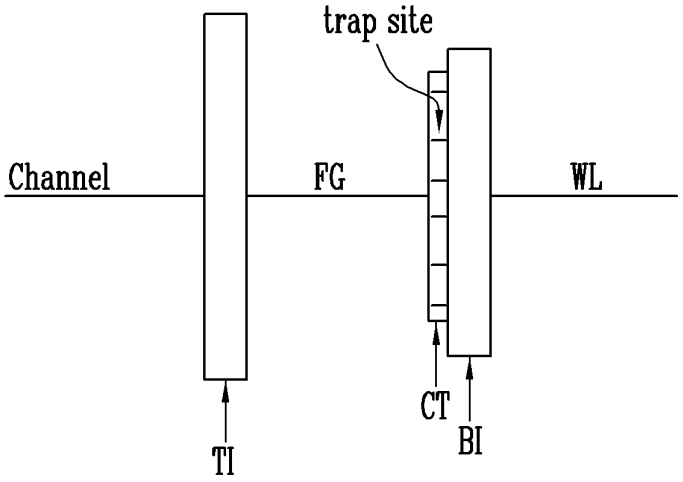


FIG. 2B

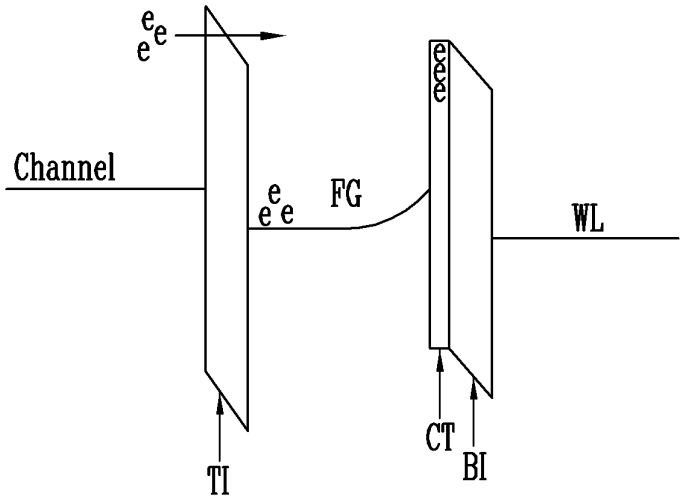


FIG. 3A

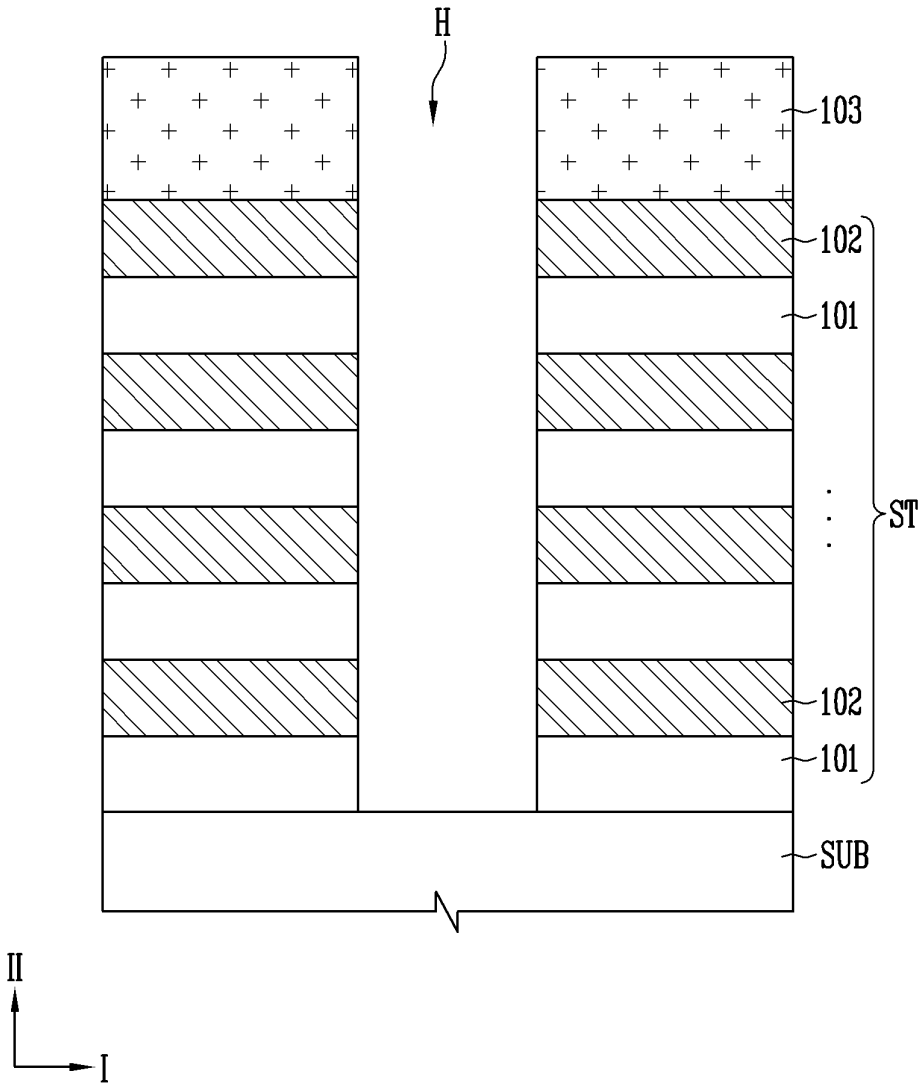


FIG. 3B

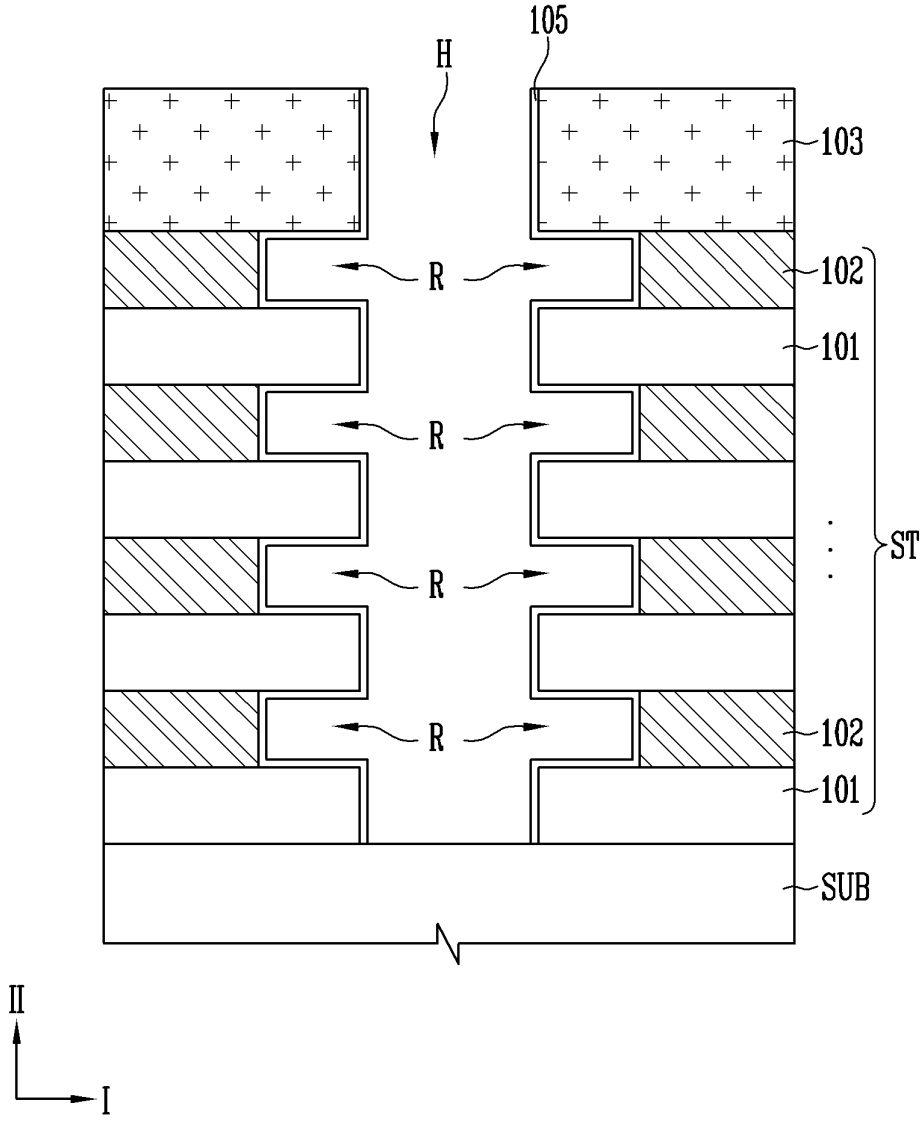


FIG. 3C

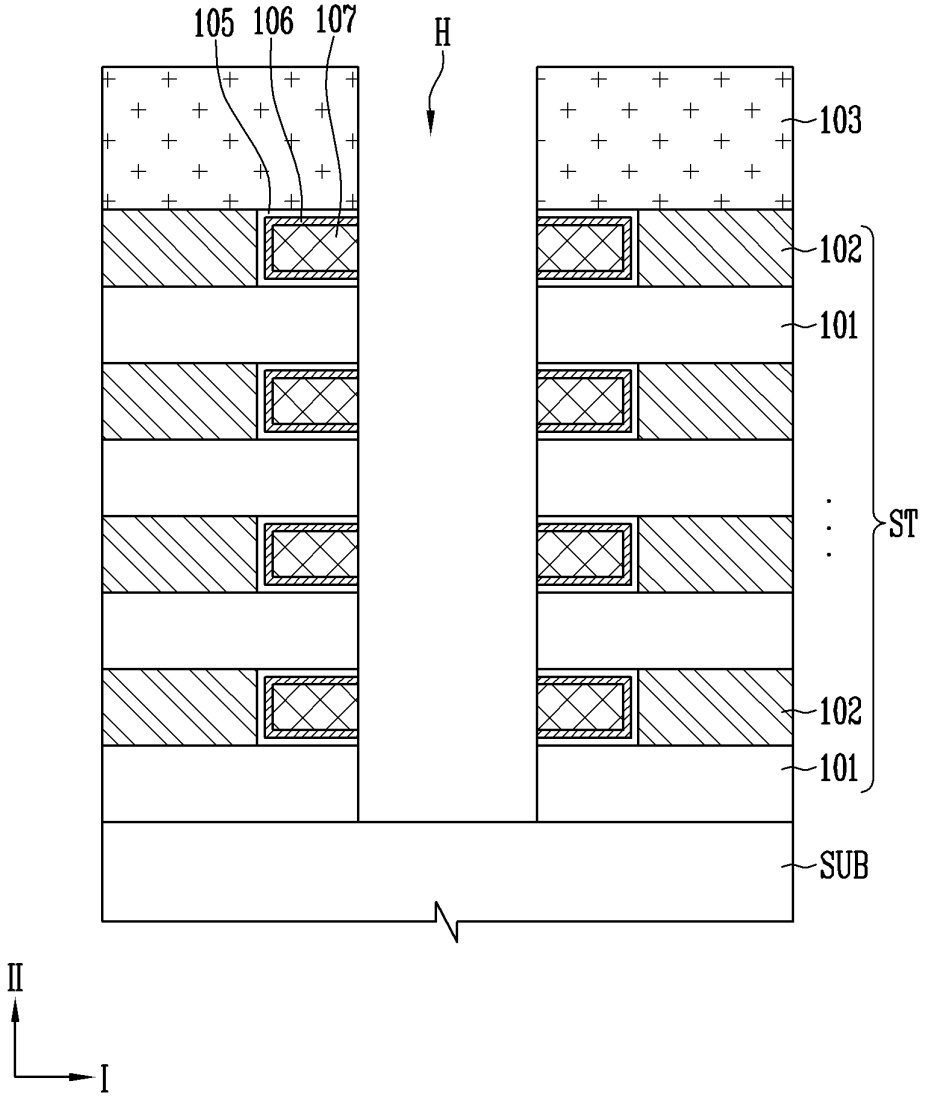


FIG. 3D

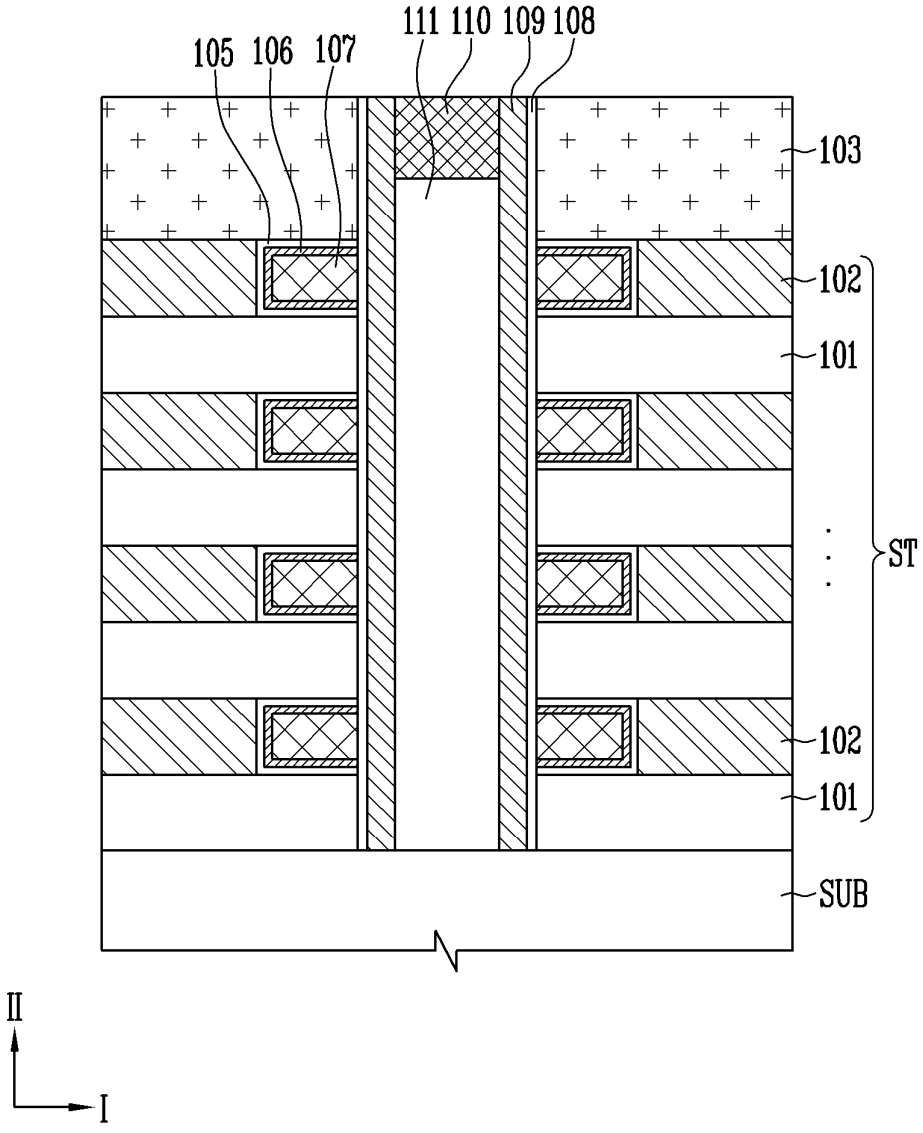


FIG. 3E

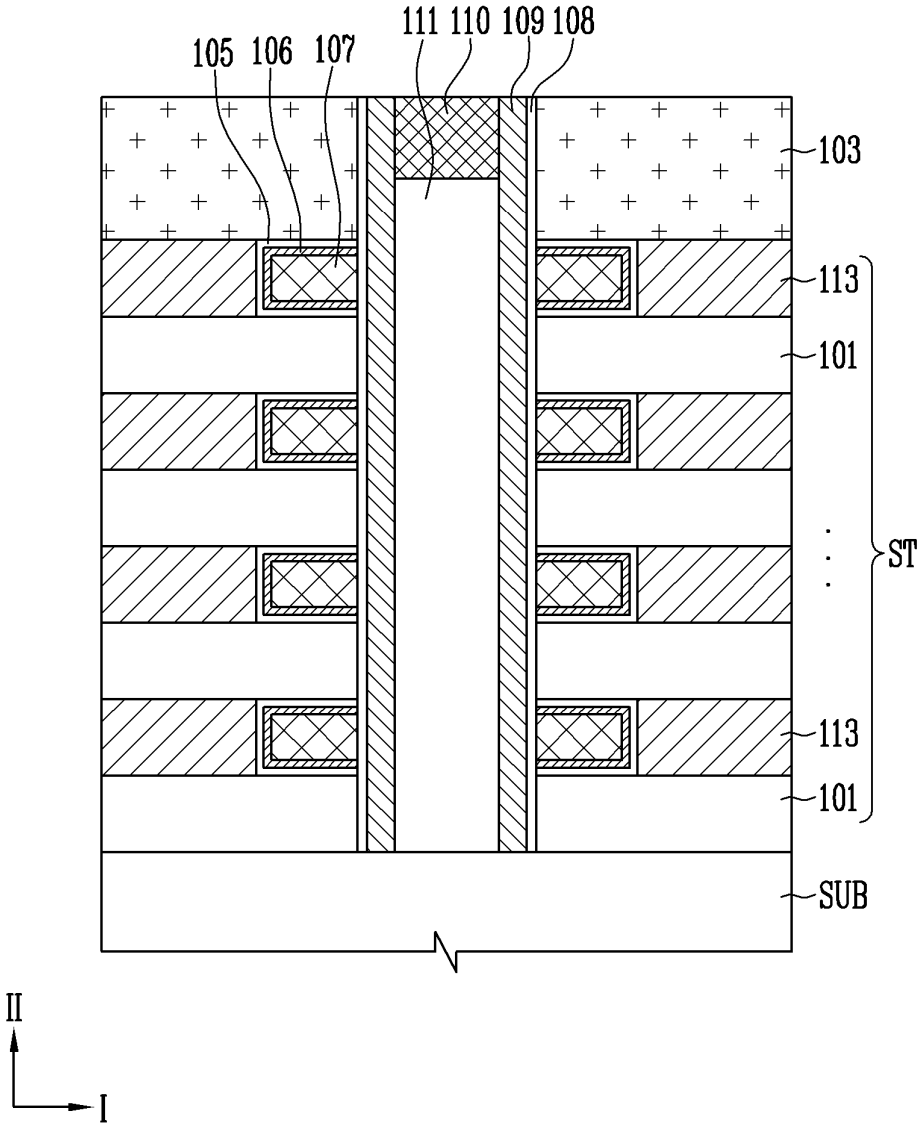


FIG. 4

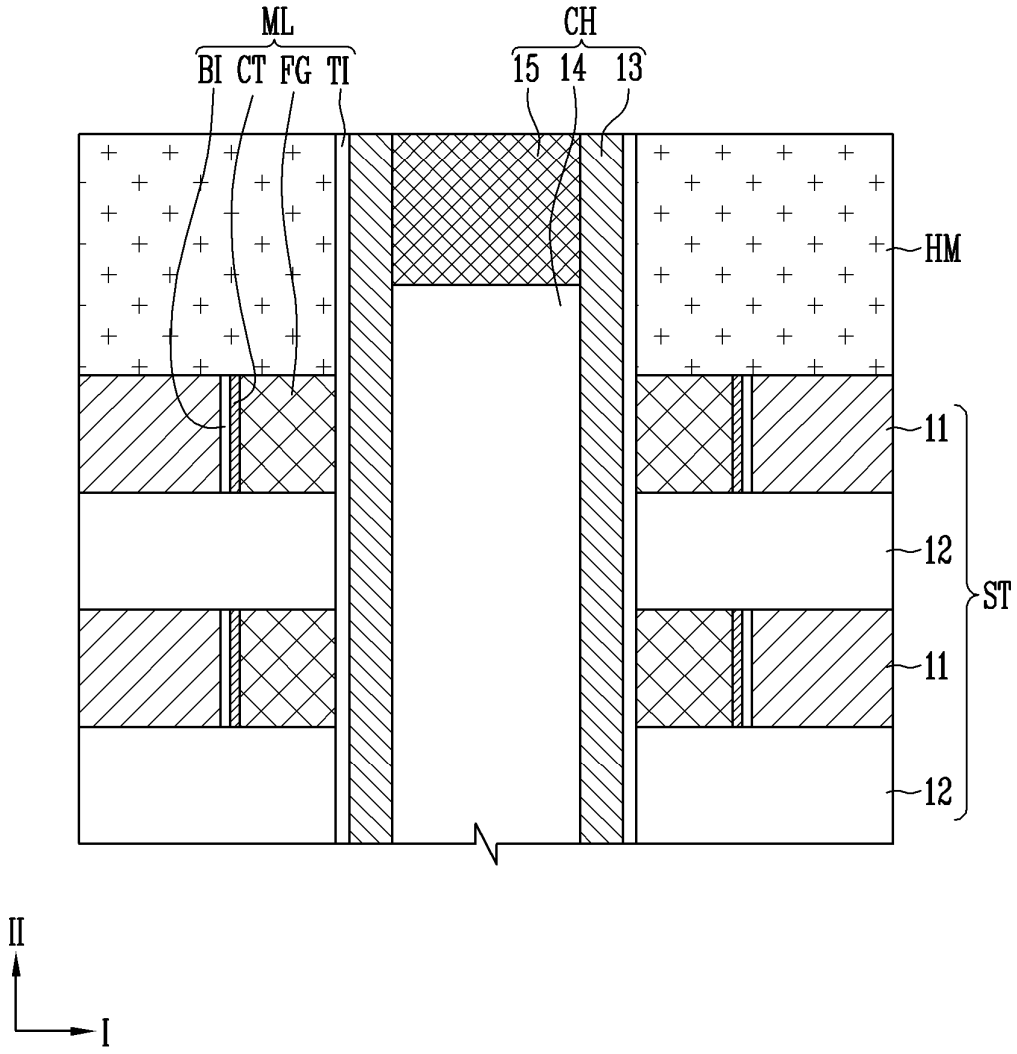


FIG. 5

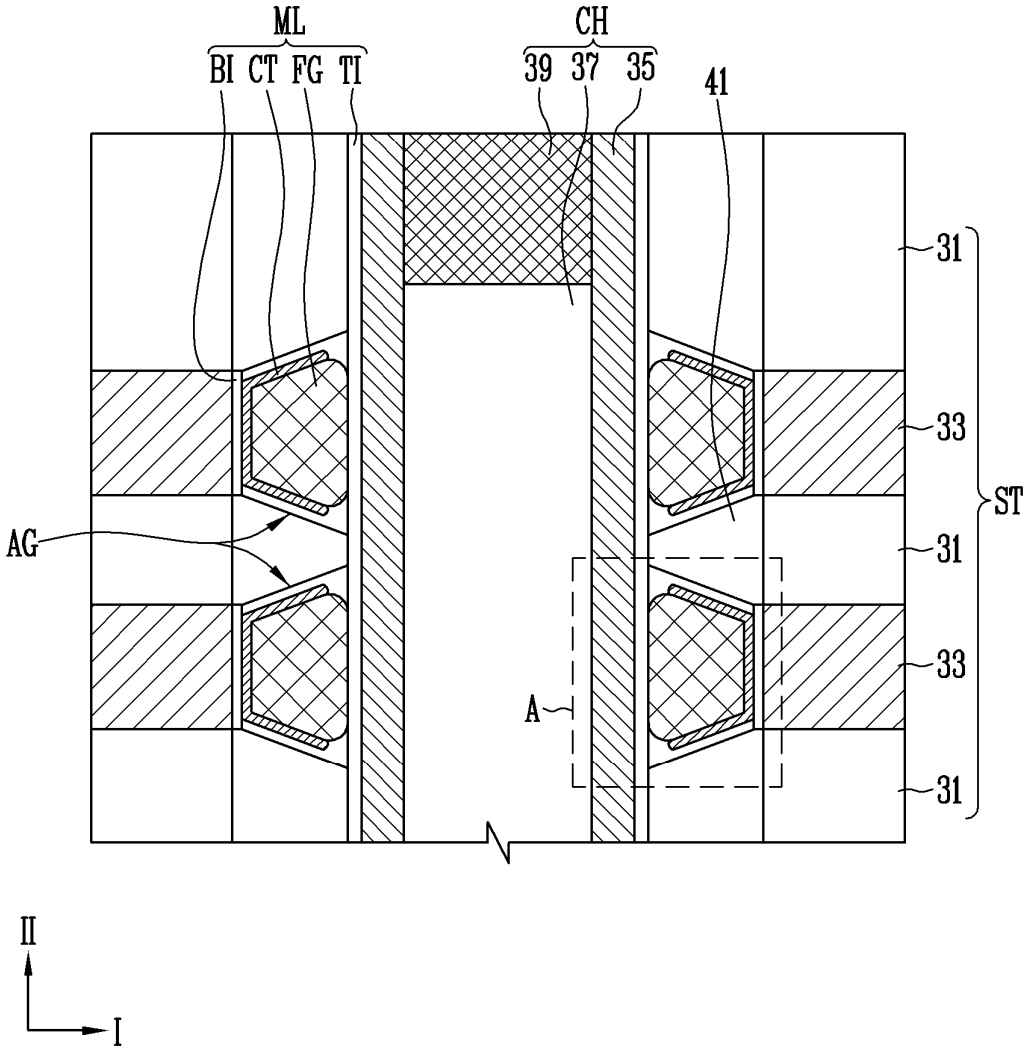


FIG. 6

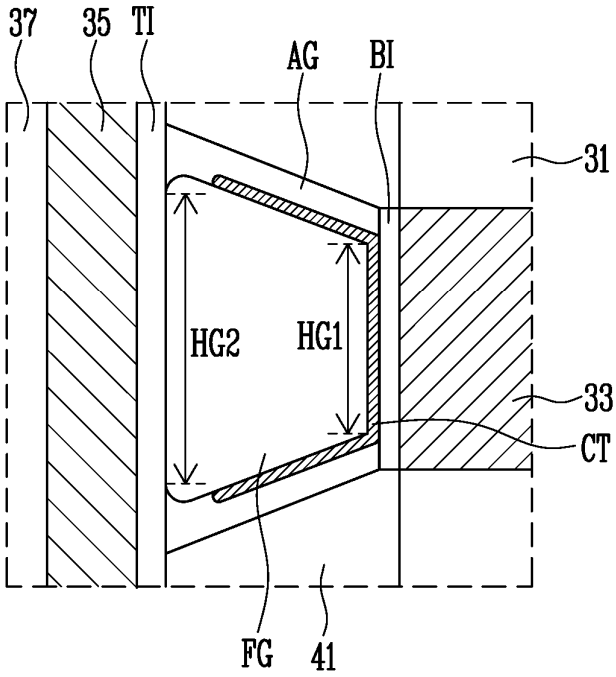


FIG. 7A

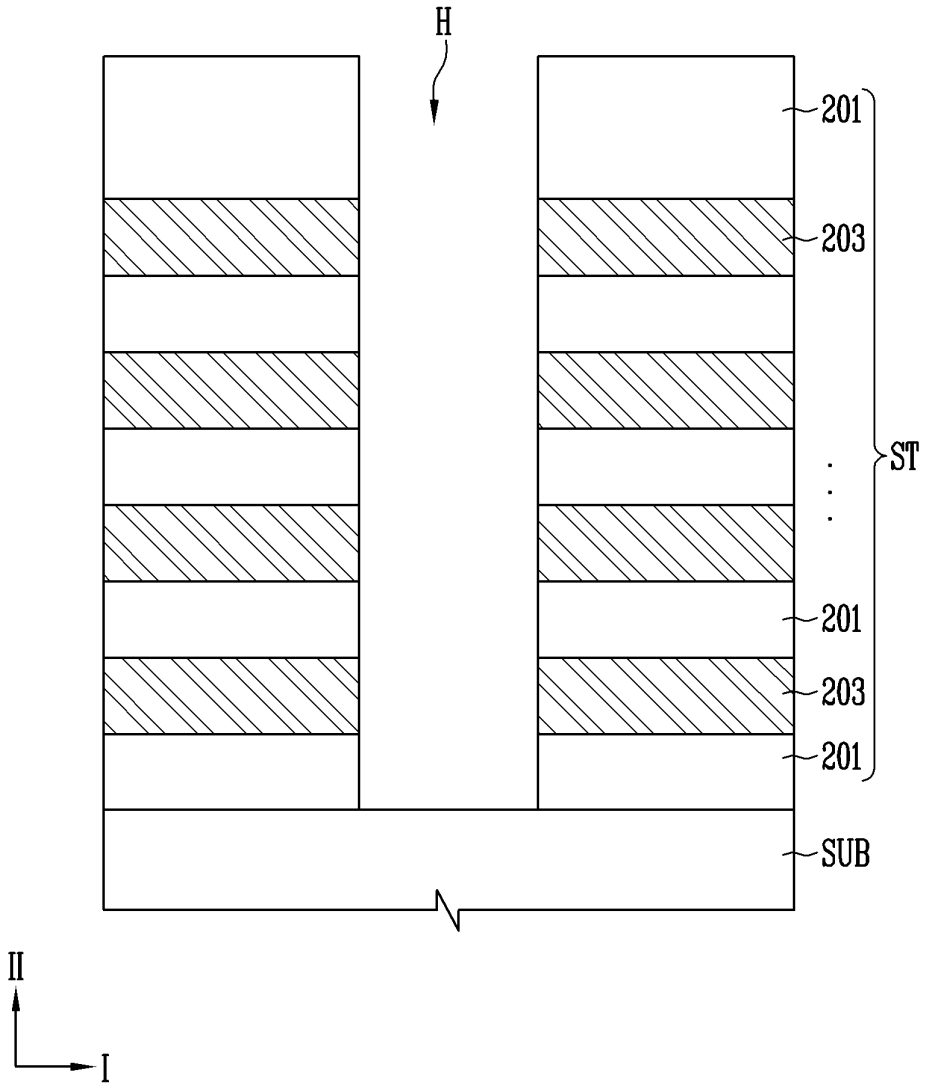


FIG. 7B

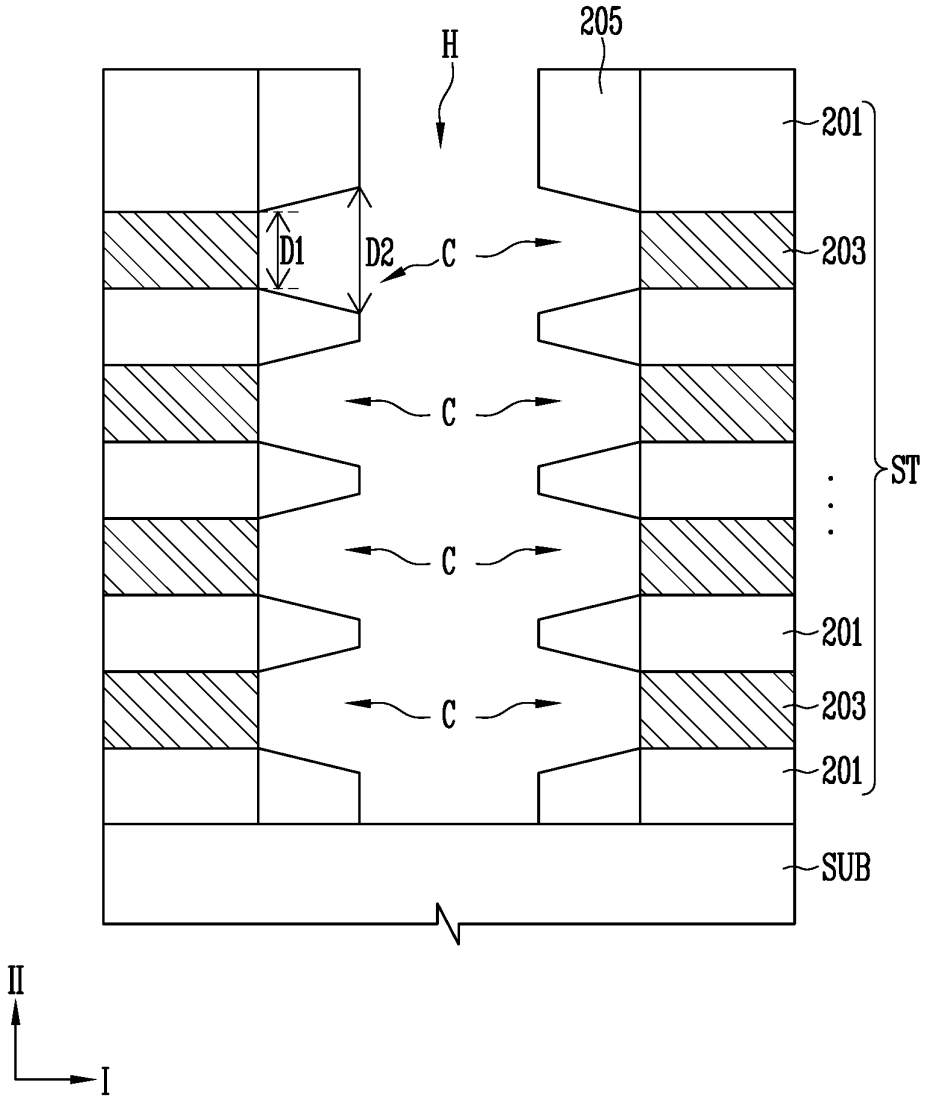


FIG. 7C

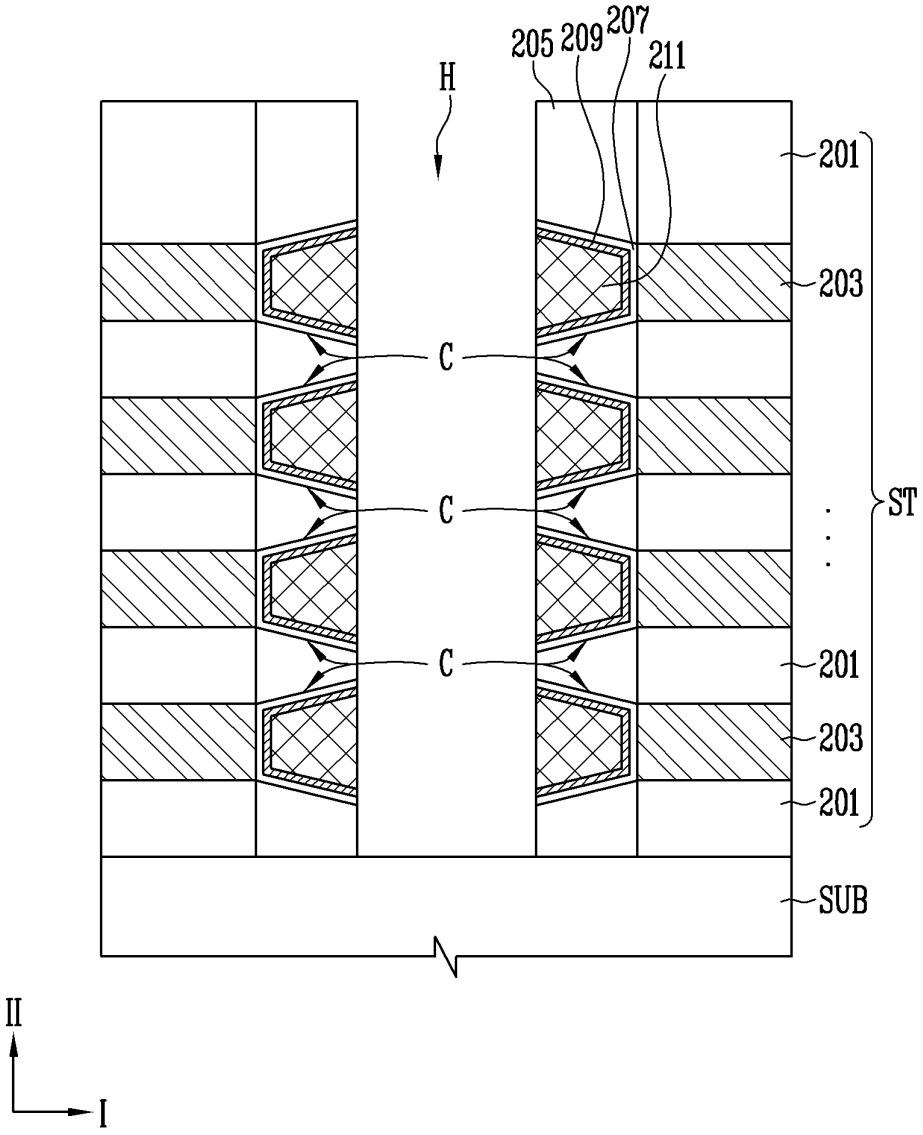


FIG. 7D

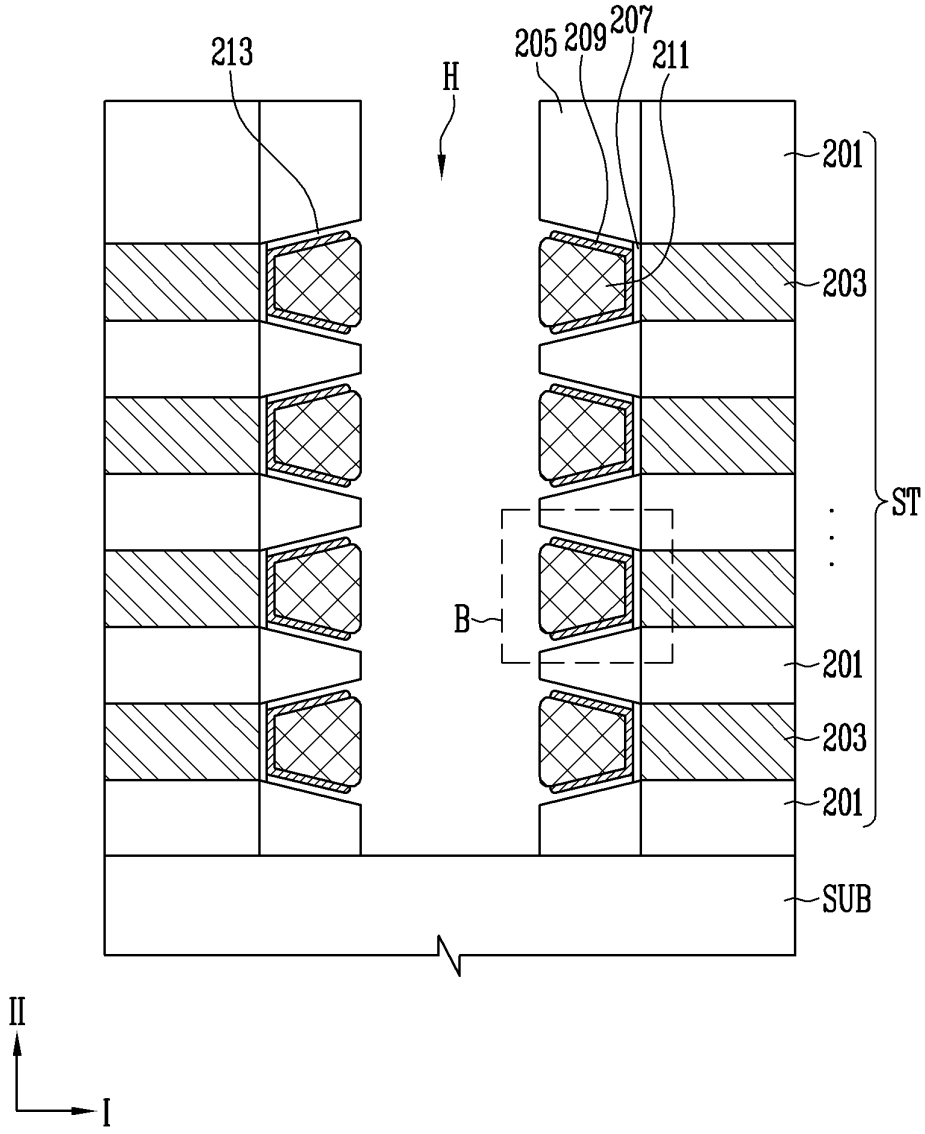


FIG. 7E

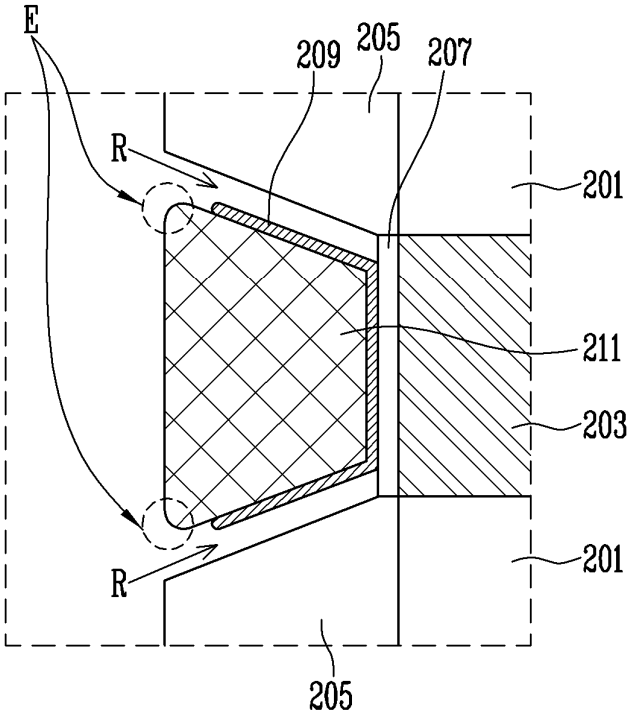


FIG. 7F

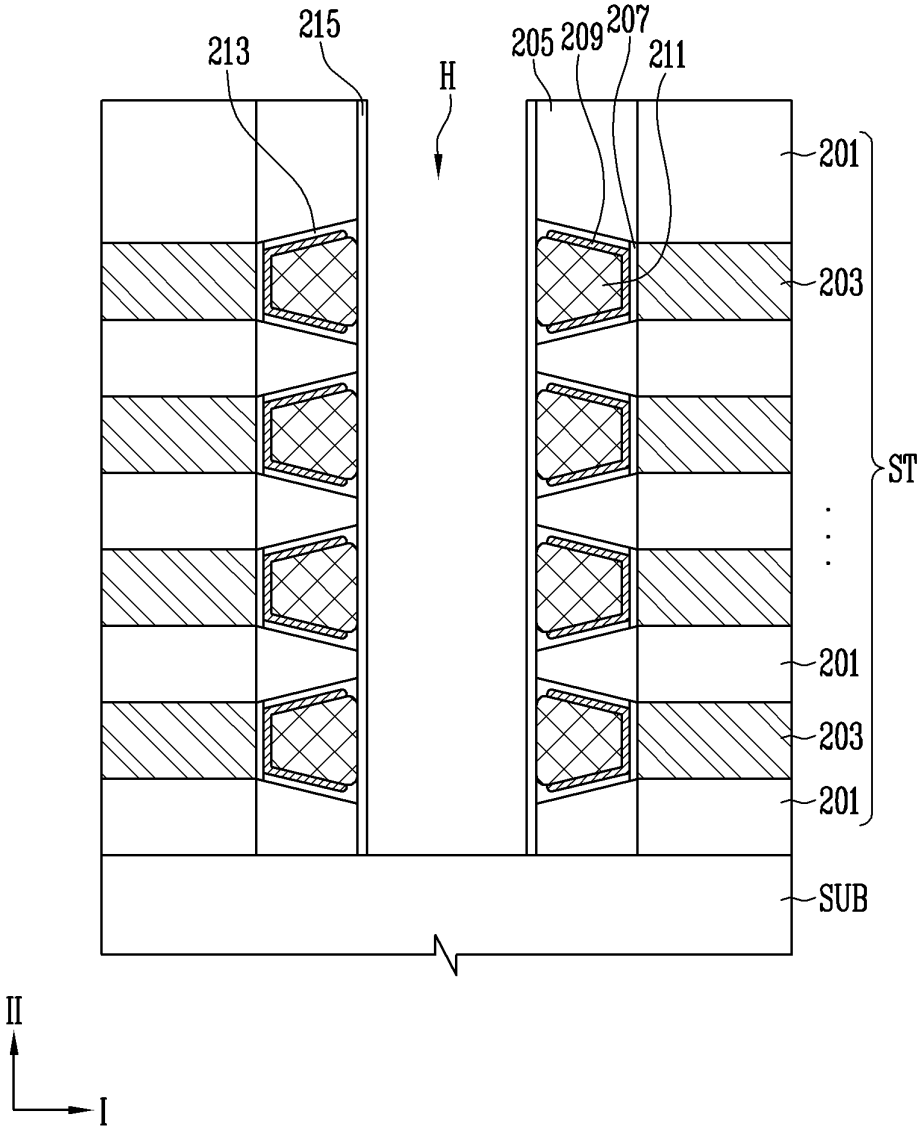


FIG. 7G

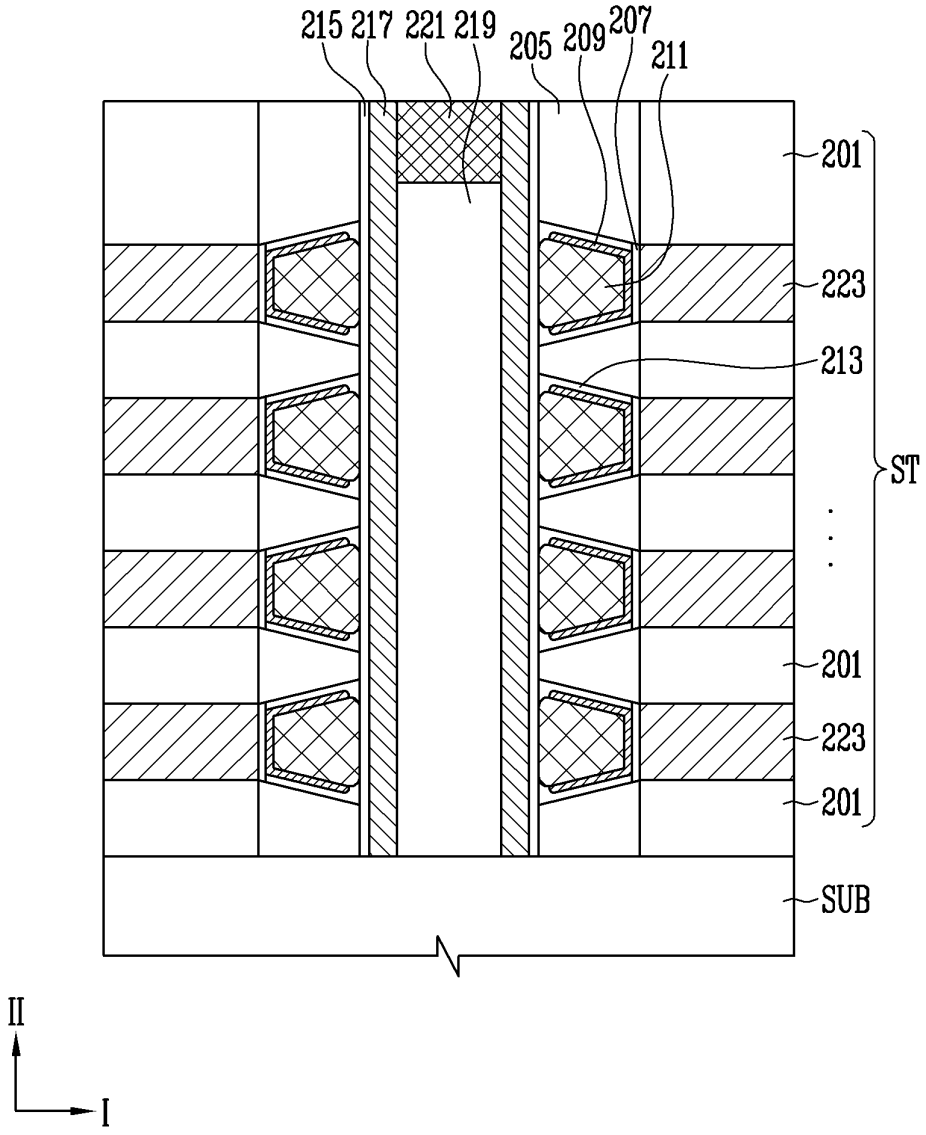

215
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201
223
213
ST
201
223
201
SUB
II
I

FIG. 8

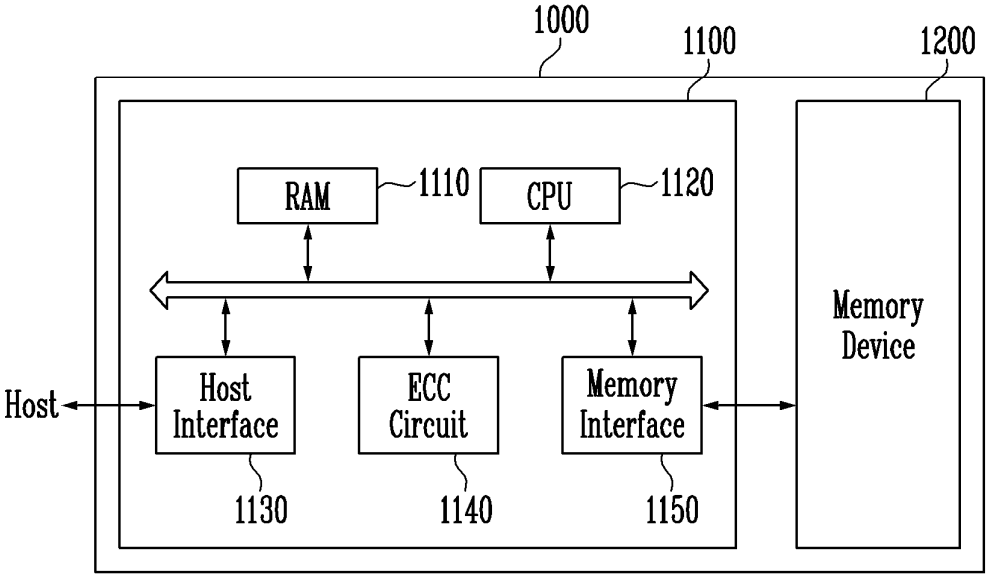

1000
1100
1200
RAM
1110
CPU
1120
Host
Host Interface
ECC Circuit
Memory Interface
Memory Device
1130
1140
1150

FIG. 9

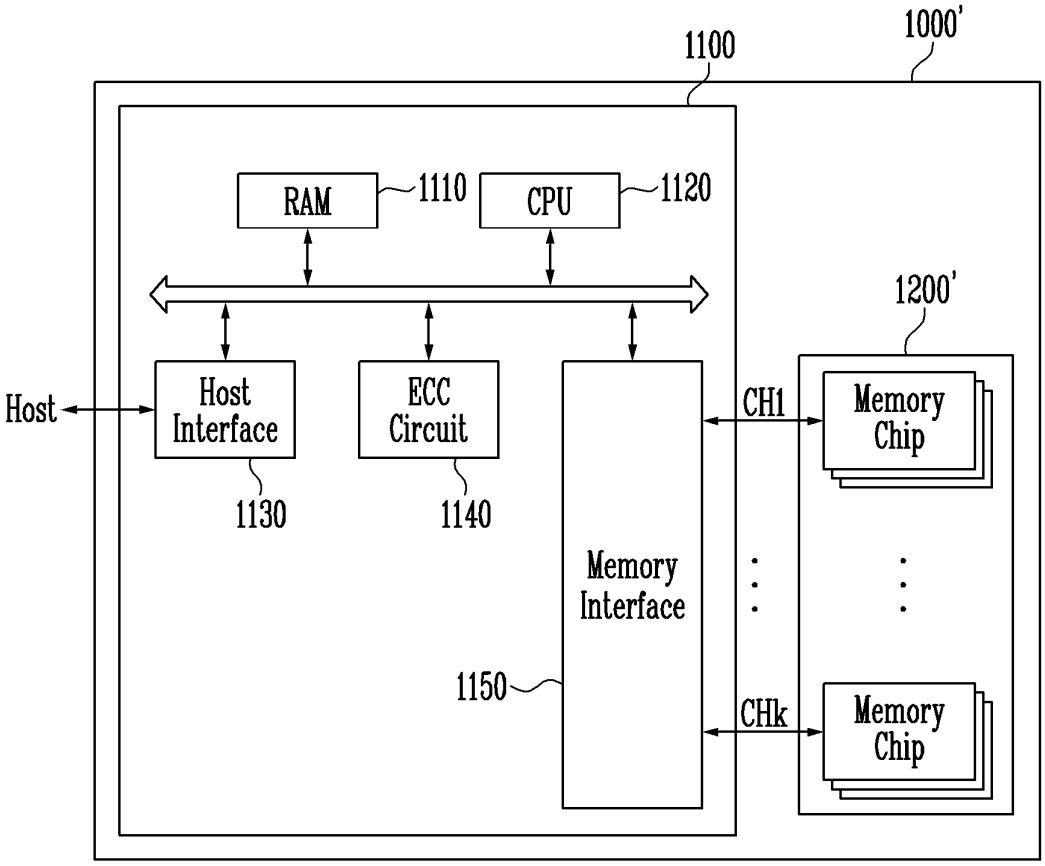

1000'
1100
RAM
1110
CPU
1120
1200'
Host
Host Interface
1130
ECC Circuit
1140
Memory Interface
1150
CH1
CHk
Memory Chip
Memory Chip

FIG. 10

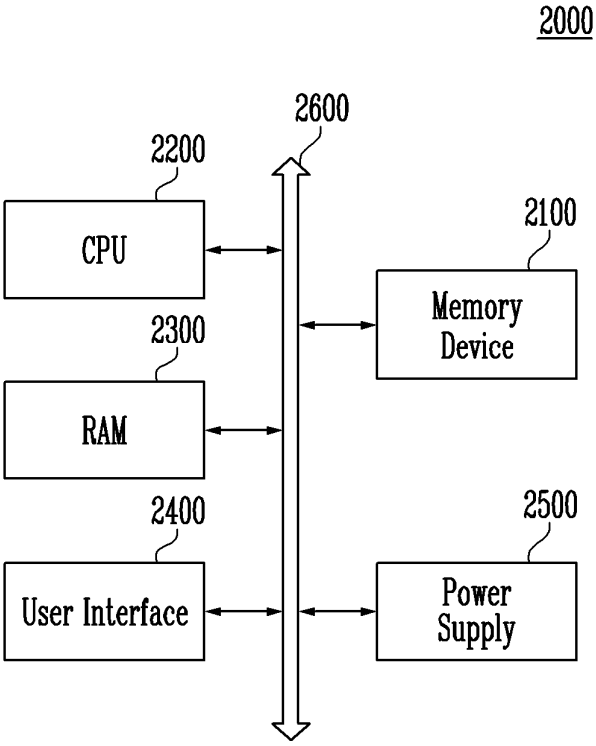
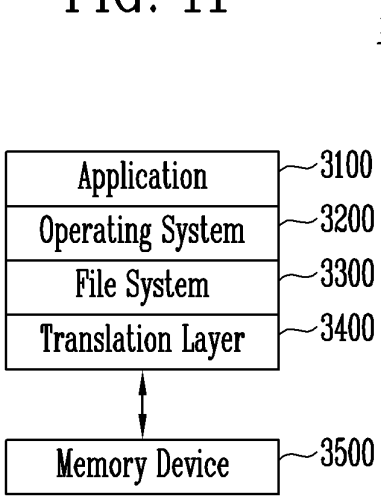

2000
2600
2200
CPU
2100
Memory Device
2300
RAM
2400
User Interface
2500
Power Supply

FIG. 11


3000
Application
3100
Operating System
3200
File System
3300
Translation Layer
3400
Memory Device
3500