

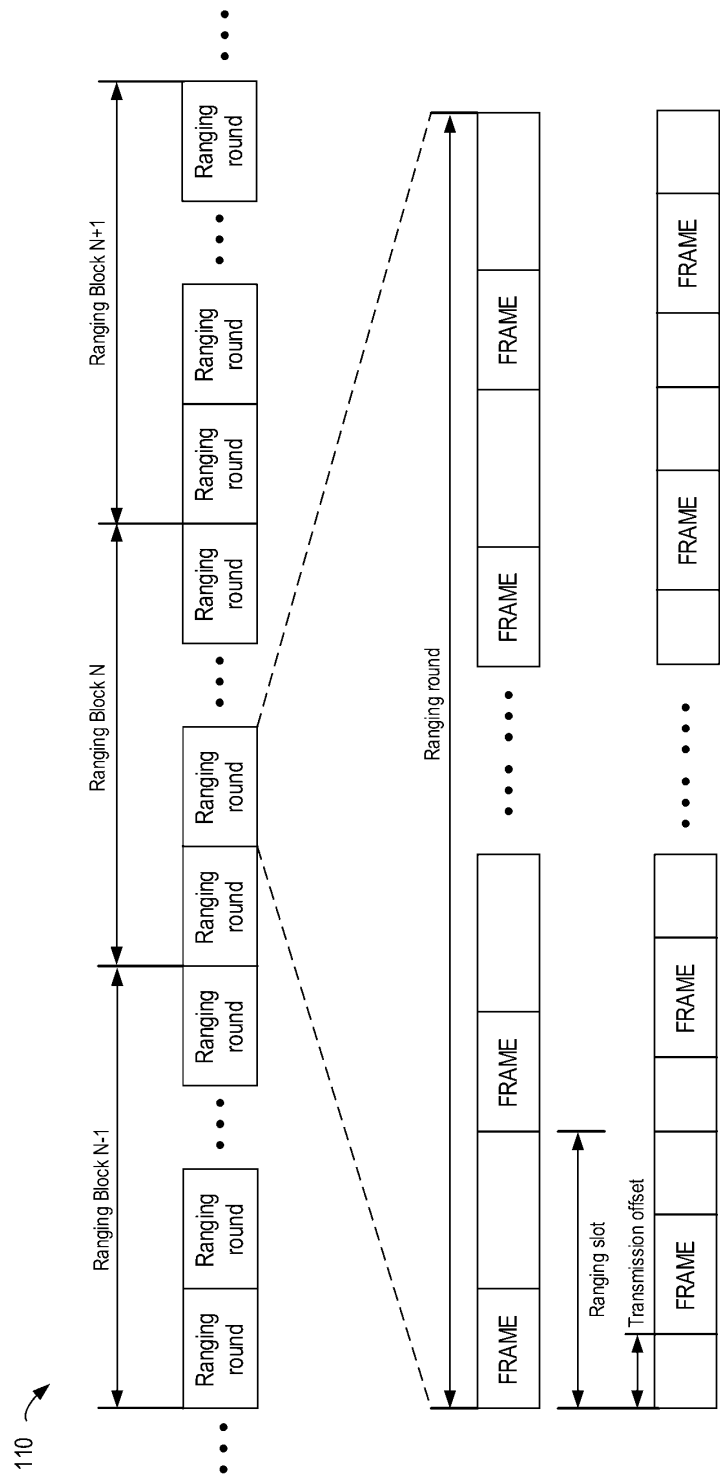


FIG. 1A

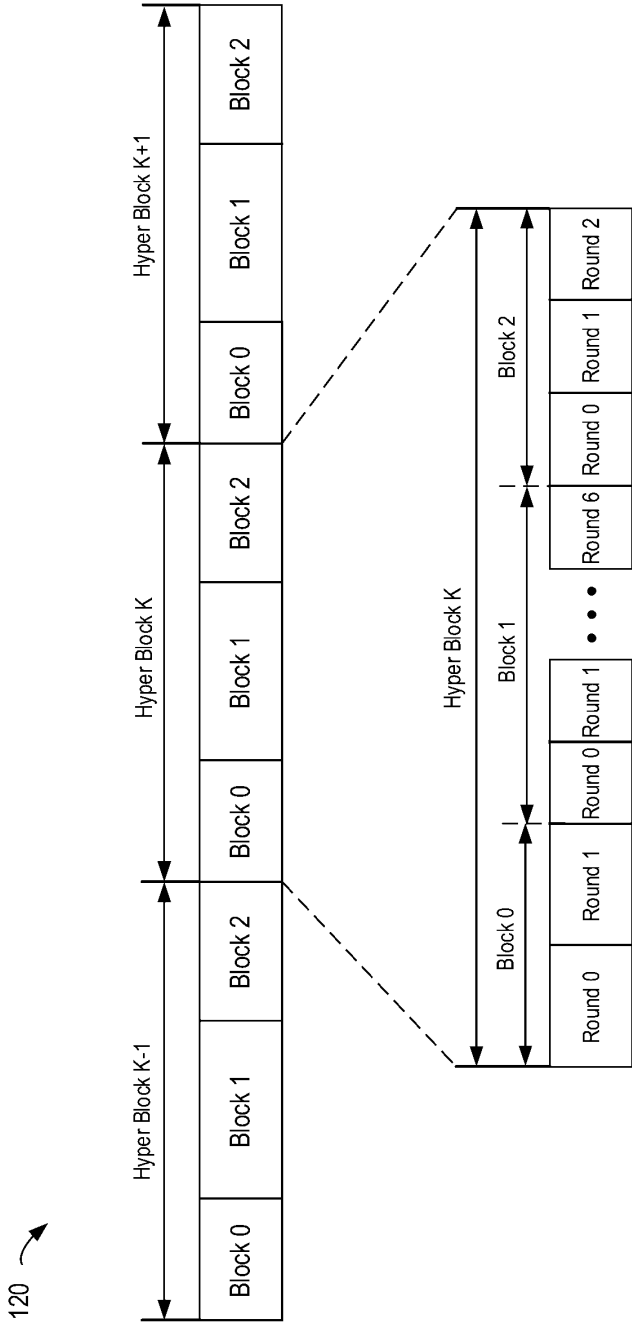


FIG. 1B

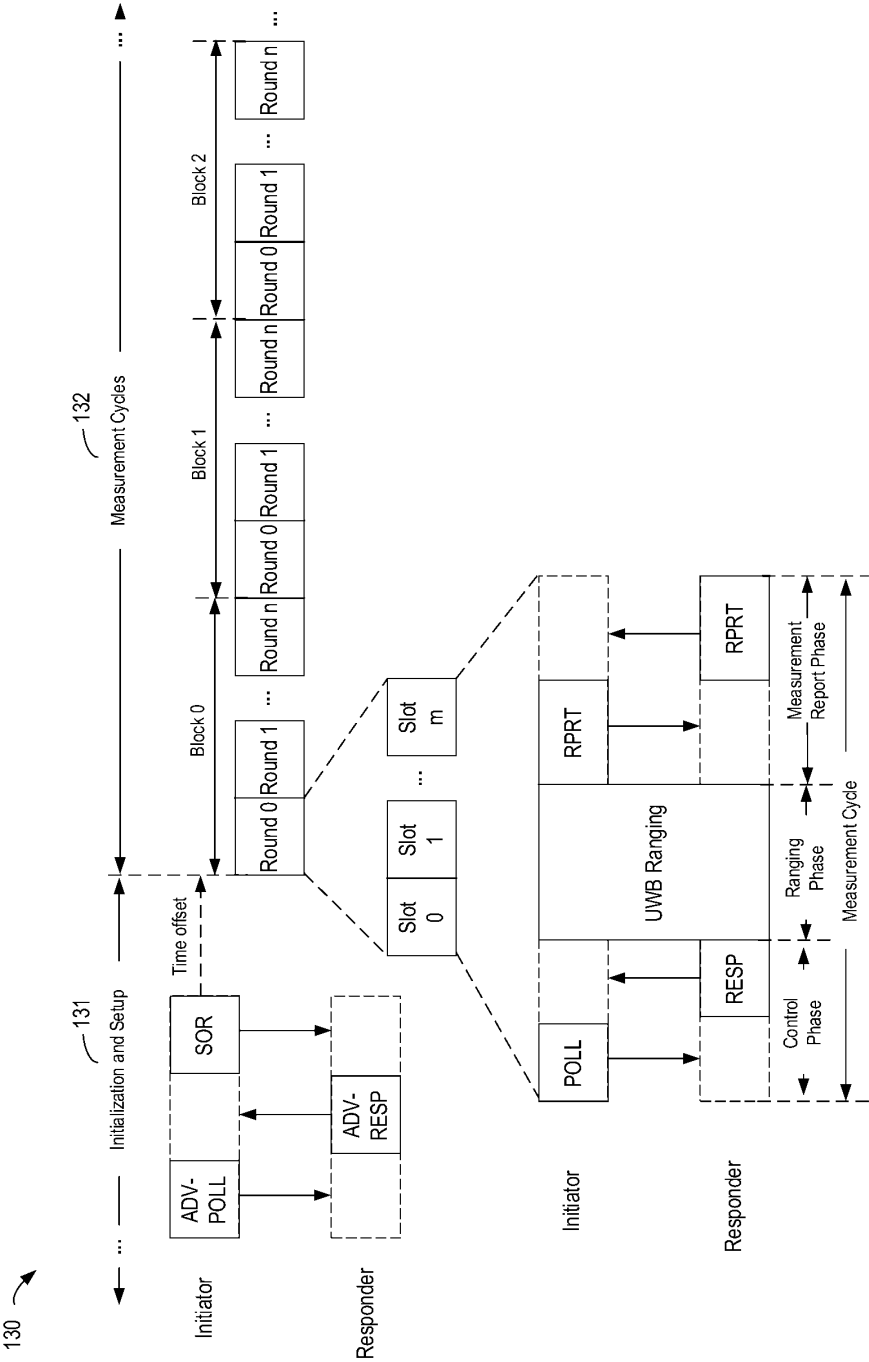


FIG. 1C

140 ↘

Octets: 1	2	variable	2
ID	Address	Payload	CRC

FIG. 1D

150 ↘

Octets: 1/2	2	1	variable	2
Frame Control	Address	Header IE message ID (128~255)	Payload	CRC

FIG. 1E

160 ↘

Octets: 8	4	1
Source Address	Frame Counter	Nonce Security Level

FIG. 1F

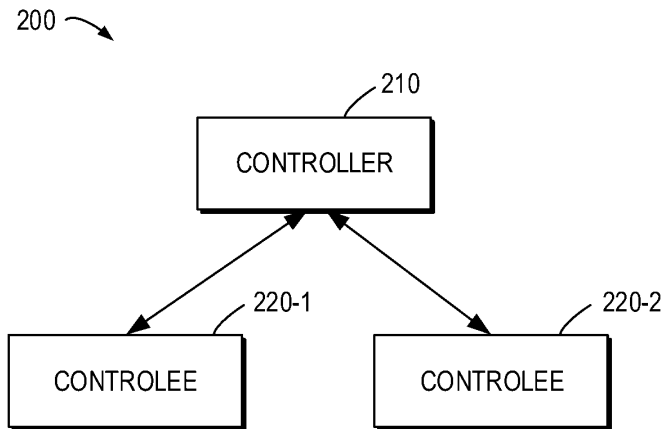


FIG. 2A

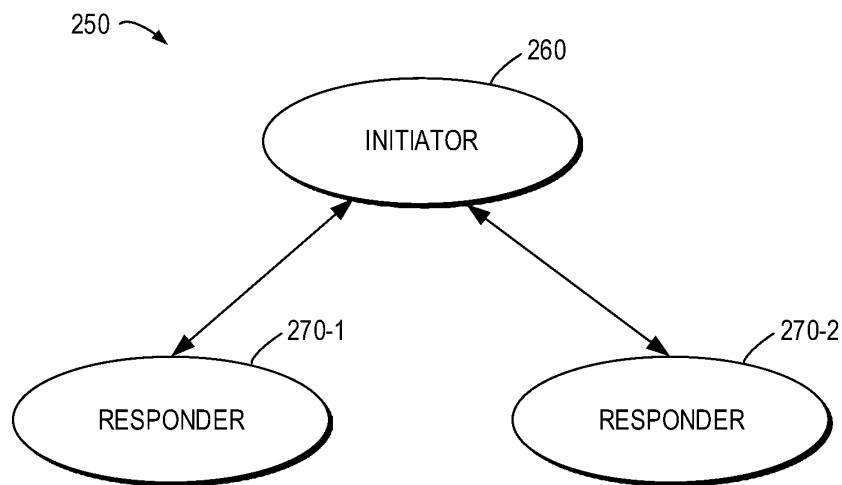


FIG. 2B

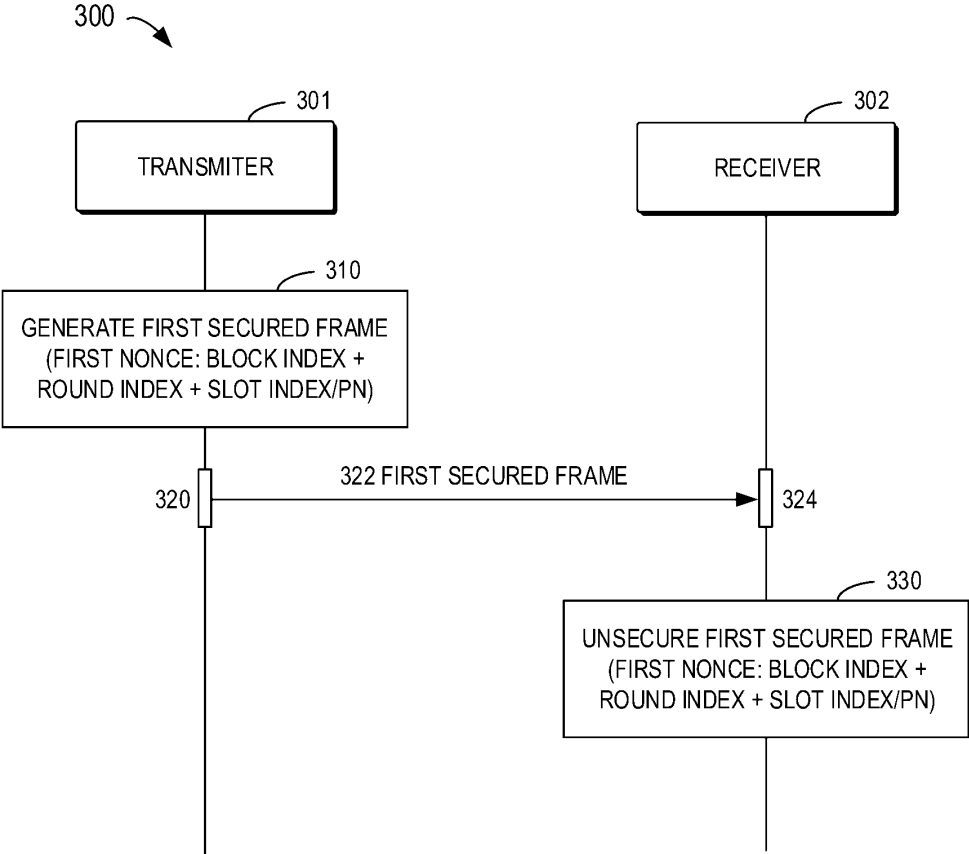


FIG. 3

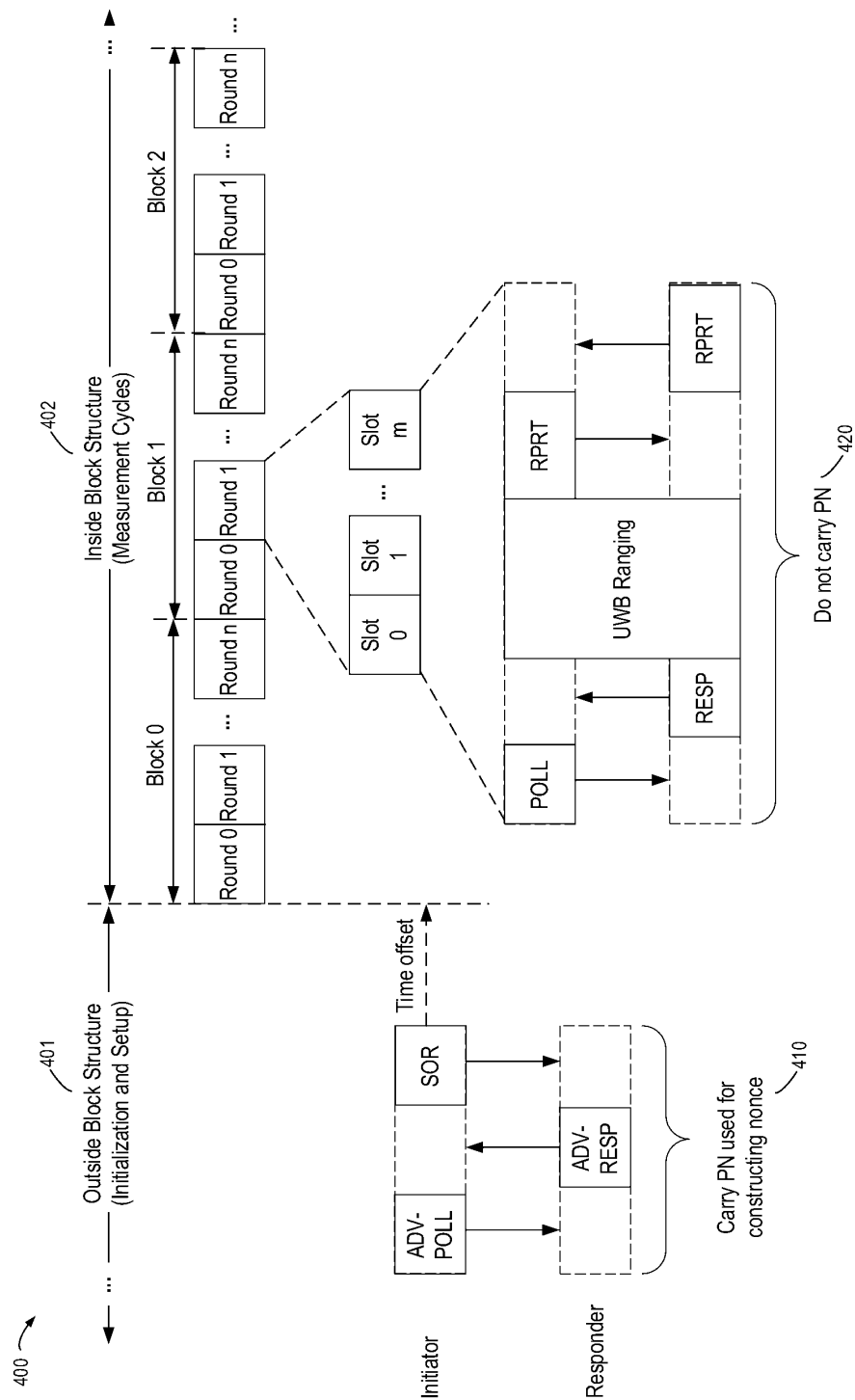


FIG. 4

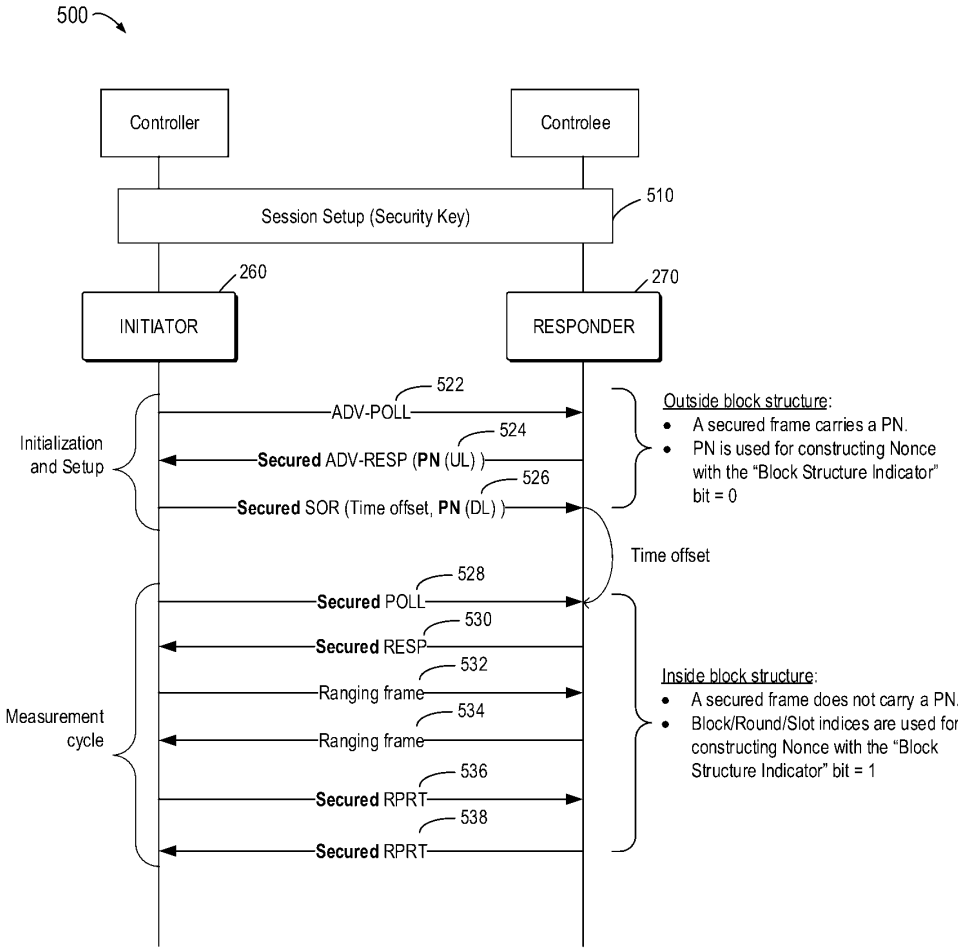


FIG. 5

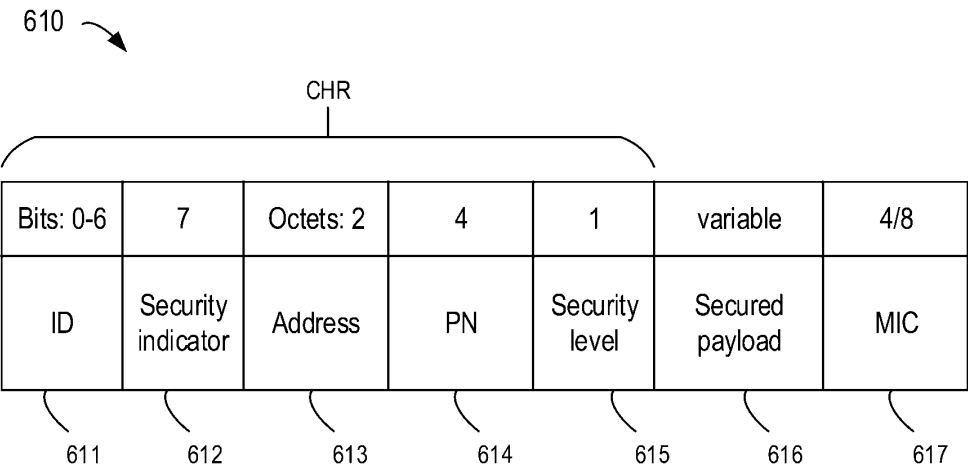


FIG. 6A

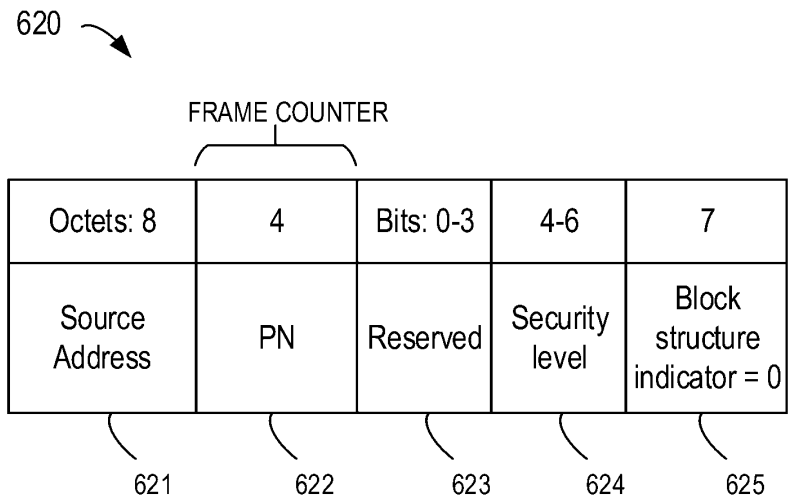


FIG. 6B

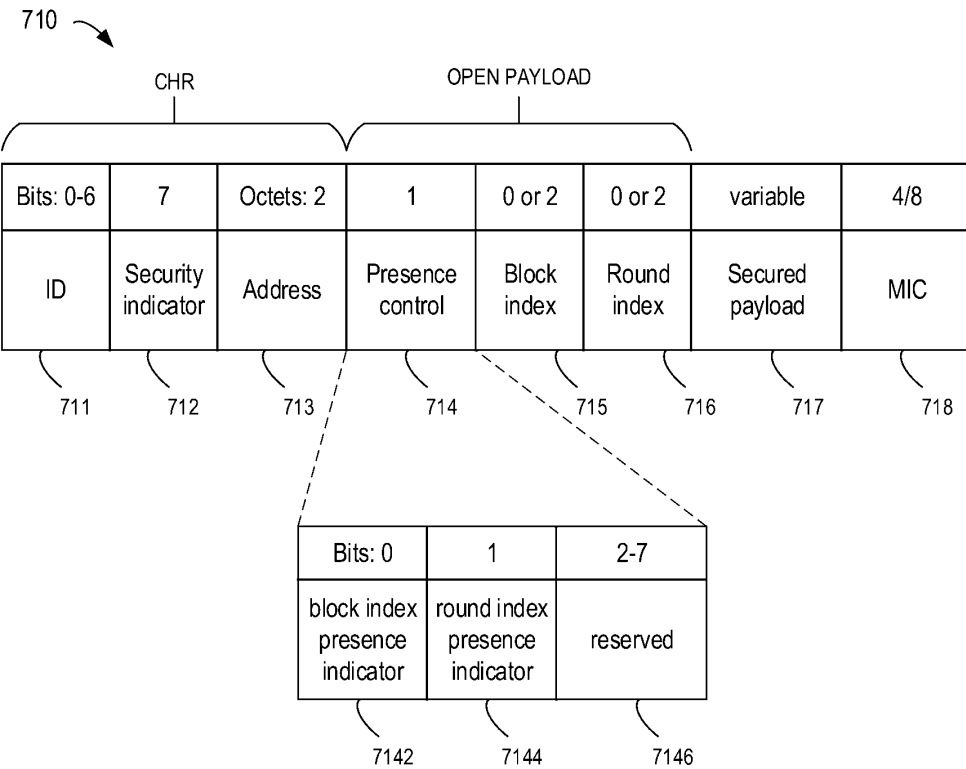


FIG. 7A

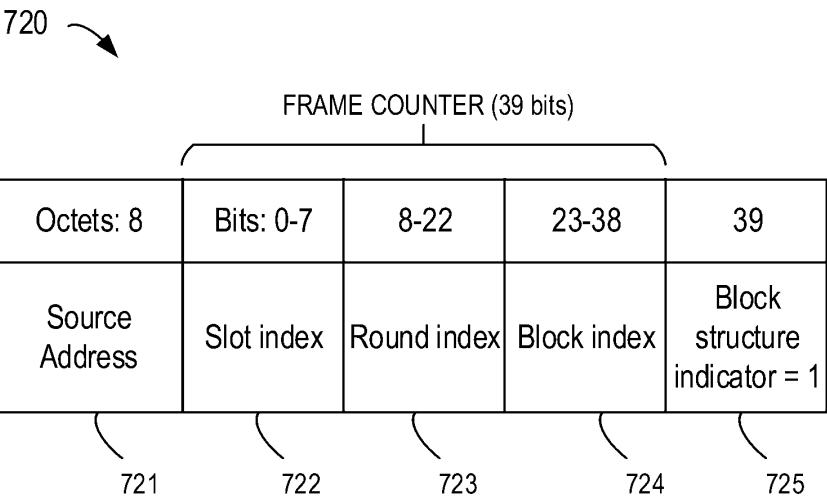


FIG. 7B

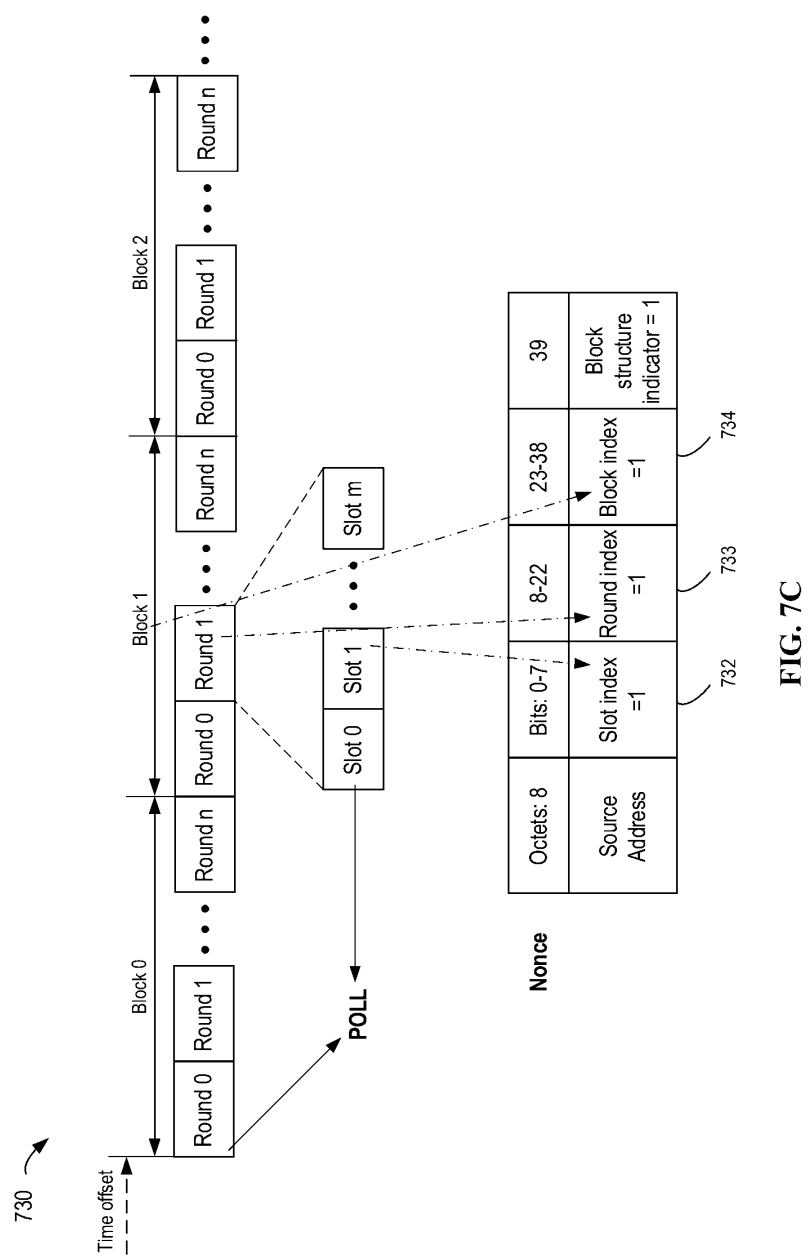


FIG. 7C

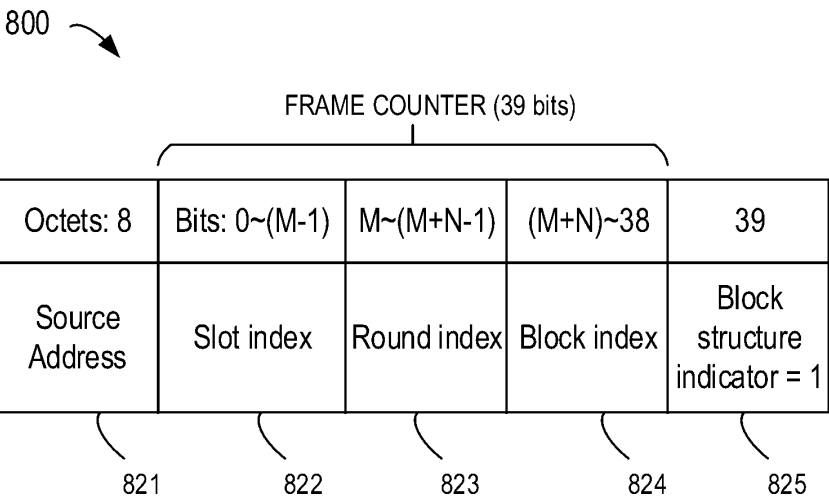


FIG. 8

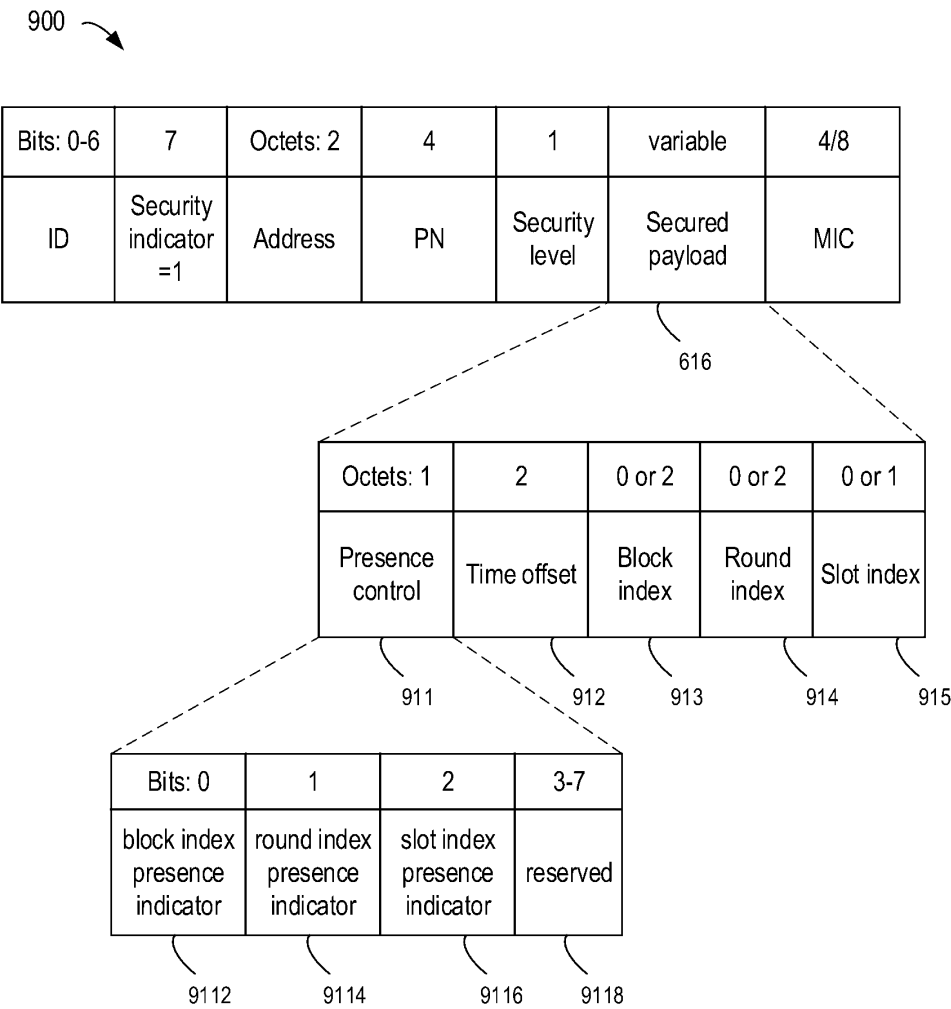


FIG. 9

1010 ↗

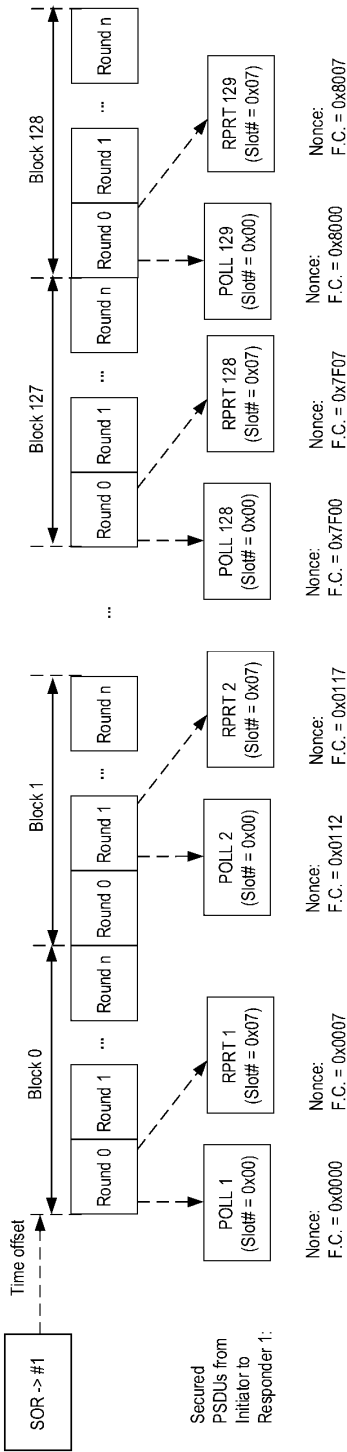


FIG. 10A

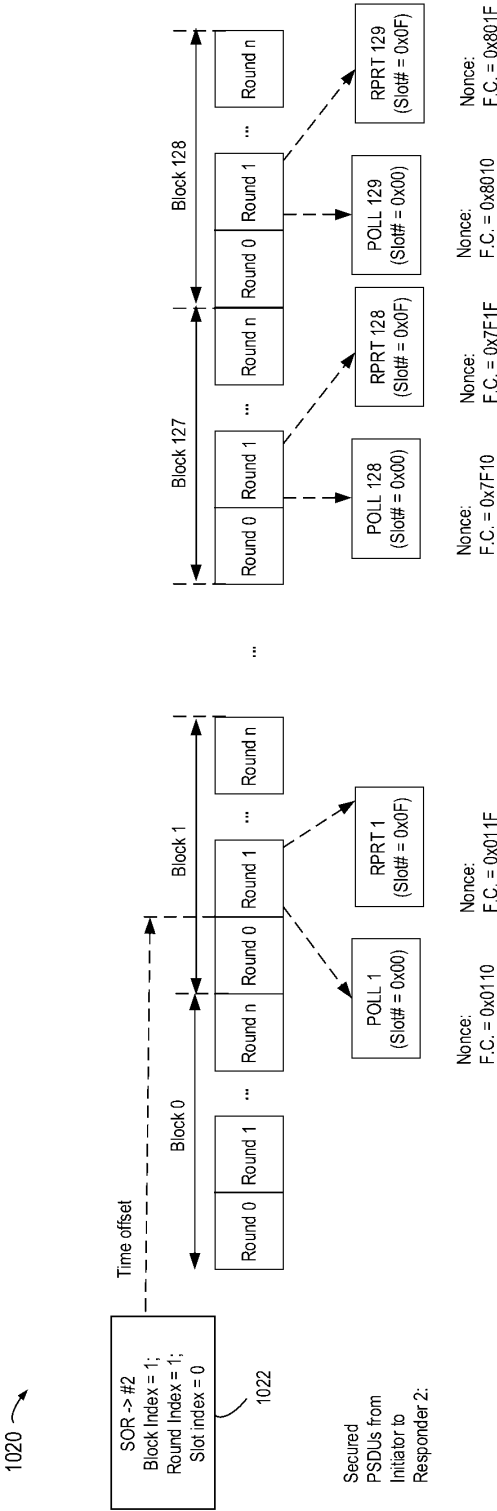


FIG. 10B

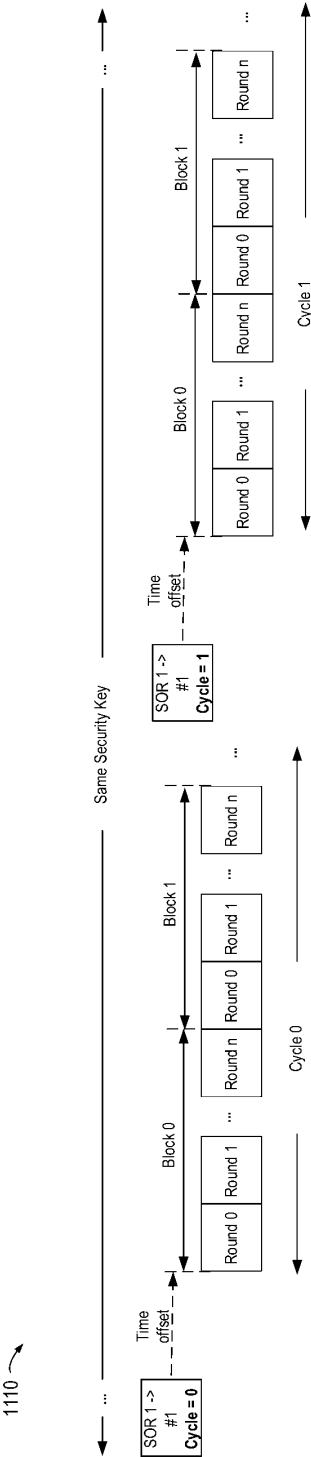


FIG. 11A

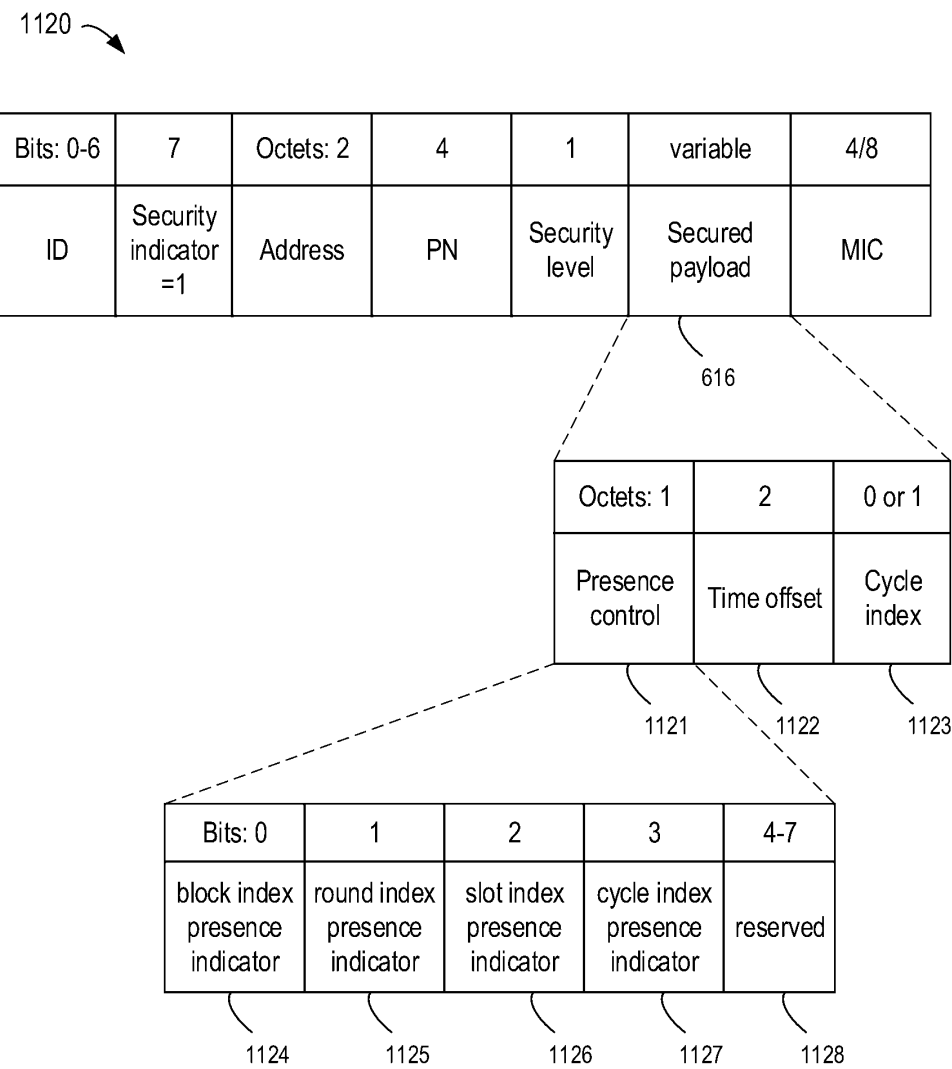


FIG. 11B

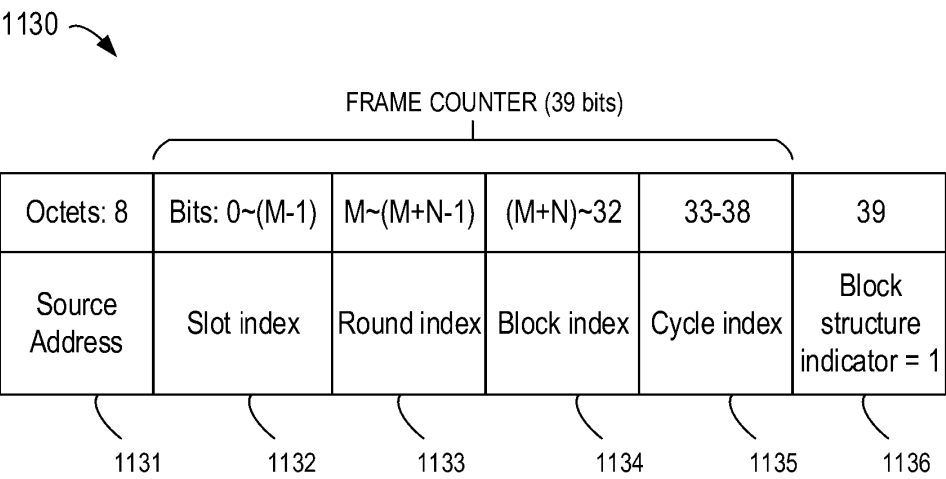


FIG. 11C

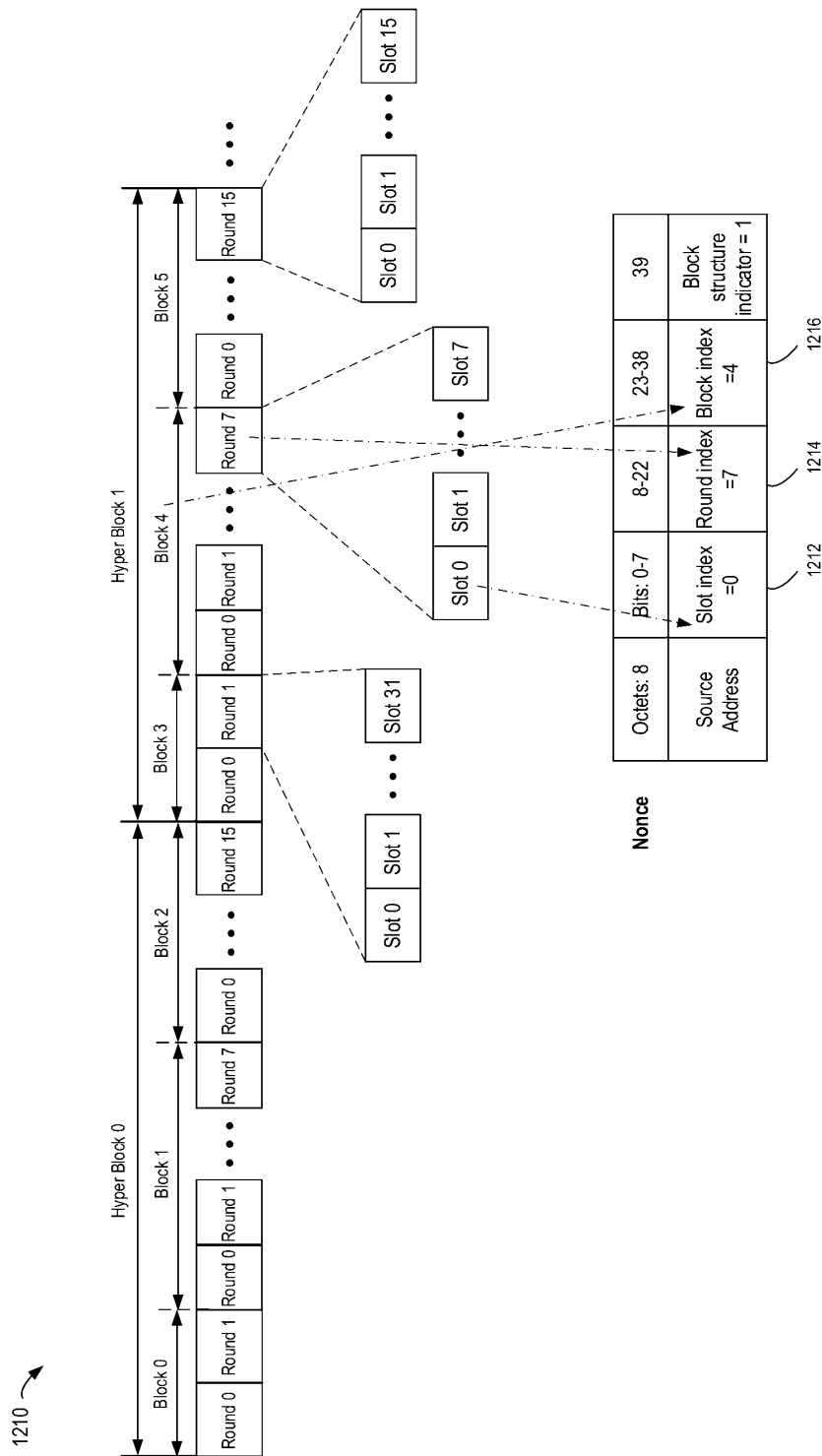


FIG. 12A

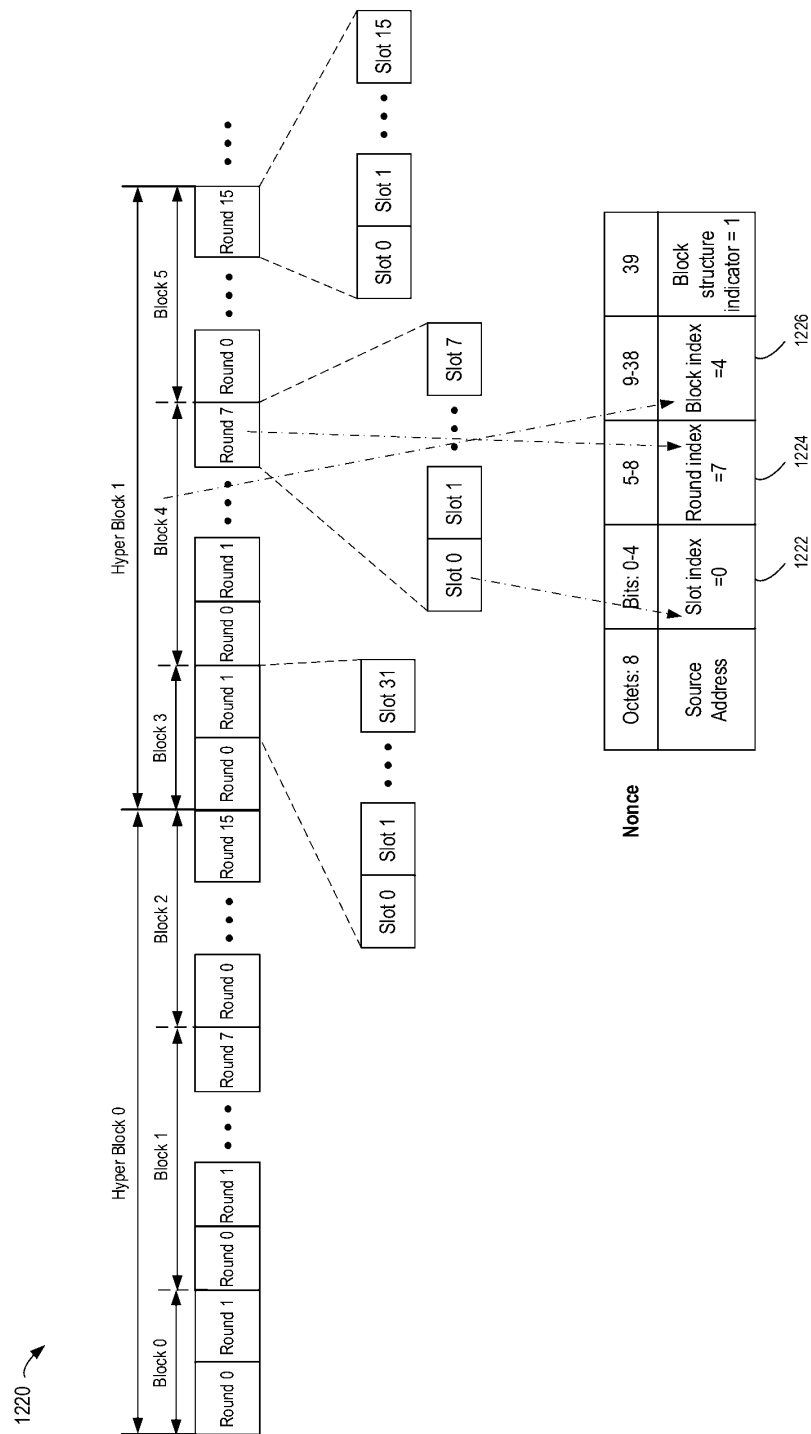


FIG. 12B

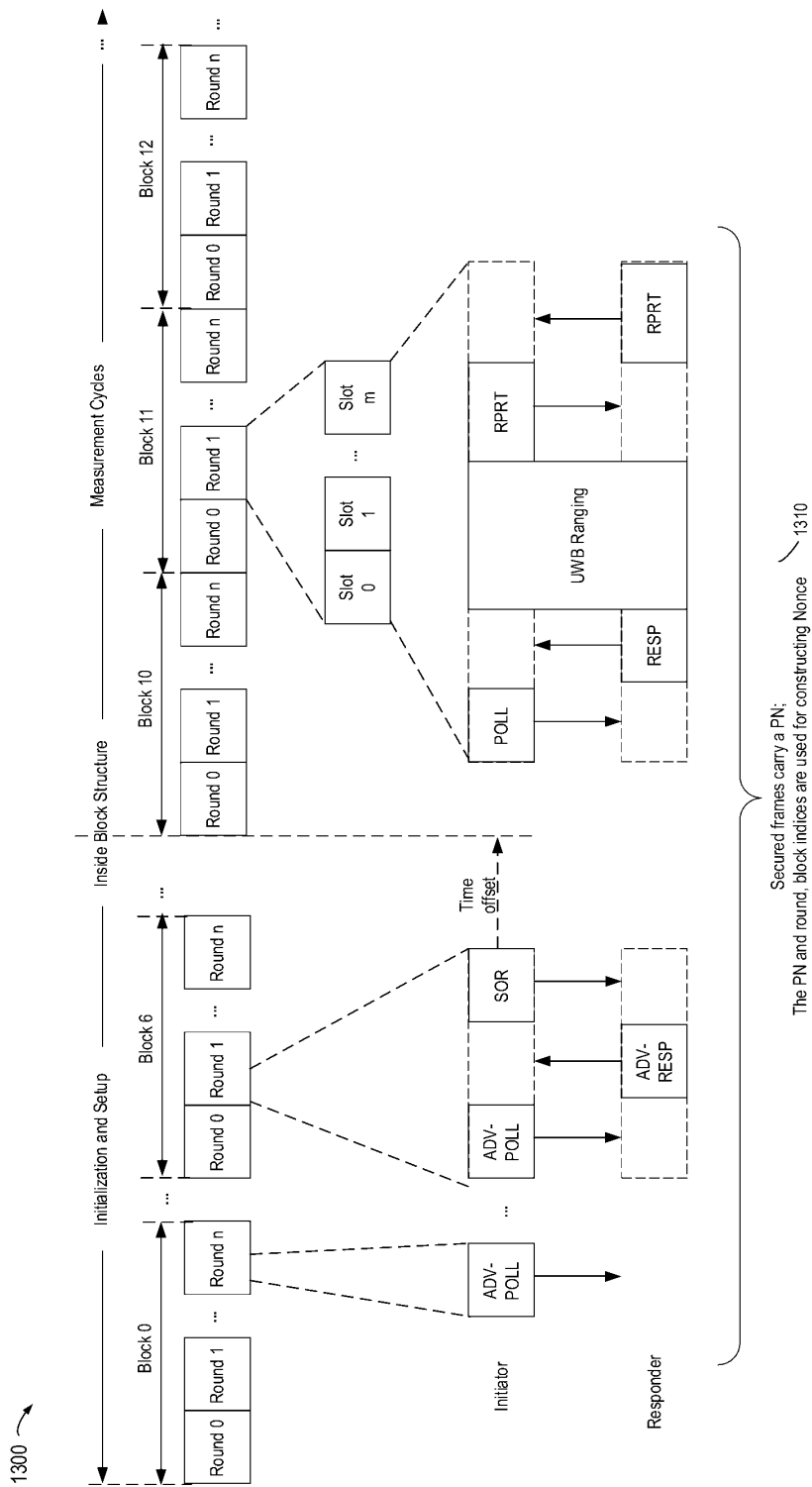
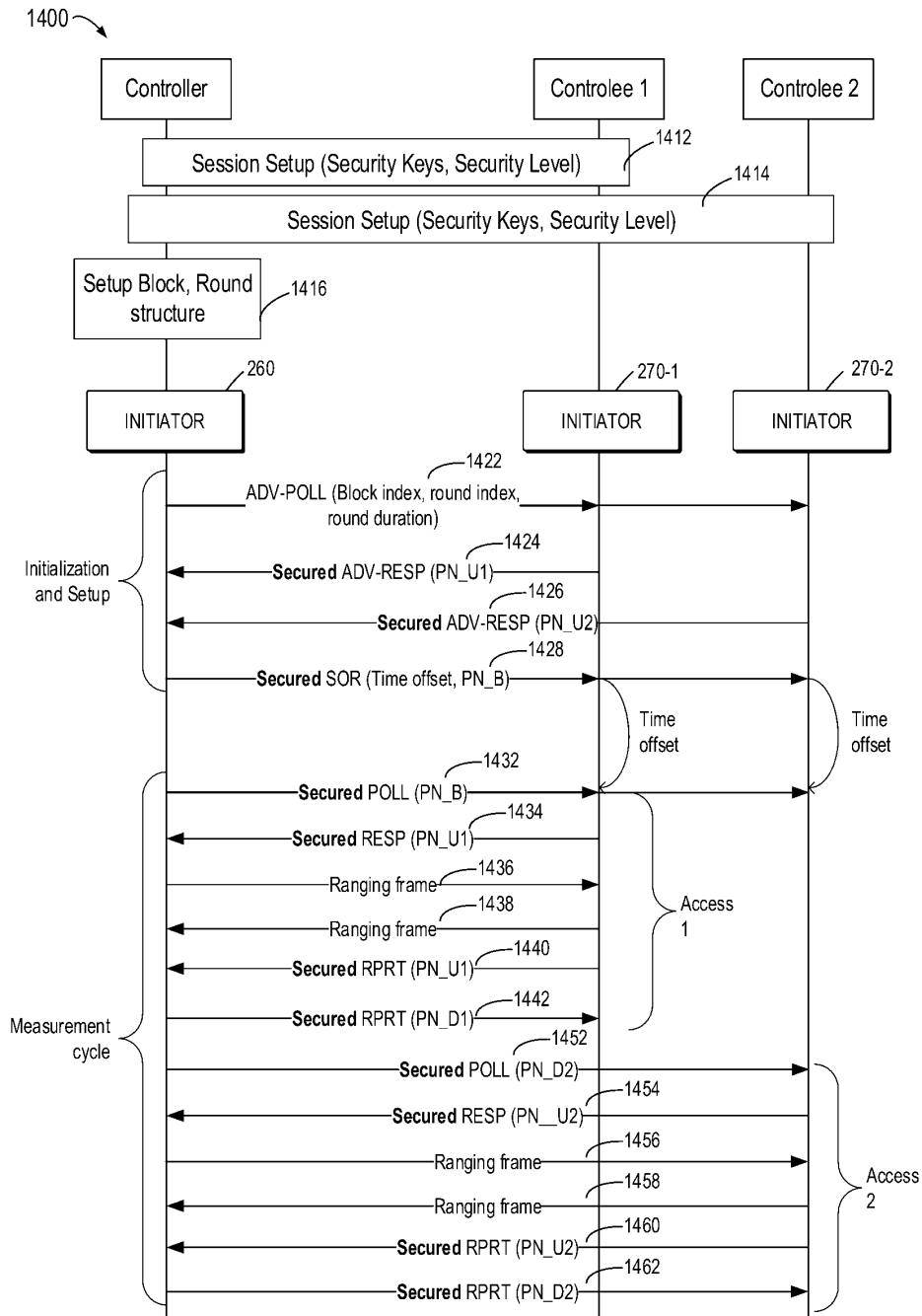


FIG. 13



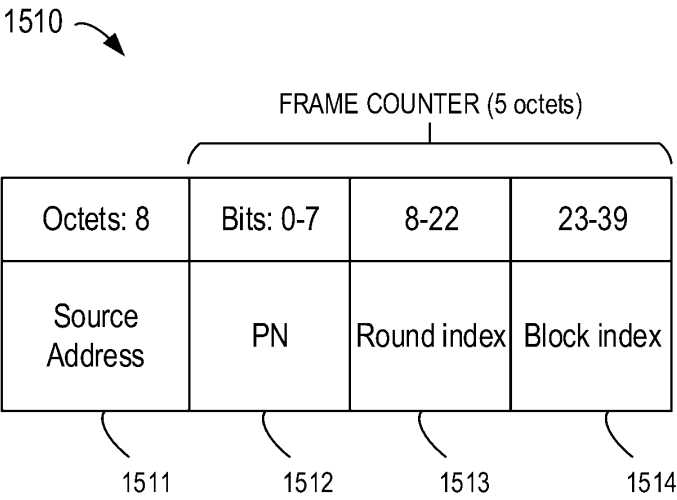


FIG. 15A

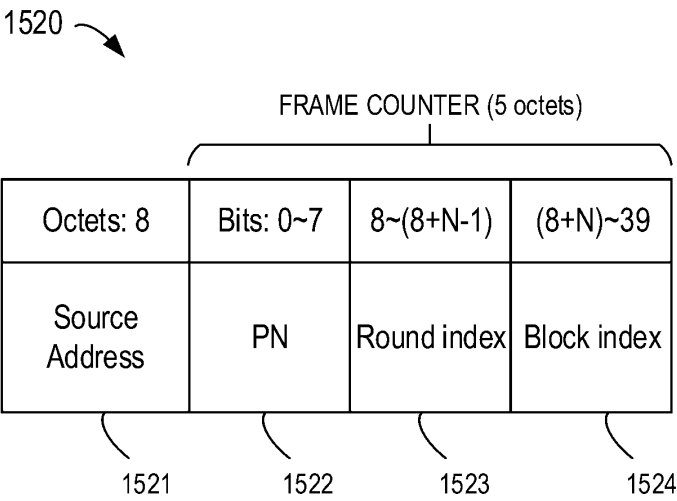


FIG. 15B

1610

Bits: 0-6	7	Octets: 2	1	variable	4/8
ID	Security indicator	Address	PN	Secured payload	MIC

1611

1614

1613

1614

1615

1616

FIG. 16A

1620

Bits: 0-6	7	Octets: 2	1	0 or 2	0 or 2	0 or 2	variable	4/8
ID	Security indicator =0	Address	Presence control	Block index	Round index	Round duration	Payload	CRC

1621

1622

1623

1624

1625

1626

1627

1628

1629

Bits: 0	1	2	3-7
block index presence indicator	round index presence indicator	round duration presence indicator	reserved

1681

1682

1683

1684

FIG. 16B

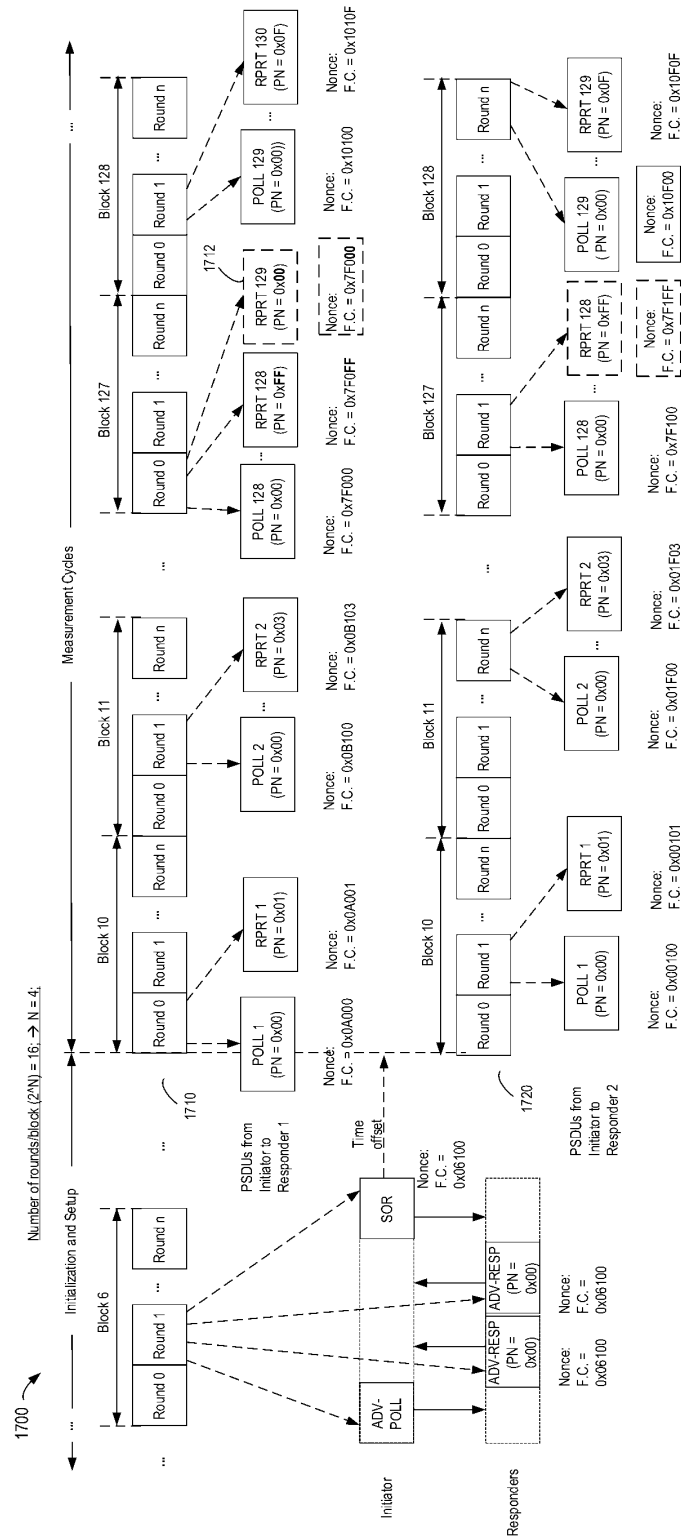


FIG. 17

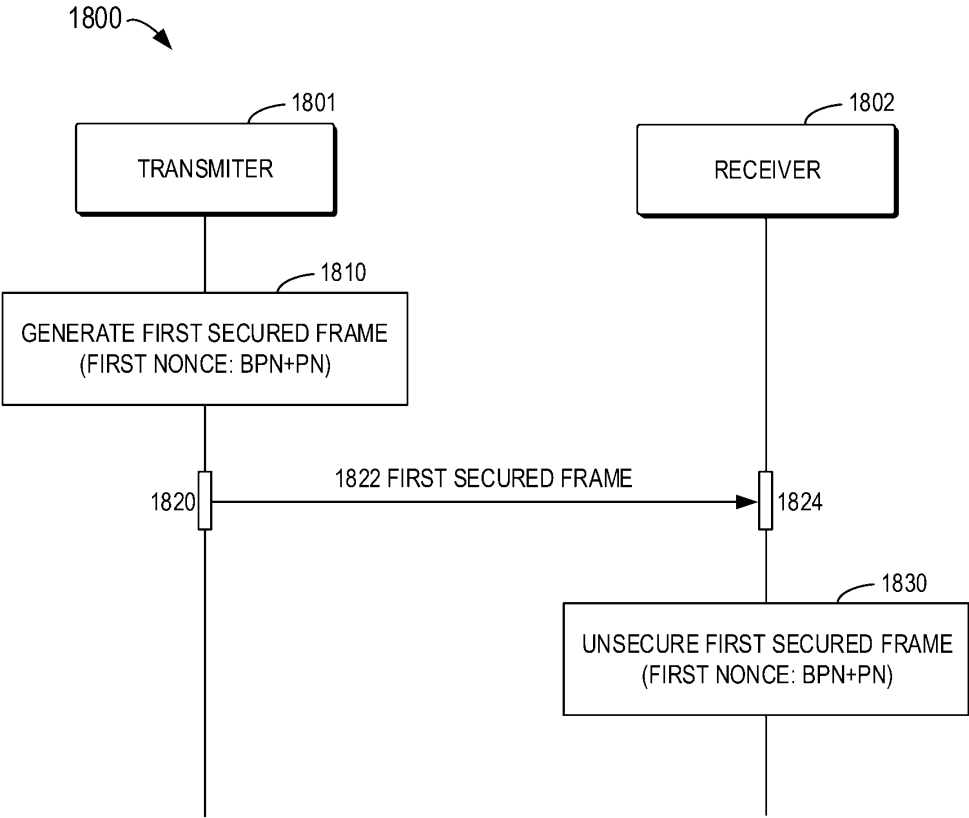


FIG. 18

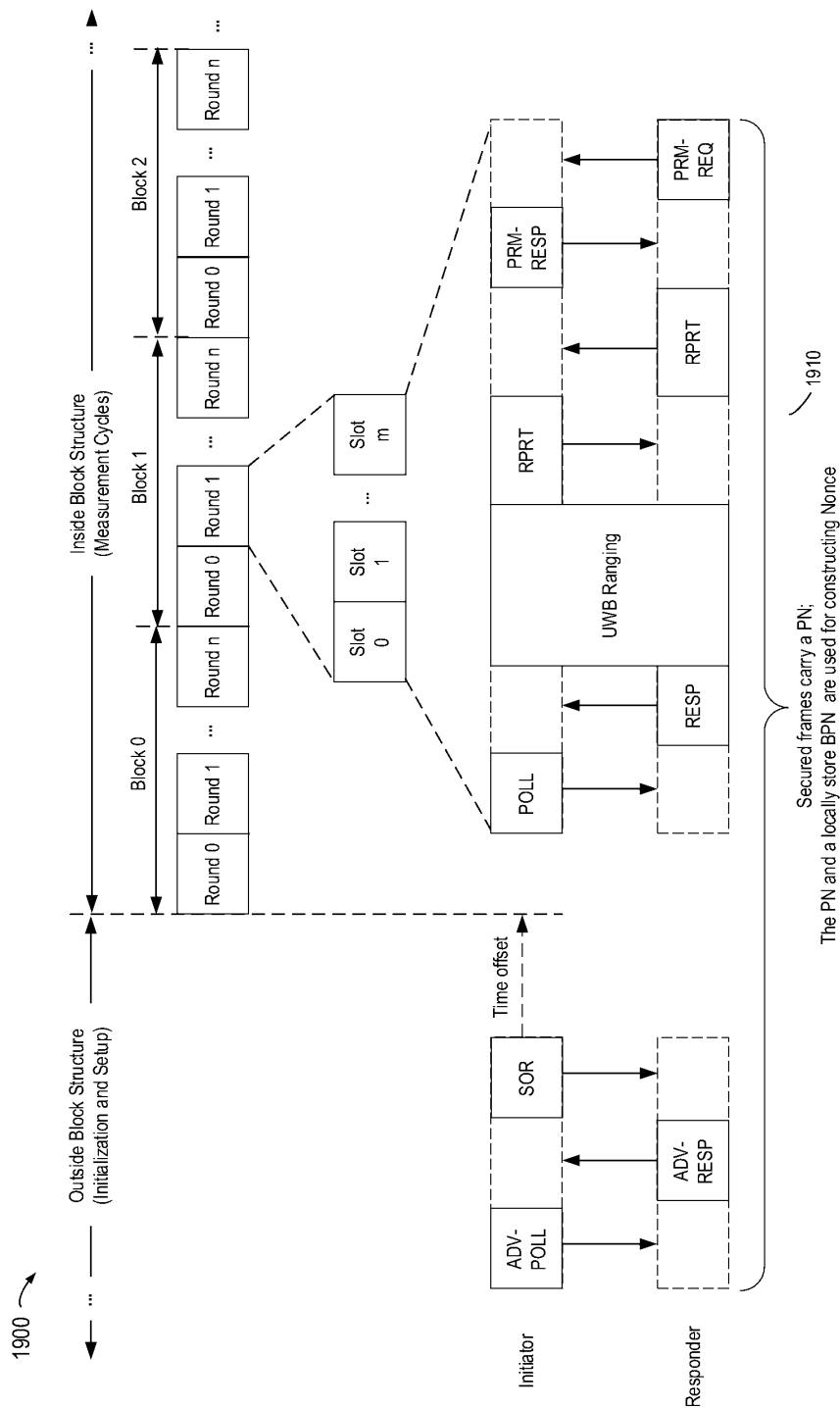


FIG. 19

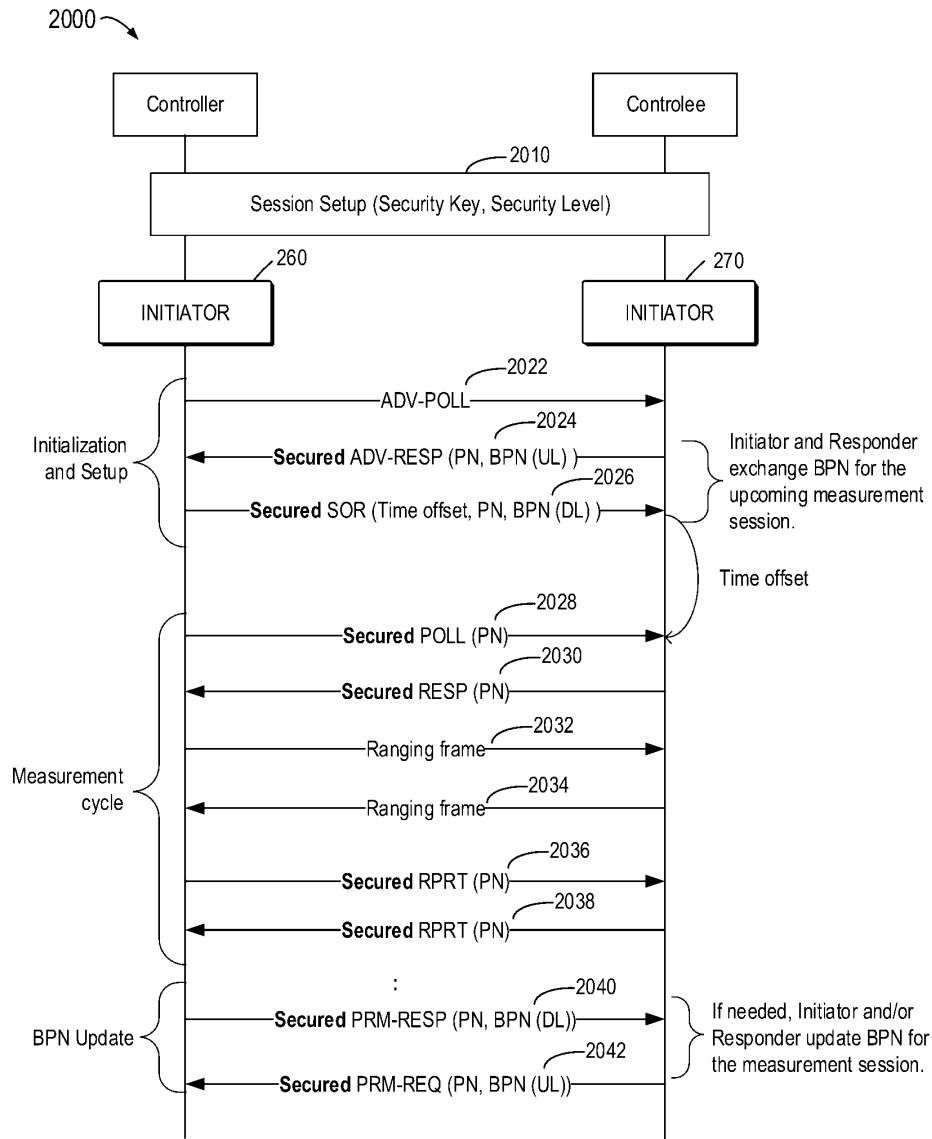


FIG. 20

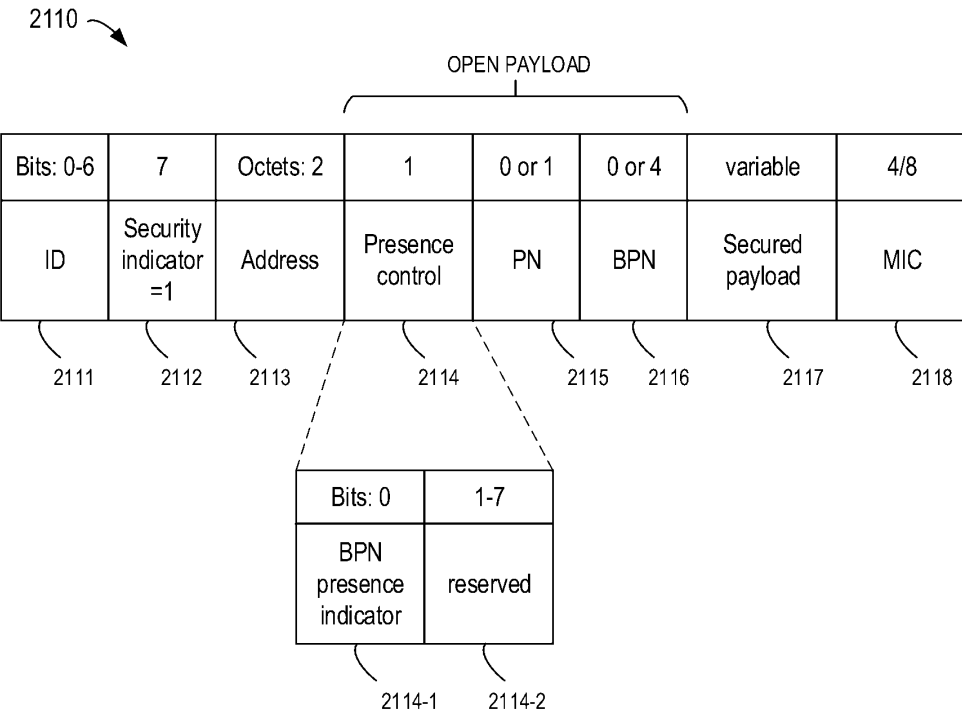


FIG. 21A

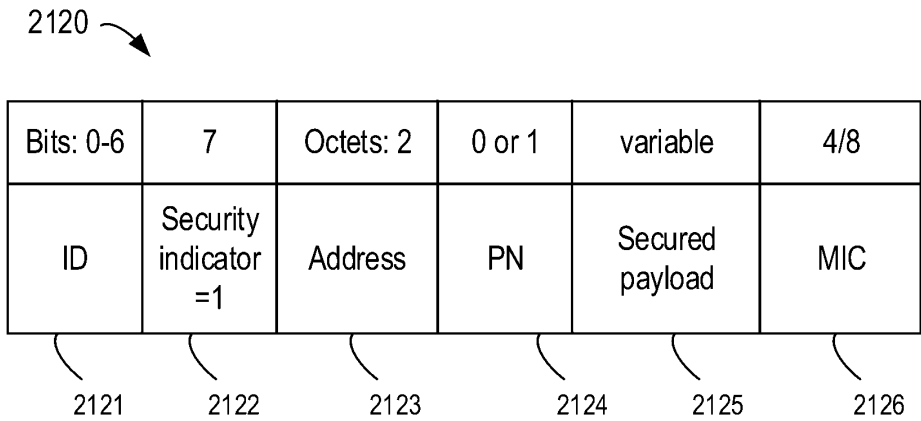


FIG. 21B

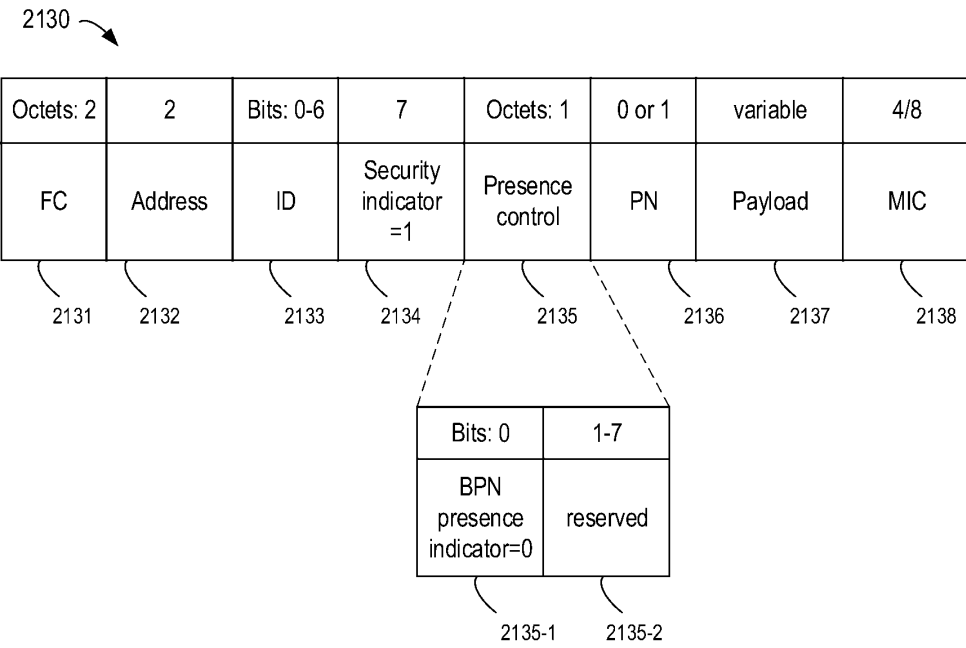


FIG. 21C

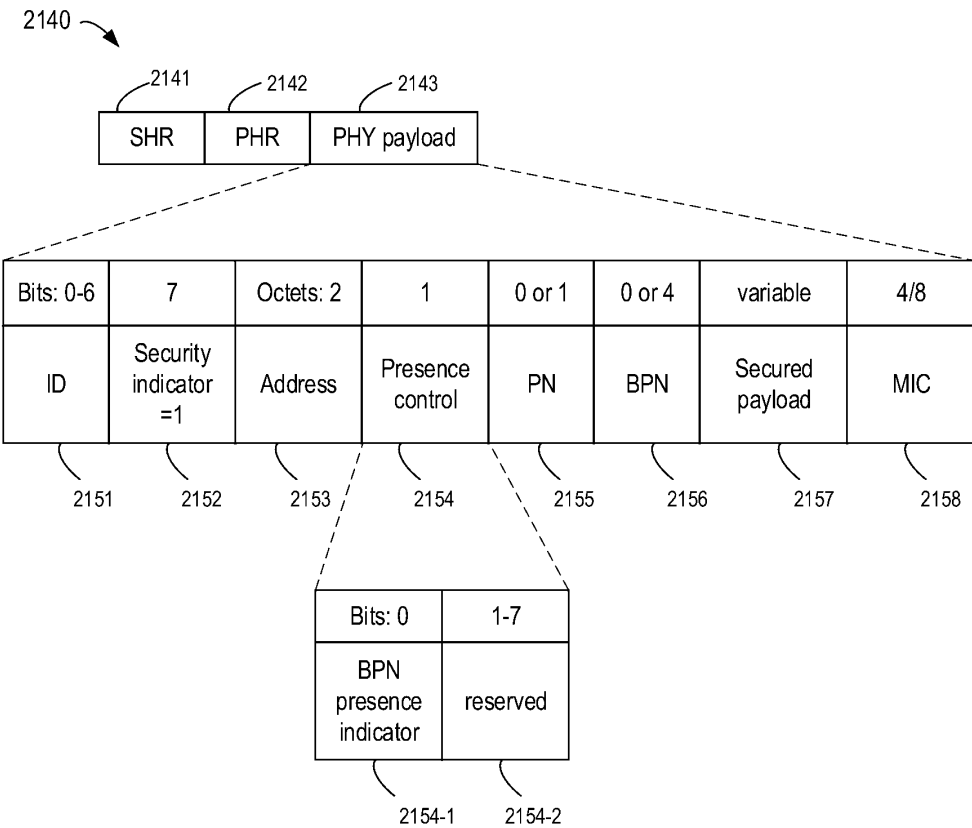


FIG. 21D

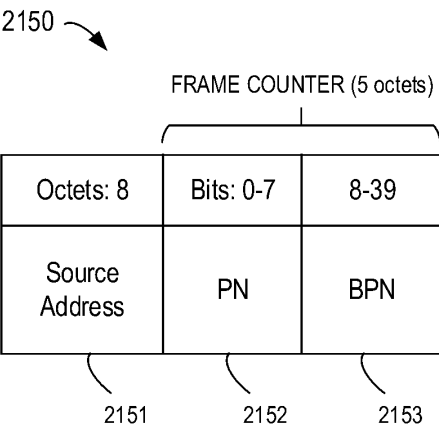


FIG. 21E

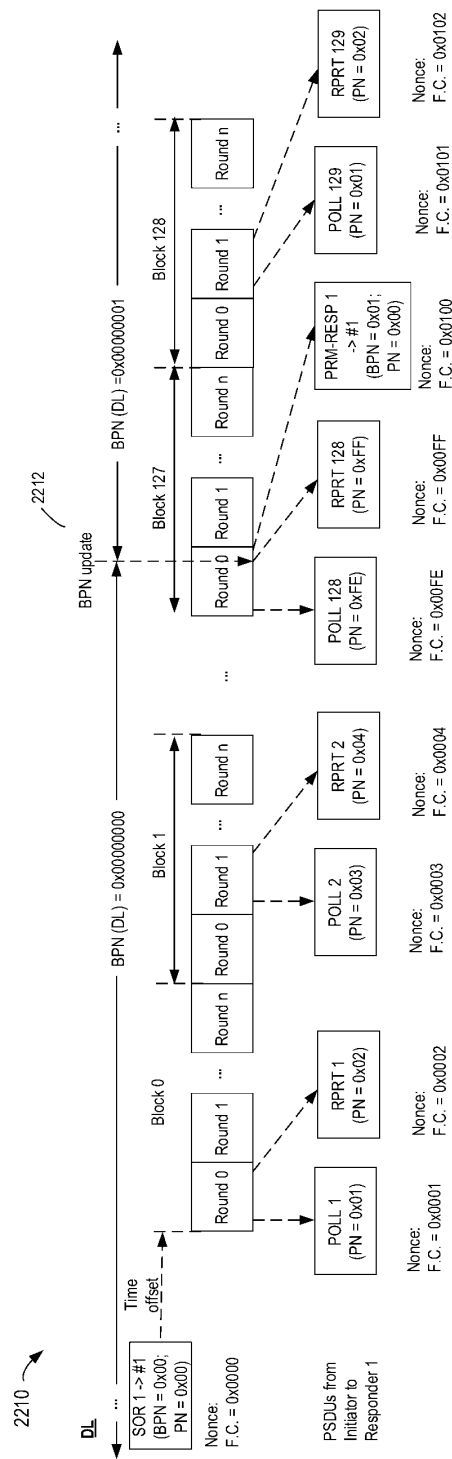


FIG. 22A

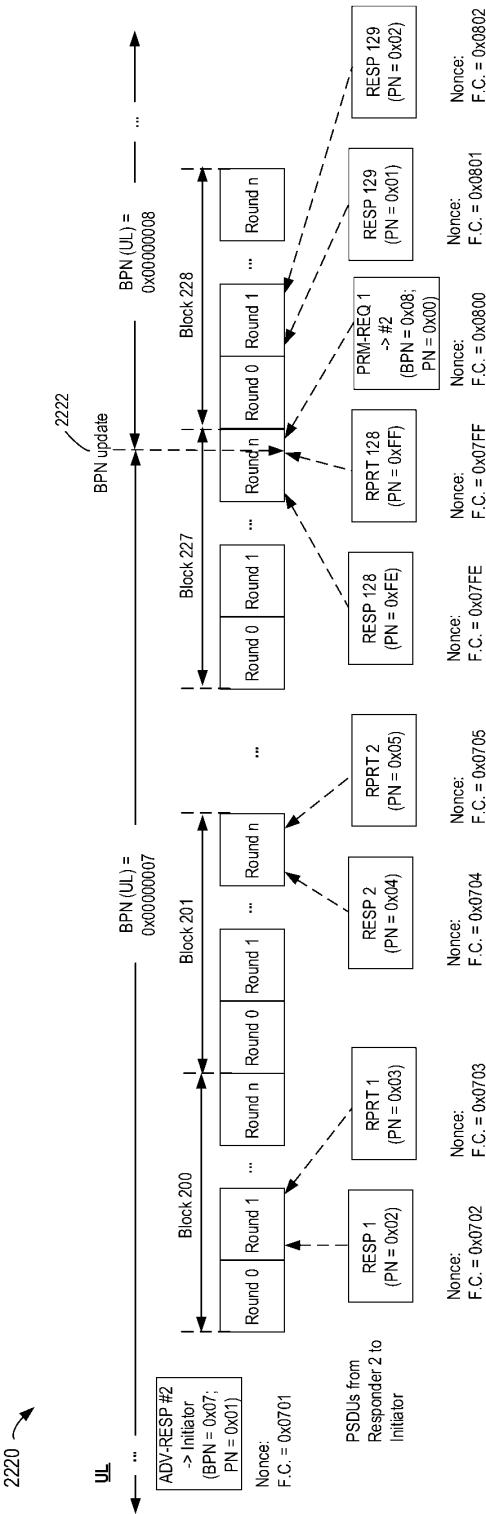


FIG. 22B

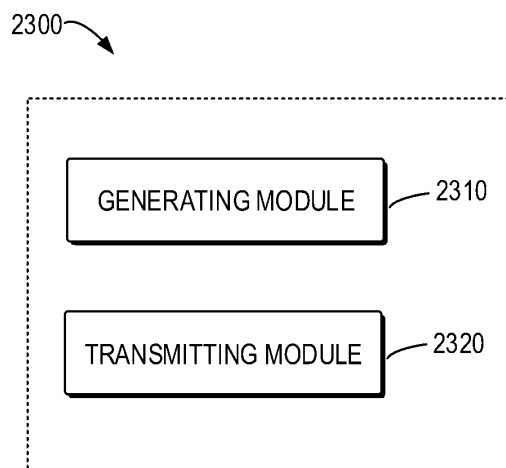


FIG. 23

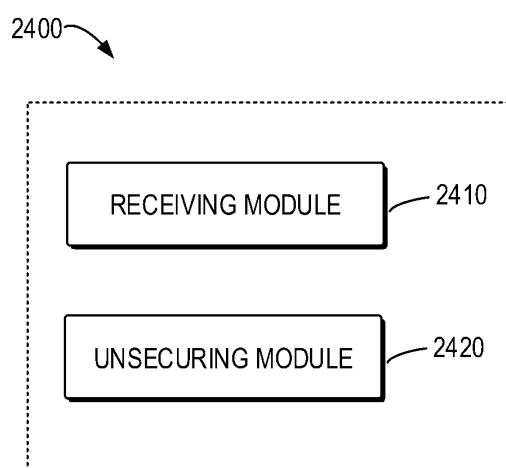


FIG. 24

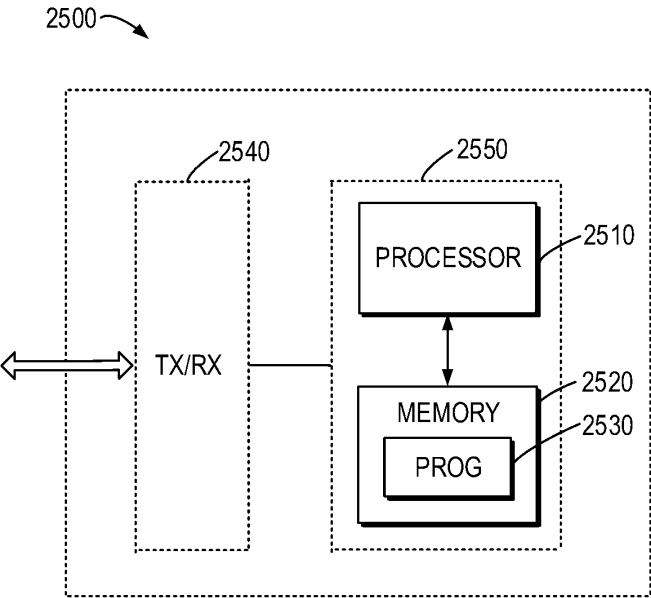


FIG. 25