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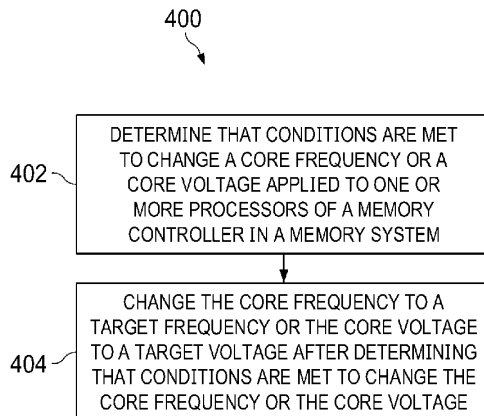


FIG. 4

(57) Abstract: A method for reducing power consumption in a memory system includes, determining, by a controller of a memory system and based on multiple commands received by the memory system during a first period of time, that a condition is met (602), a frequency or a voltage applied to one or more processors of the controller is reduced by the controller in response to determining that the condition is met (604).

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