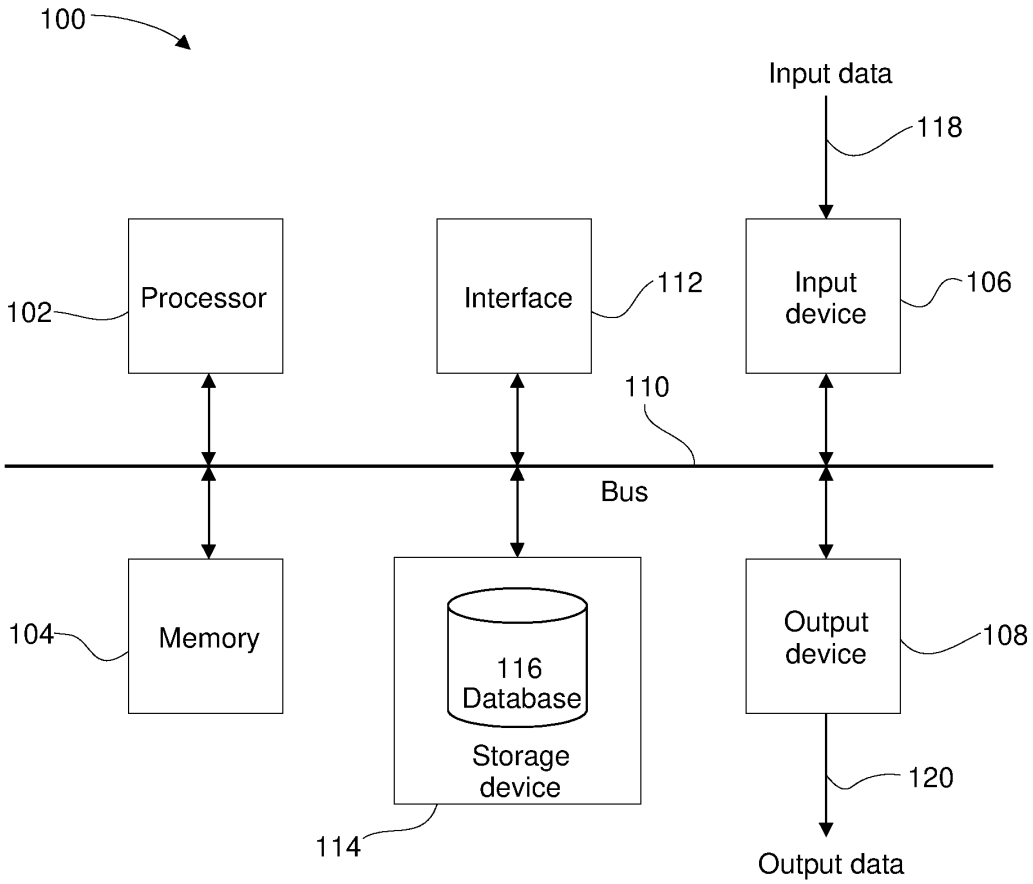

100
Input data
118
Processor
102
Interface
112
Input device
106
110
Bus
Memory
104
116 Database
Storage device
114
Output device
108
120
Output data

Fig. 1

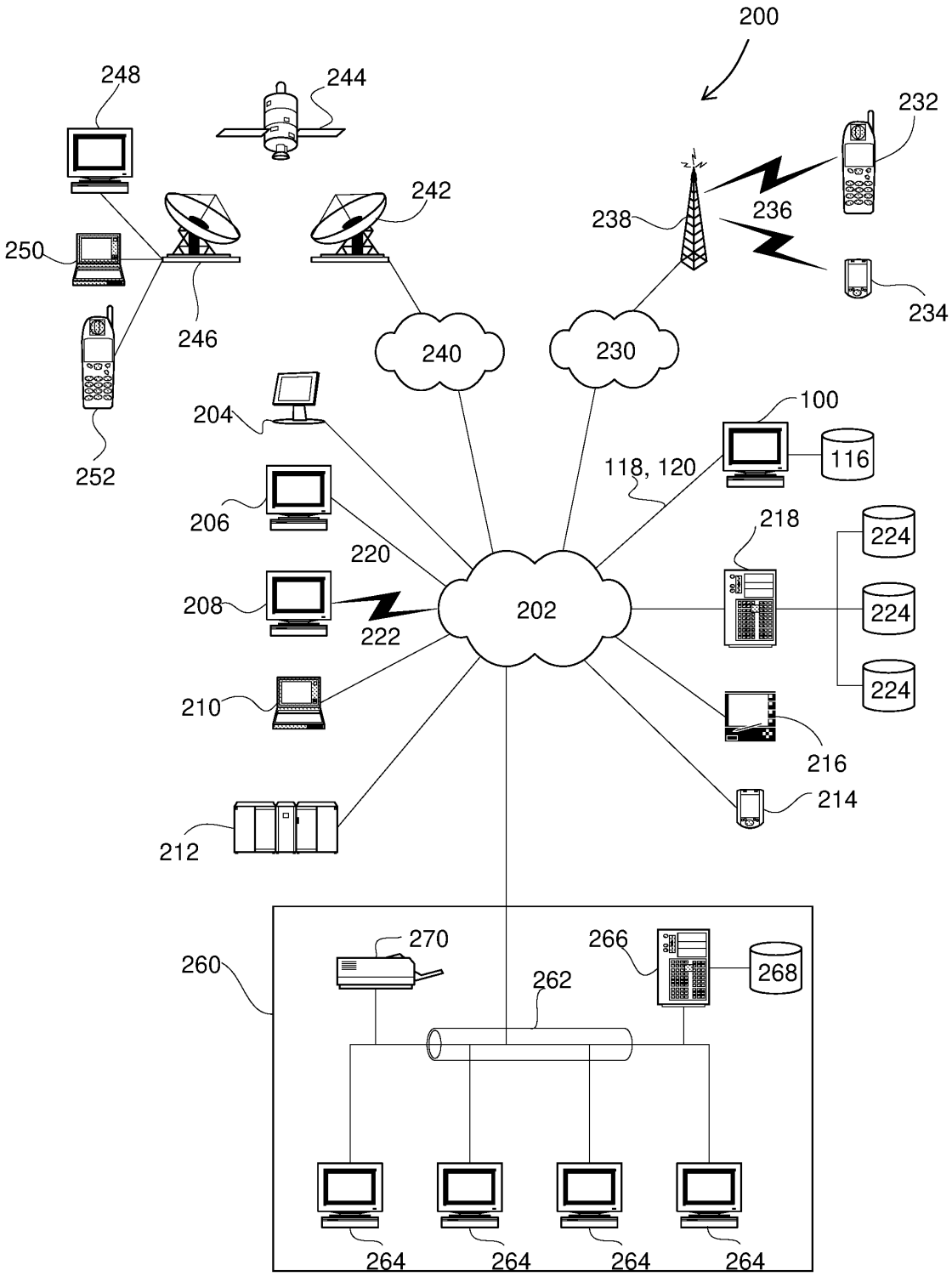

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264
264
264

Fig. 2

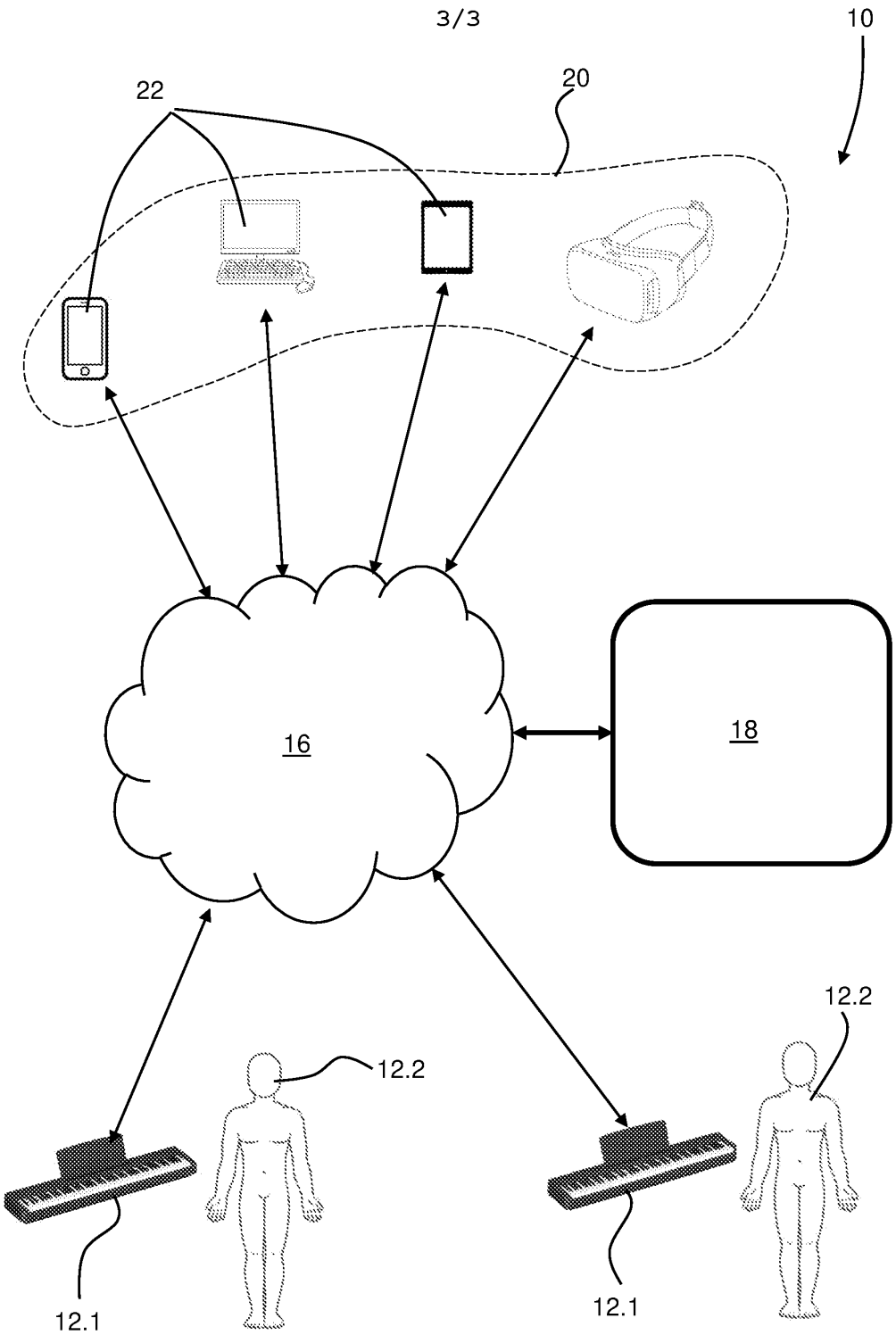


Fig. 3