

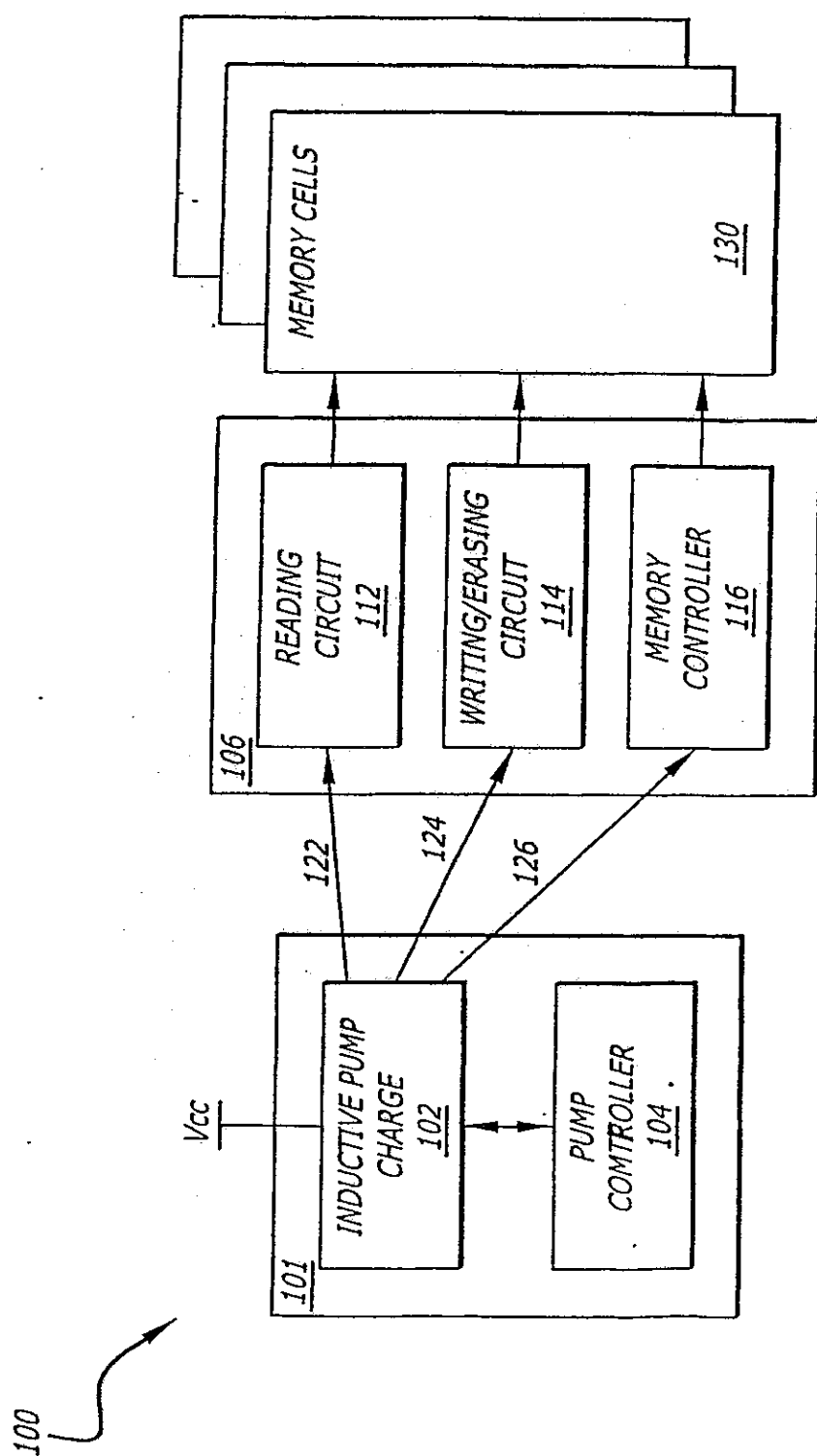


FIG. 1

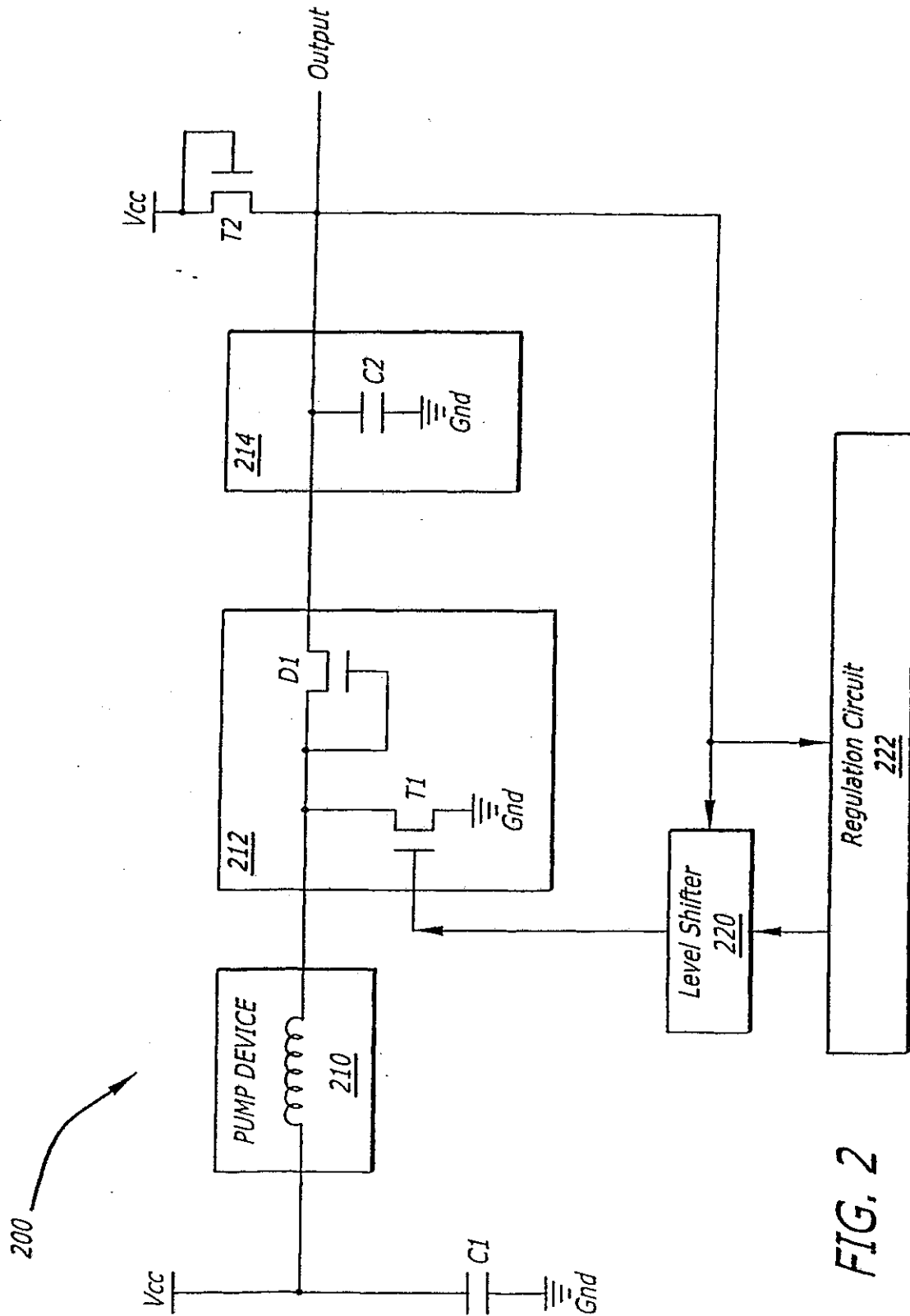
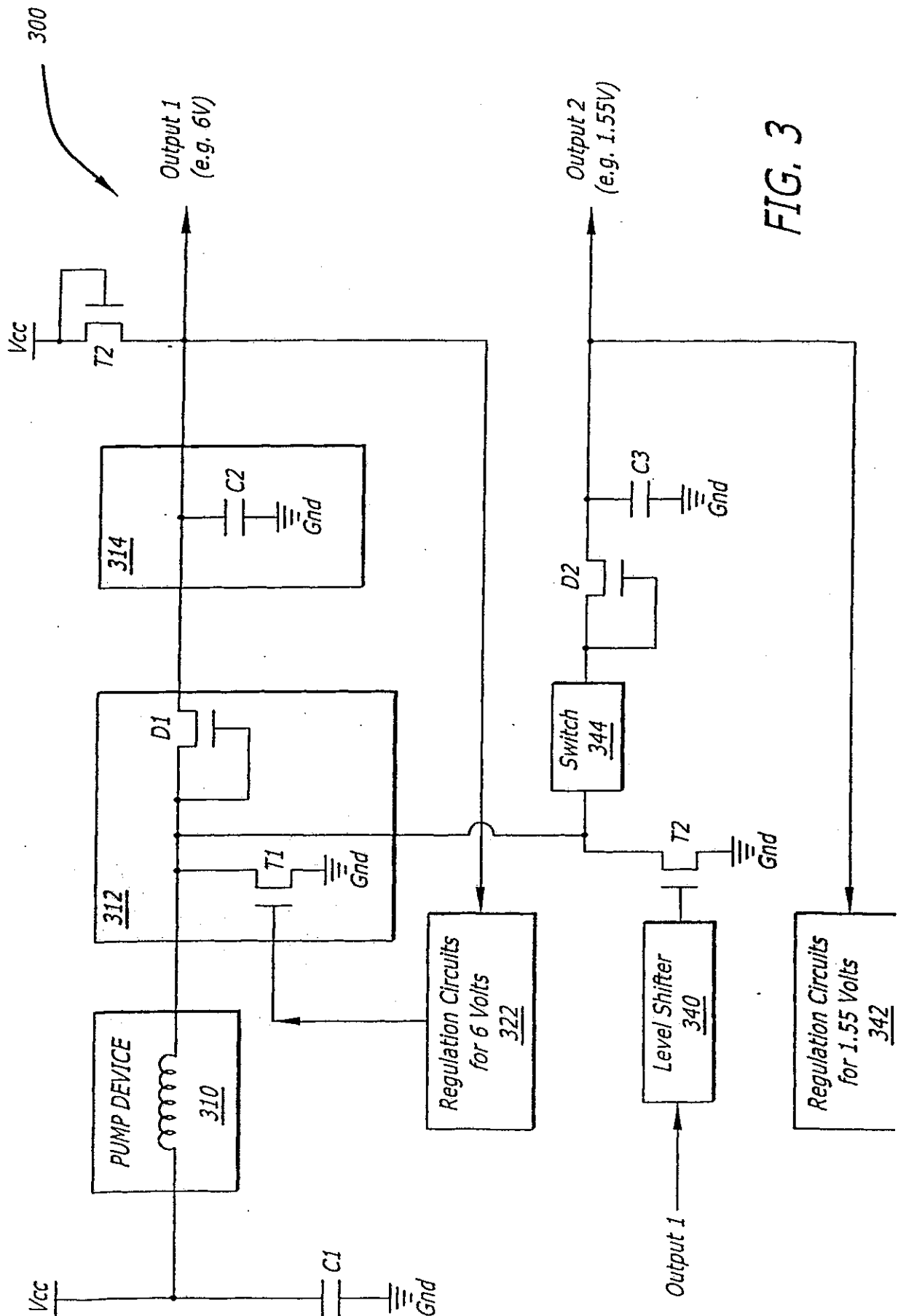


FIG. 2

3/9



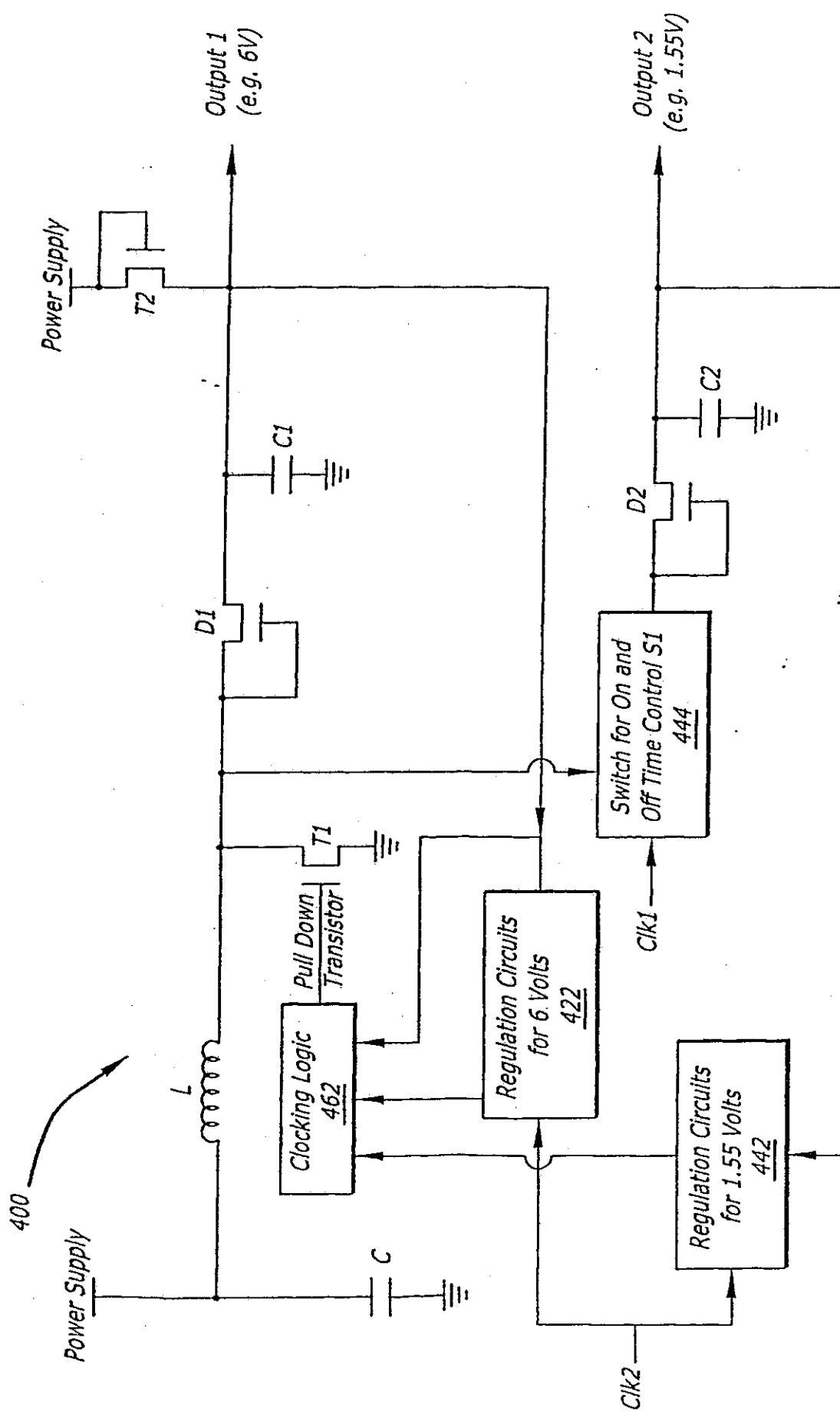


FIG. 4

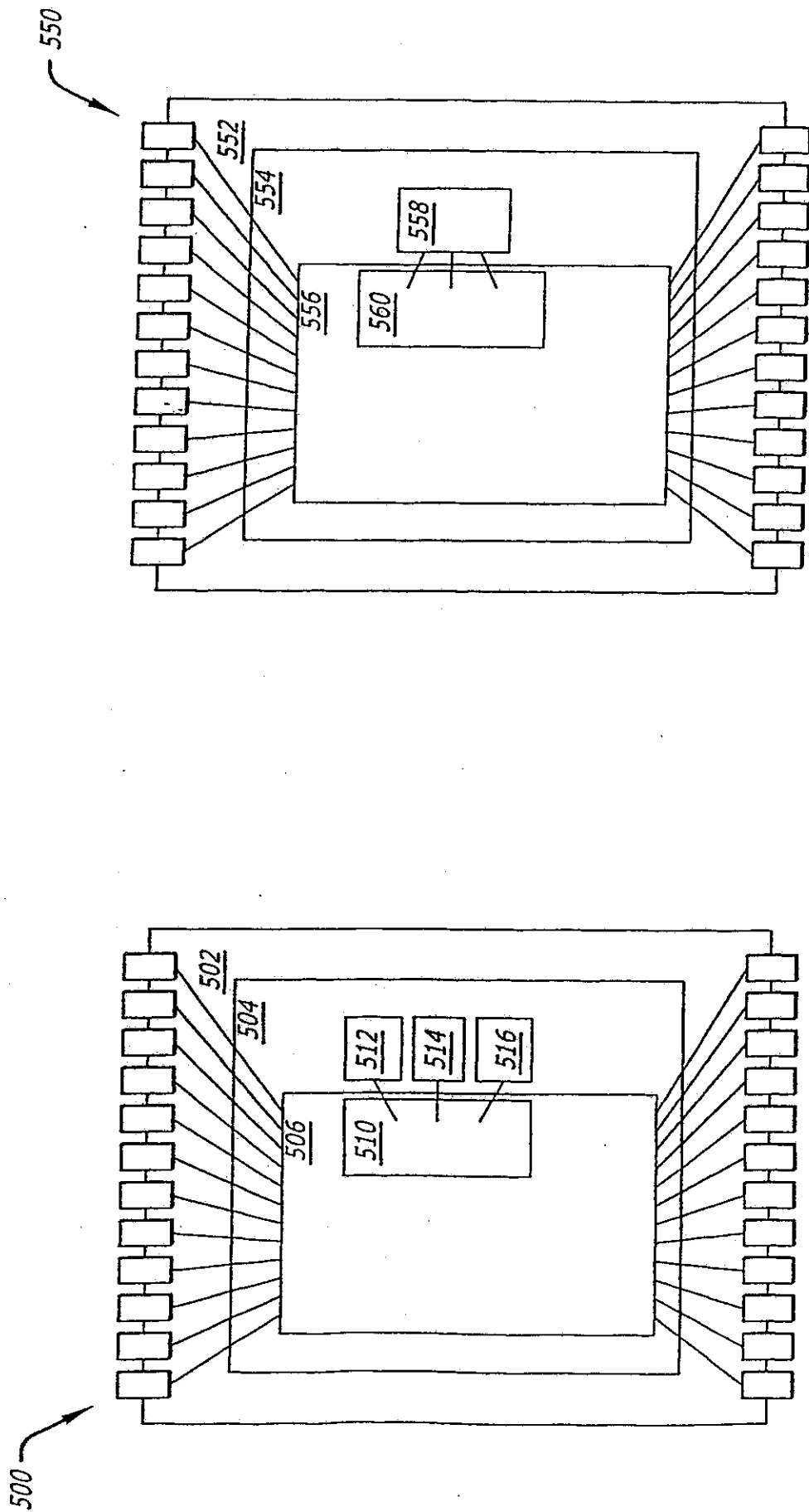


FIG. 5B

FIG. 5A

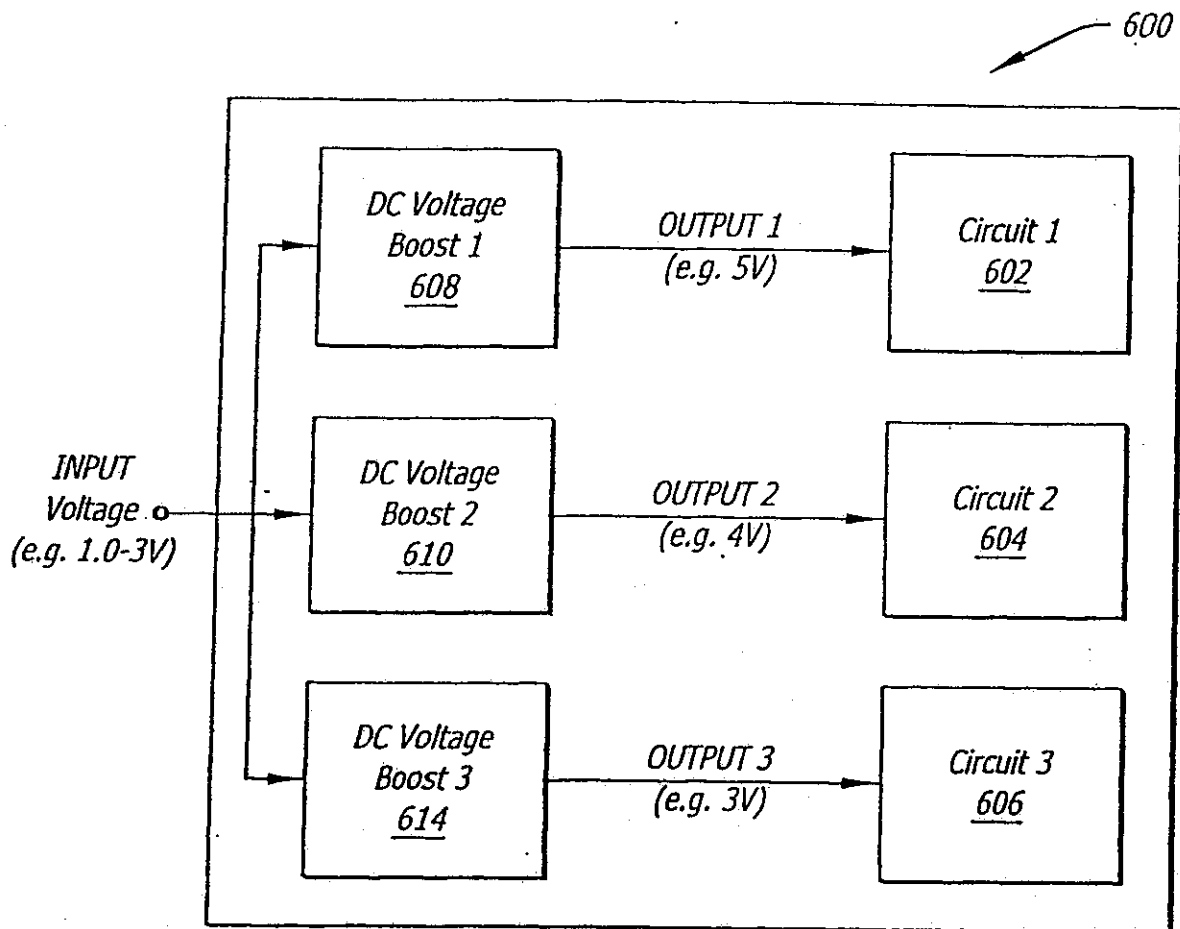


FIG. 6

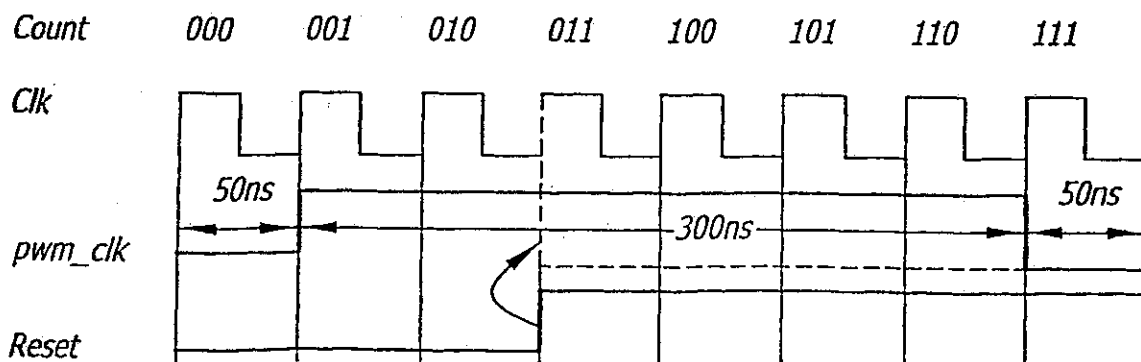
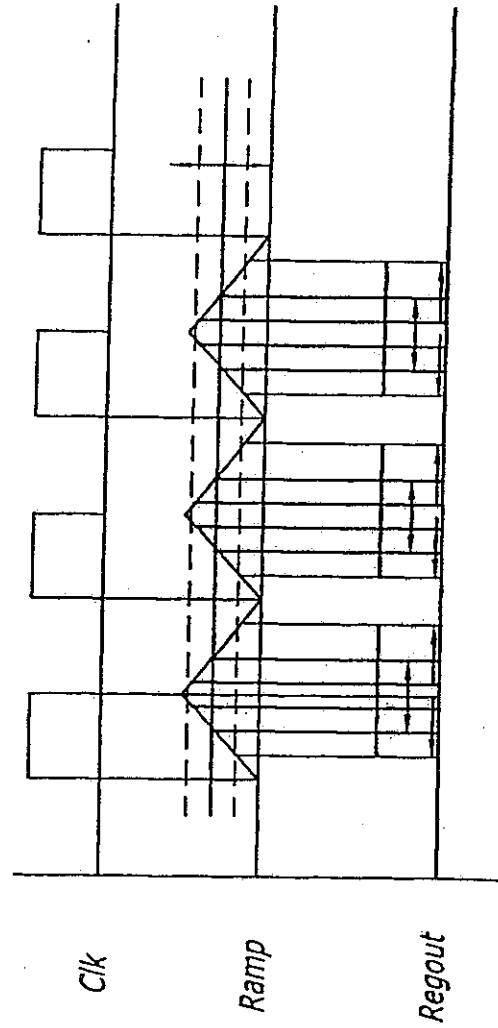
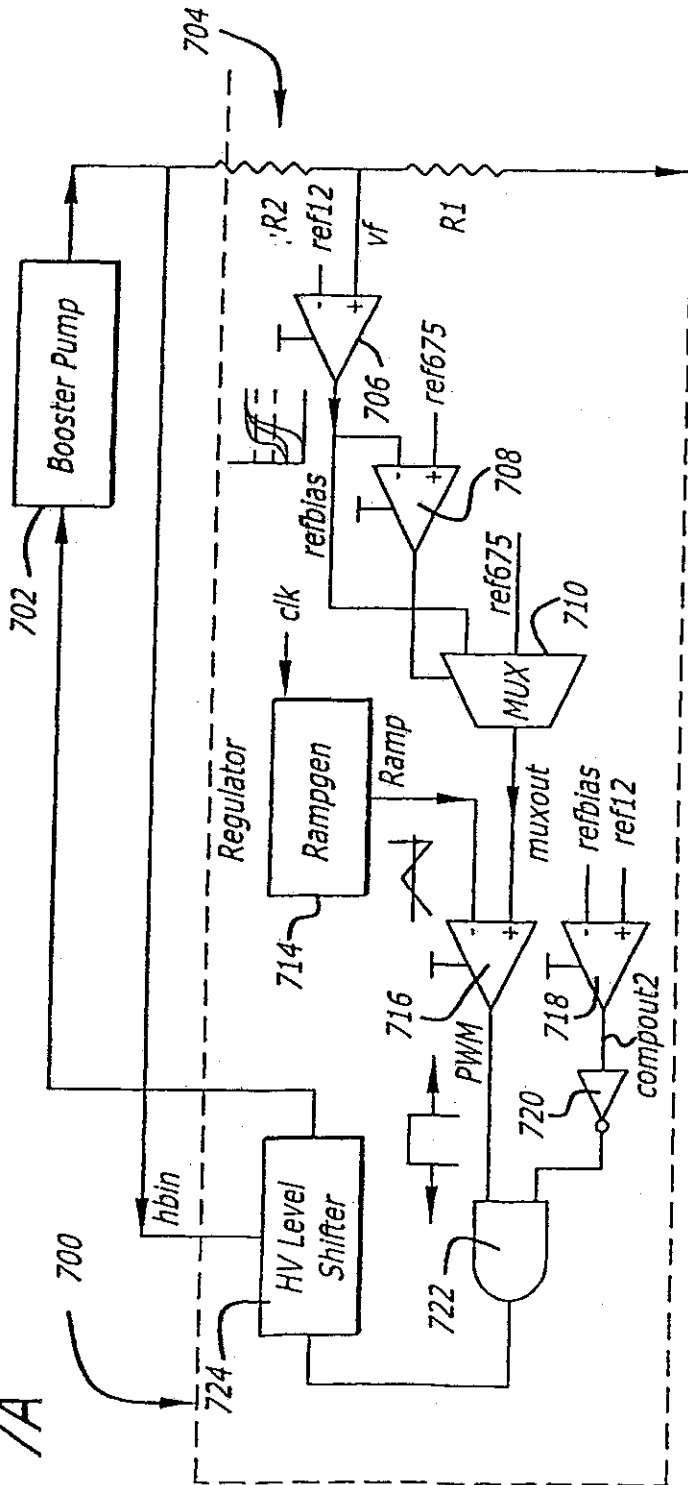


FIG. 9C

FIG. 7B

FIG. 7A



8/9

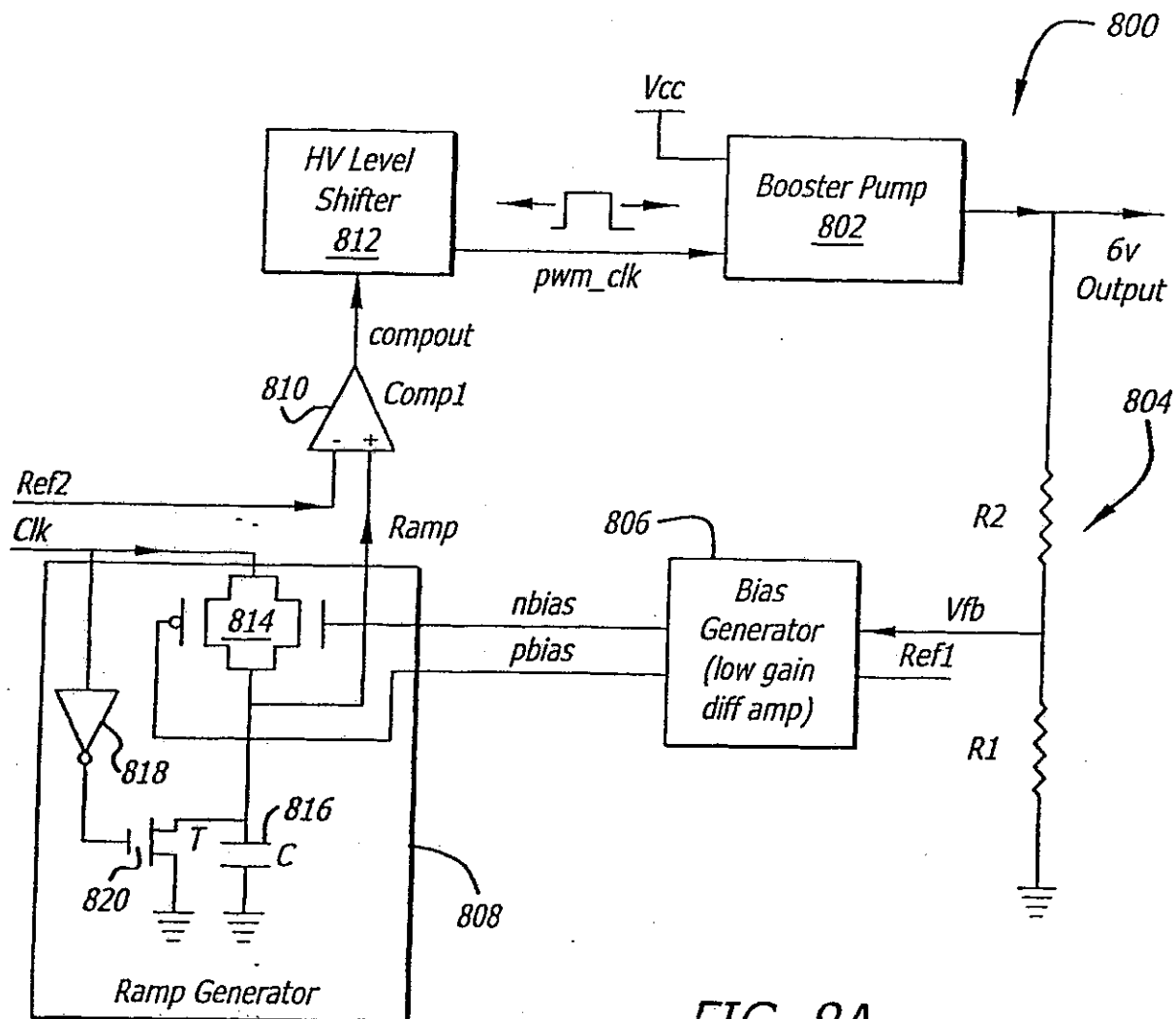


FIG. 8A

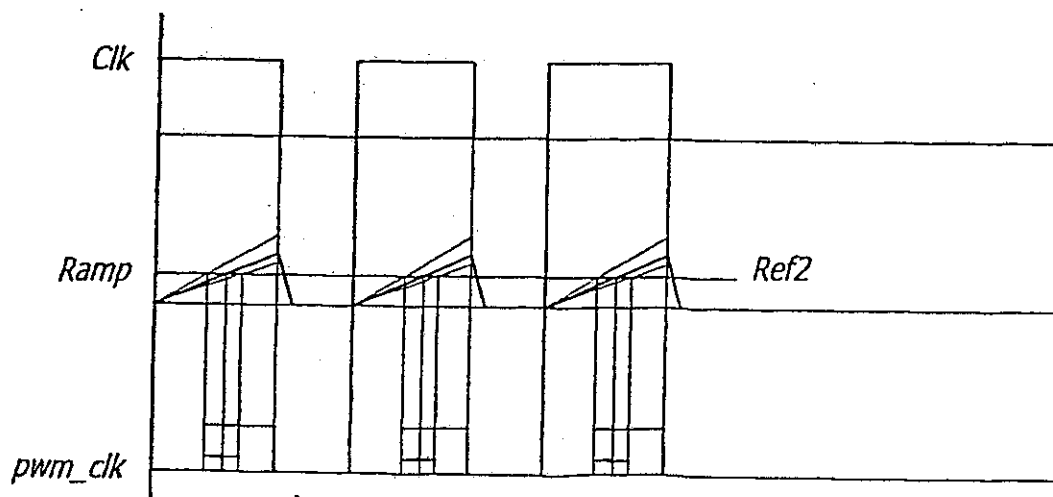


FIG. 8B

9/9

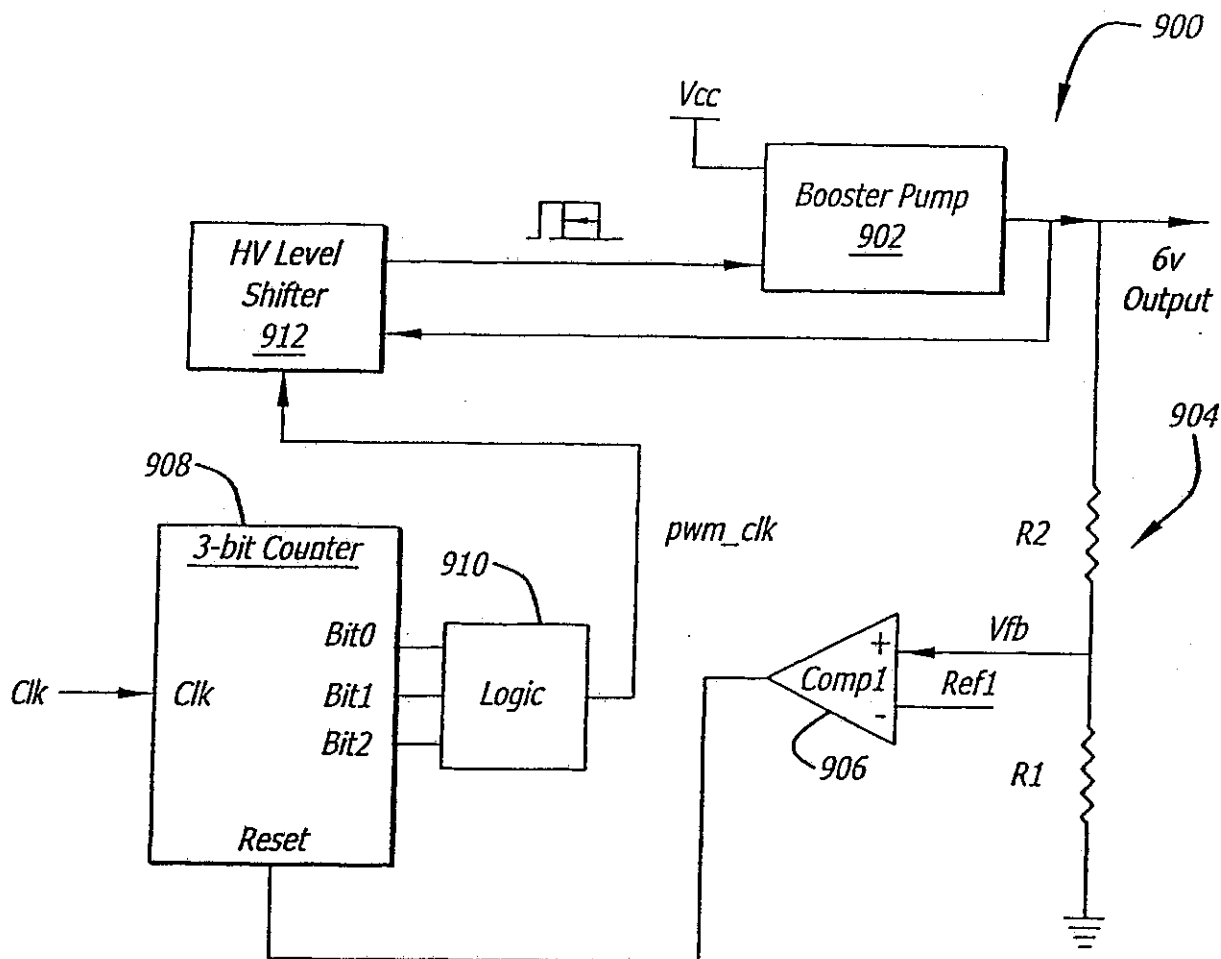


FIG. 9A

Count			
bit0	bit1	bit2	pwm_clk
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0

FIG. 9B