WHAT IS CLAIMED IS:

1. An ion pulse treatment apparatus for eye, comprising:

two conductor elements;

a carrier for carrying said conductor elements to the eye acupoint region;

a voltage generating element provided at said carrier, wherein one conductor element is electrically connected to a ground terminal of said voltage generating element, and the other conductor element is electrically connected to a voltage output terminal of said voltage generating element;

a power supply provided at said carrier;

a control board, wherein said power supply and said voltage generating element are respectively and electrically connected to said control board; and

a vacuum discharge tube electrically connected with said voltage output terminal of said voltage generating element to enhance the penetration effect of reduced ion, wherein said carrier is configured from a spectacle frame comprising a lens frame, a nose bridge connecting two ring-shaped spectacle rings of said lens frame, two nose pads disposed on said spectacle rings of said lens frame and two spectacle temples respectively and rotatably mounted on said lens frame, wherein said lens frame is divided into a lens frame body with a route groove and a lens frame cover corresponding to said lens frame body, wherein said lens frame cover is mounted on a side opening of said lens frame body, wherein said route groove of said lens frame body comprises two circumferential grooves surrounding said two spectacle rings of said lens frame, and a nose bridge groove provided at said nose bridge and communicating with said two circumferential grooves respectively, so that when said lens frame body is closed by said lens frame cover, a concealed wiring channel of said lens frame is defined.

2. The ion pulse treatment apparatus for eye, as recited in claim 1, wherein said carrier is a spectacle frame, wherein said spectacle frame comprises two spectacle temples, a spectacle frame, a nose bridge and two nose pads, wherein said spectacle frame comprises two spectacle rings, wherein said spectacle rings are connected with each other by said nose bridge, wherein said nose pads are respectively provided on said spectacle rings.

3. The ion pulse treatment apparatus for eye, as recited in claim 2, wherein said spectacle frame defines a concealed wiring groove, wherein said power supply, said voltage generating element and said control board are provided within said concealed wiring groove.

4. The ion pulse treatment apparatus for eye, as recited in claim 3, wherein said concealed wiring groove defines two circumferential grooves surrounding said spectacle rings respectively and a nose bridge groove communicated with said circumferential grooves respectively.

5. The ion pulse treatment apparatus for eye, as recited in claim 2, wherein said conductor elements are respectively provided at said nose pads.

6. The ion pulse treatment apparatus for eye, as recited in claim 2, wherein said conductor elements define said nose pads respectively.

7. The ion pulse treatment apparatus for eye, as recited in claim 2, further comprising two counterweights, wherein each spectacle temple of said spectacle frame comprises an ear front portion, an ear rear portion and an ear support portion, wherein said ear support portion is provided between said ear front portion and said ear rear portion, wherein said counterweights are respectively provided at said ear rear portion of said spectacle temples.

8. The ion pulse treatment apparatus for eye, as recited in claim 1, wherein said carrier is a helmet, wherein said helmet comprises a surrounding fixing portion for surrounding the head and an overhead surrounding portion, wherein two ends of said overhead surrounding portion is fixed respectively on said surrounding fixing portion, wherein said conductor elements and said power supply are arranged at said surrounding fixing portion.

9. The ion pulse treatment apparatus for eye, as recited in claim 1, wherein said carrier is a blinder, wherein said blinder comprises two layers, wherein said two layers define a sandwiched space therebetween, wherein said power supply, said voltage generating element

and said control board are arranged within said sandwiched space.

10. The ion pulse treatment apparatus for eye, as recited in claim 9, further comprising two counterweight rods, each of two counterweight rods defines an ear front part, wherein said ear front parts of said counterweight rods are respectively arranged on two edges of said blinder.

11. The ion pulse treatment apparatus for eye, as recited in claim 1, wherein said control board comprises a CPU module, a booster module and a power supply unit, wherein said power supply unit comprises a charging module, a charger and a battery, wherein said charging module is electrically connected with said battery and said charger respectively, wherein said booster module is electrically connected with said voltage generating element, wherein a control chip of said CPU module is electrically connected with said booster module through at least one pin, and said control chip of said CPU module is electrically connected with said charging module through at least two pins, wherein another pin of said control chip of said CPU module is respectively connected with at least three capacitors, and at least two of said three capacitors are respectively and electrically connected with said power supply through a same resistance.

12. The ion pulse treatment apparatus for eye, as recited in claim 11, further comprising a vibration motor module, wherein said vibration motor module comprises a vibration motor and a transistor, wherein said vibration motor is respectively and electrically connected with the collector of said transistor and said power supply, the emitter of said transistor is grounded, and the base of said transistor is electrically connected with said CPU module through at least one resistor.

13. The ion pulse treatment apparatus for eye, as recited in claim 7, wherein two batteries of said power supply define said counterweights respectively.

14. The ion pulse treatment apparatus for eye, as recited in claim 2, wherein said nose pad comprises a nose arm and a supporting leaf installed with said nose arm, wherein said nose arm and said supporting leaf are made of conductive material.

15. An ion pulse generating assembly, comprising:

two conductor elements;

a voltage generating element, wherein one conductor element is electrically connected to a ground terminal of said voltage generating element, and the other conductor element is electrically connected to a voltage output terminal of said voltage generating element;

a power supply;

a control board, wherein said power supply and said voltage generating element are respectively and electrically connected to said control board; and

a vacuum discharge tube electrically connected with said voltage output terminal of said voltage generating element to enhance the penetration effect of reduced ion.

16. The ion pulse generating assembly, as recited in claim 15, wherein said control board comprises a CPU module, a booster module and a power supply unit, wherein said power supply unit comprises a charging module, a charger and a battery, wherein said charging module is electrically connected with said battery and said charger respectively, wherein said booster module is electrically connected with said voltage generating element, wherein a control chip of said CPU module is electrically connected with said booster module through at least one pin, and said control chip of said CPU module is electrically connected with said charging module through at least two pins, wherein another pin of said control chip of said CPU module is respectively connected with at least three capacitors, and at least two of said three capacitors are respectively and electrically connected with said power supply through a same resistance.

17. An ion pulse generating assembly, comprising:

two conductor elements;

a negative ion generating circuit comprising a CPU module, a boost module, a voltage module and a power supply unit, wherein said power supply unit comprises a charging module, a charger connected to said charging module, and a battery connected to said charging module, wherein said power supply unit is connected to said CPU module and said boost module, said CPU module is connected to said boost module, said booster module is connected to said

voltage module, wherein one conductor element is electrically connected to a ground terminal, and said other conductor element is electrically connected to said voltage module of said negative ion generating circuit; and

a control board, wherein said power supply and said negative ion generating circuit are respectively and electrically connected to said control board, wherein said voltage module comprises a voltage comparator IC1B, a voltage comparator IC1C, a voltage comparator IC1D, a transistor Q1, a piezoelectric ceramic transformer PZT1; a connector J1, a connector J2 connected to said charging module of said power supply module; an input voltage terminal of said piezoelectric ceramic transformer PZT1 is connected to a voltage power supply input terminal of said voltage plate through an inductor L1, a voltage power supply input terminal of said inductor L1 is connected with a capacitor C15, and said other end of said capacitor C15 is connected to said ground terminal GND of said voltage plate, a ground terminal of said piezoelectric ceramic transformer PZT1 is connected to said ground terminal GND of said voltage plate, an output voltage terminal of said piezoelectric ceramic transformer PZT1 is connected with a diode D3 and a diode D4 which are connected in antiparallel with each other, a capacitor C17 is provided between said diode D3 and said diode D4, said diode D3 and diode D4 connected in antiparallel with each other are connected to said voltage output terminal HV; the S pole of a transistor Q1 is connected to the connection end of said inductor L1, which is connected with said piezoelectric ceramic transformer PZT1, the D pole of said transistor Q1 is connected to the ground terminal GND of said voltage plate, the G pole of said transistor Q1 is connected to the output terminals of said voltage comparator IC1D and said voltage comparator IC1B, a capacitor C16 is connected between the S and D poles of said transistor Q1; the non-inverting input terminal of said voltage comparator IC1D is connected to the non-inverting input terminal of said voltage comparator IC1A, and the inverting input terminal of said voltage comparator IC1D is connected to the inverting input terminal of said voltage comparator IC1A; the inverting input terminal of said voltage comparator IC1B is connected to the output terminal of said voltage comparator IC1A through a resistor R28, the non-inverting input terminal of said voltage comparator IC1B is connected to the ground terminal GND of said voltage plate through said capacitor C12, a capacitor C5 is connected between the ground terminal of said capacitor C12 and the inverting input terminal of said voltage comparator IC1B;

the non-inverting input terminal of said voltage comparator IC1A is connected to said resistor R21, the inverting input terminal of said voltage comparator IC1A is connected to the ground terminal GND of said voltage plate through a capacitor C4, a resistor R25 is connected between the non-inverting input terminal and the output terminal of said voltage comparator IC1A, the VCC terminal of said voltage comparator IC1A is connected to a pin 1 of said connector J1, the ground terminal of said voltage comparator IC1A is connected to the ground terminal GND of the voltage plate, a resistor R4 and a diode D1 are provided between the voltage power input terminal of said voltage plate and the ground terminal GND of said voltage plate, said diode D1 is connected in parallel with a capacitor C2, a resistor R8 and a resistor R11 are provided between said voltage power supply input terminal of said voltage plate and the ground terminal GND of said voltage plate, the voltage power input terminal of said resistor R8 is connected to said resistor R12, said resistor R29, and said resistor R30, said resistor R12 is connected to said resistor R21 and said resistor R13, said resistor R13 is connected to the ground terminal GND of said voltage plate, a resistor R27 is connected between the inverting input terminal and the output terminal of said voltage comparator IC1A; the output terminal of said voltage comparator IC1C is connected to the non-inverting input terminal of said voltage comparator IC1B and between said resistor R8 and said resistor R11 through a resistor R36, a resistor R26 and a capacitor C3 are connected in series between the inverting input terminal and the output terminal of said voltage comparator IC1C, the inverting input terminal of said voltage comparator IC1C is connected to said resistor R34 and said resistor R35, the non-inverting input terminal of said voltage comparator IC1C is connected to said resistor R33 and said capacitor C20, said resistor R34 and said resistor R32 are connected between said capacitor C2, said resistor R4 and said diode D1, said capacitor C20 and resistor R35 are connected to the ground terminal GND of said voltage plate, the non-inverting input terminal of said voltage comparator IC1C is connected to the voltage output terminal HV through a resistor R31.