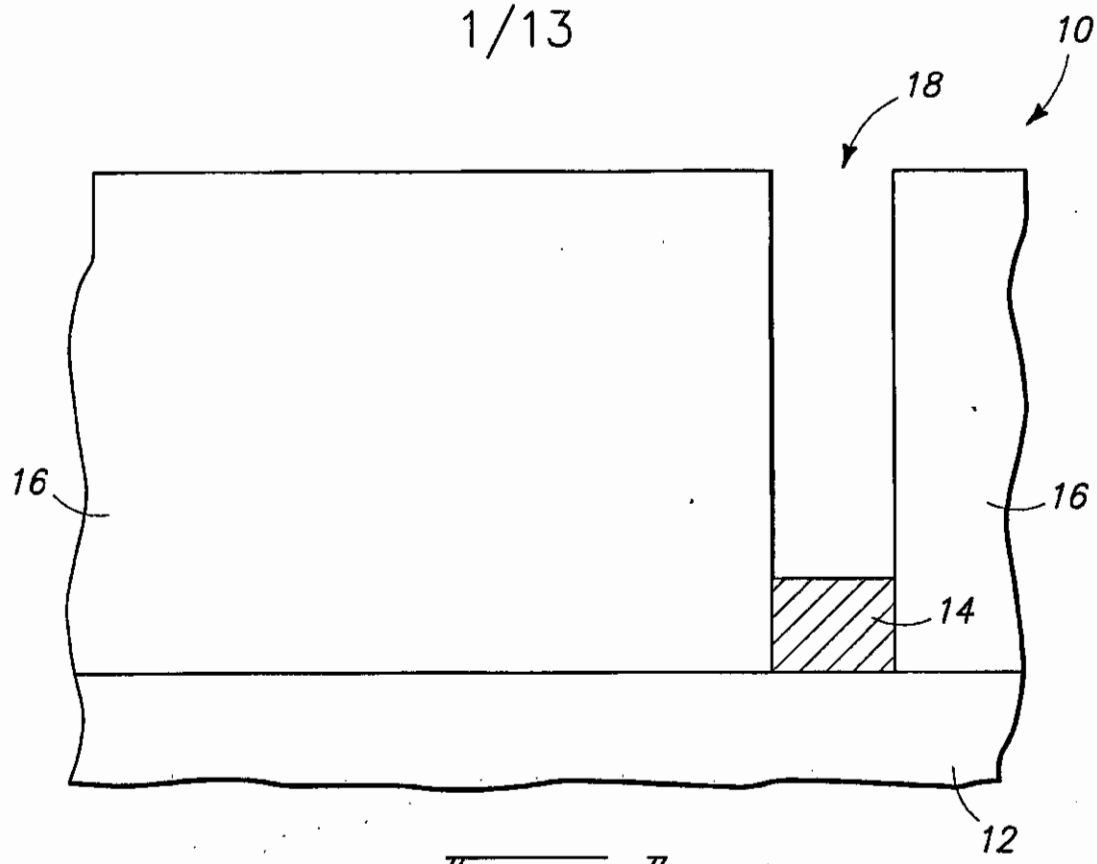


Fig 1
PRIOR ART

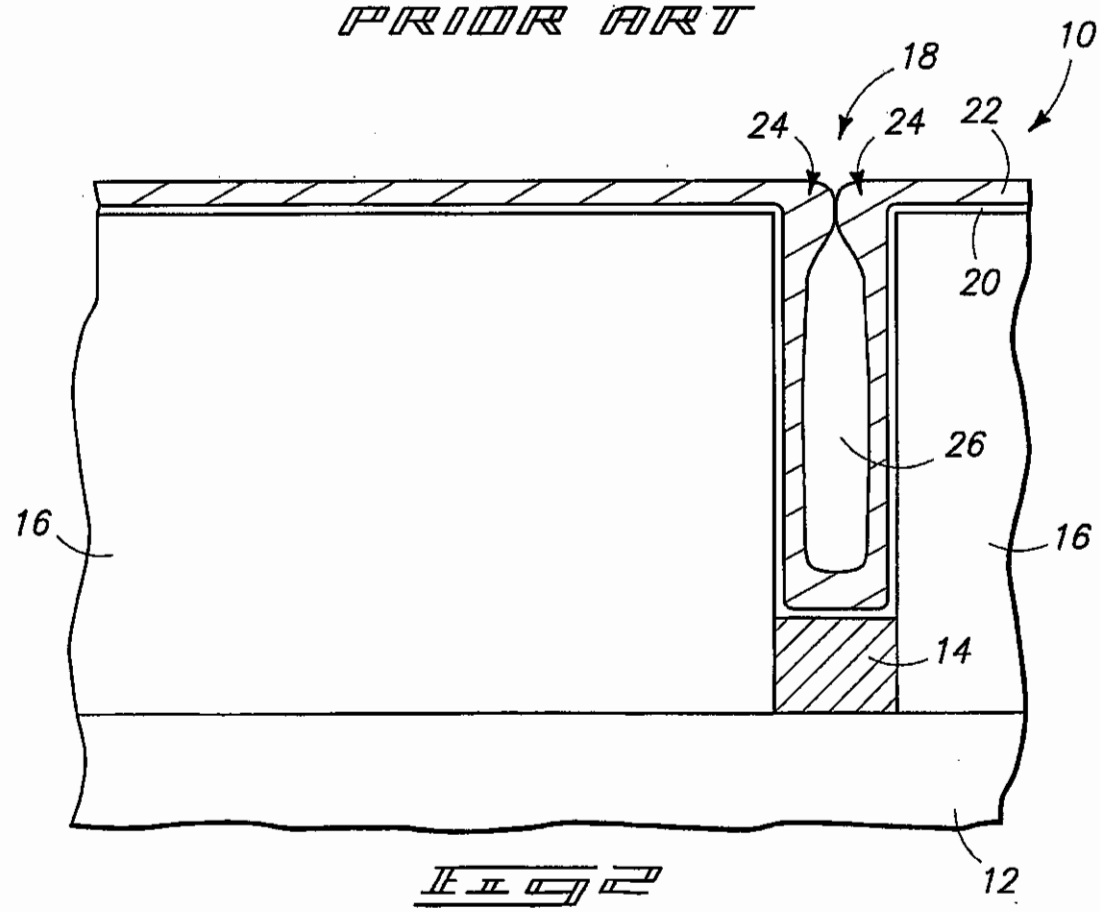


Fig 2
PRIOR ART

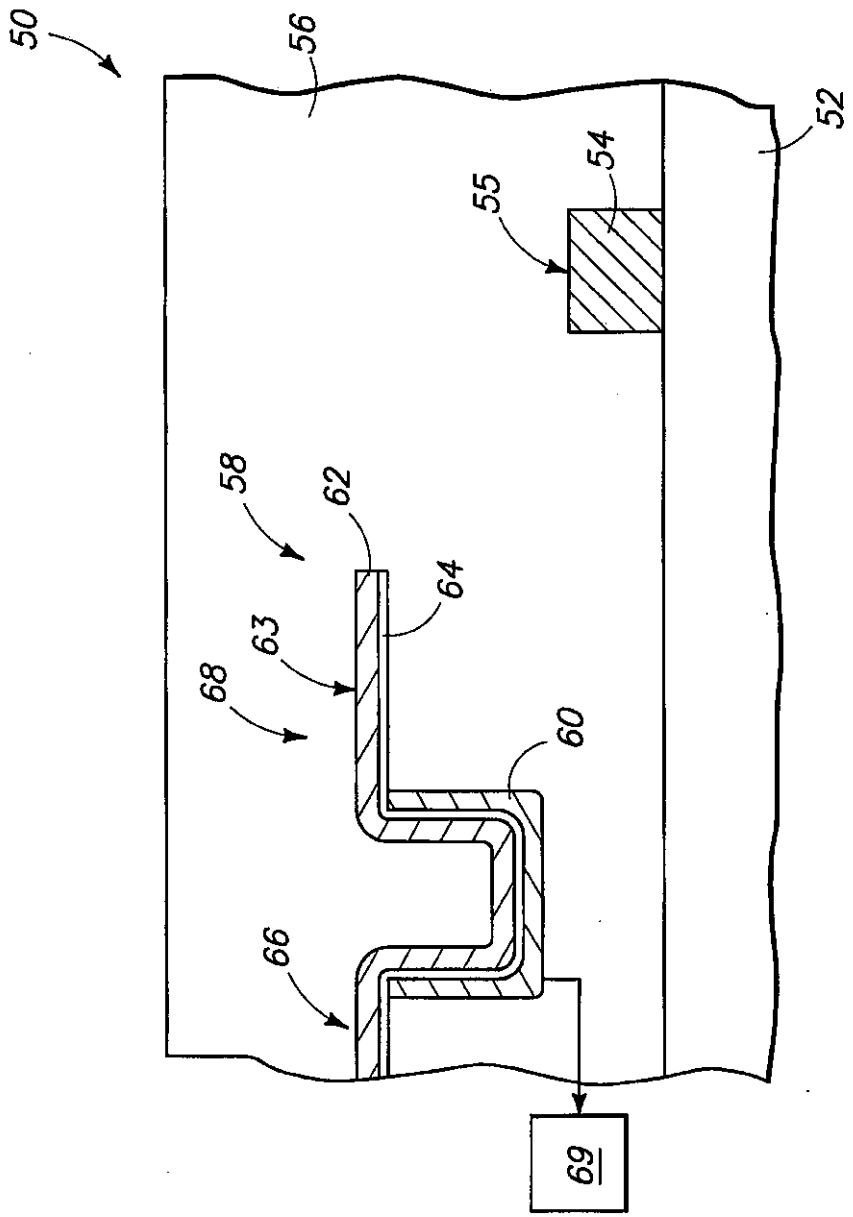


FIG 3E

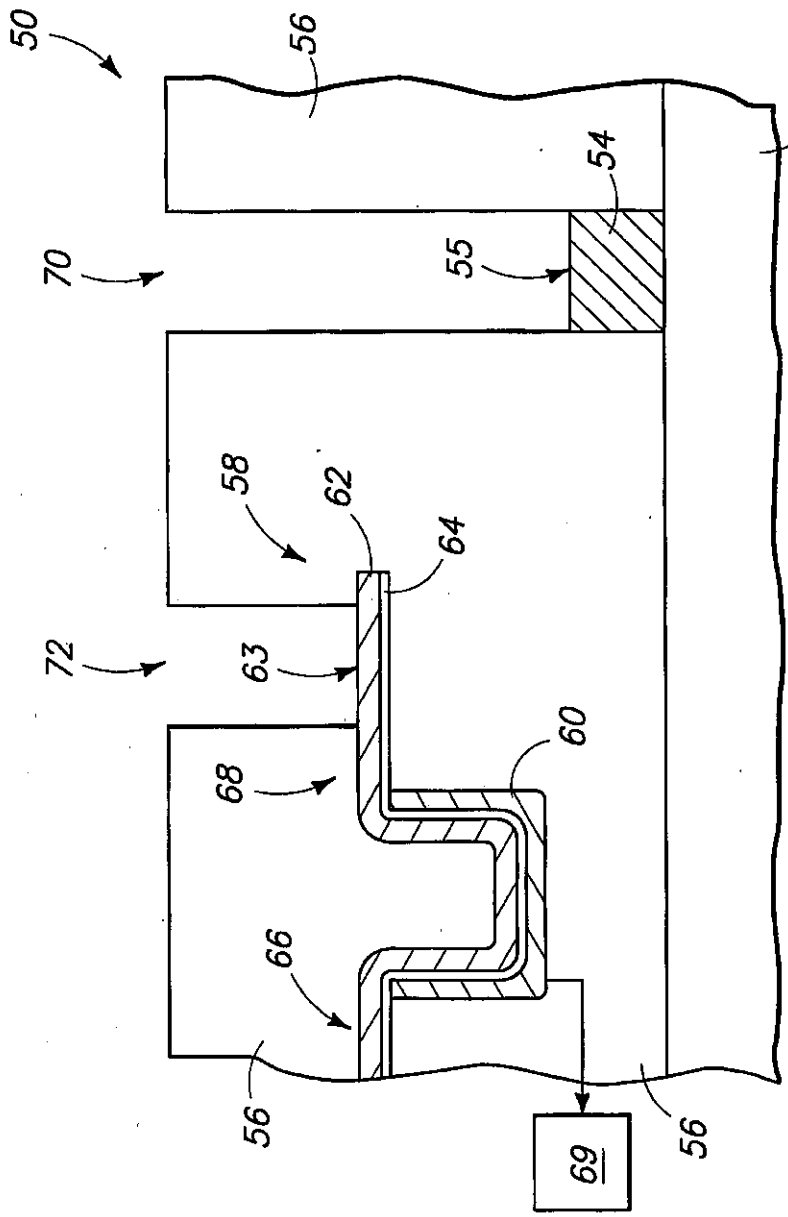


Fig 4

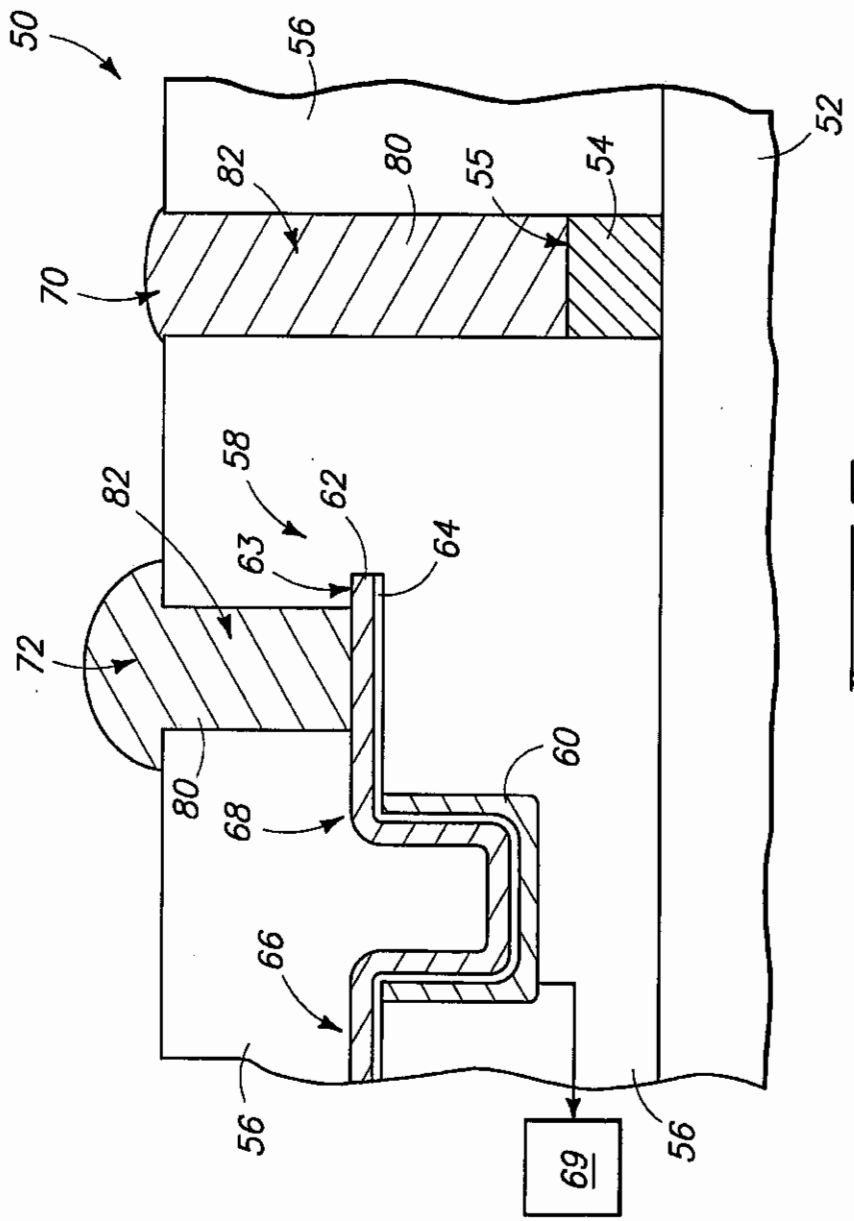


Fig 5

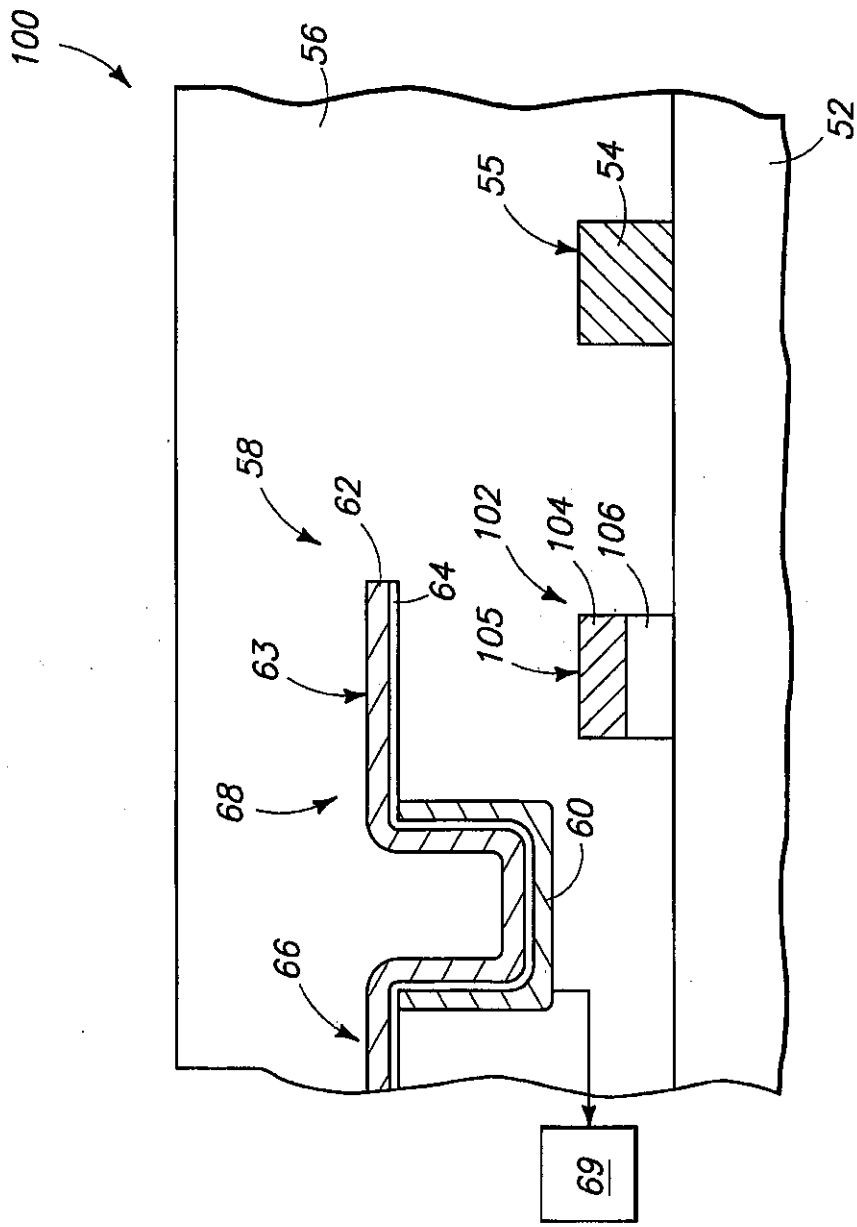


Fig 5

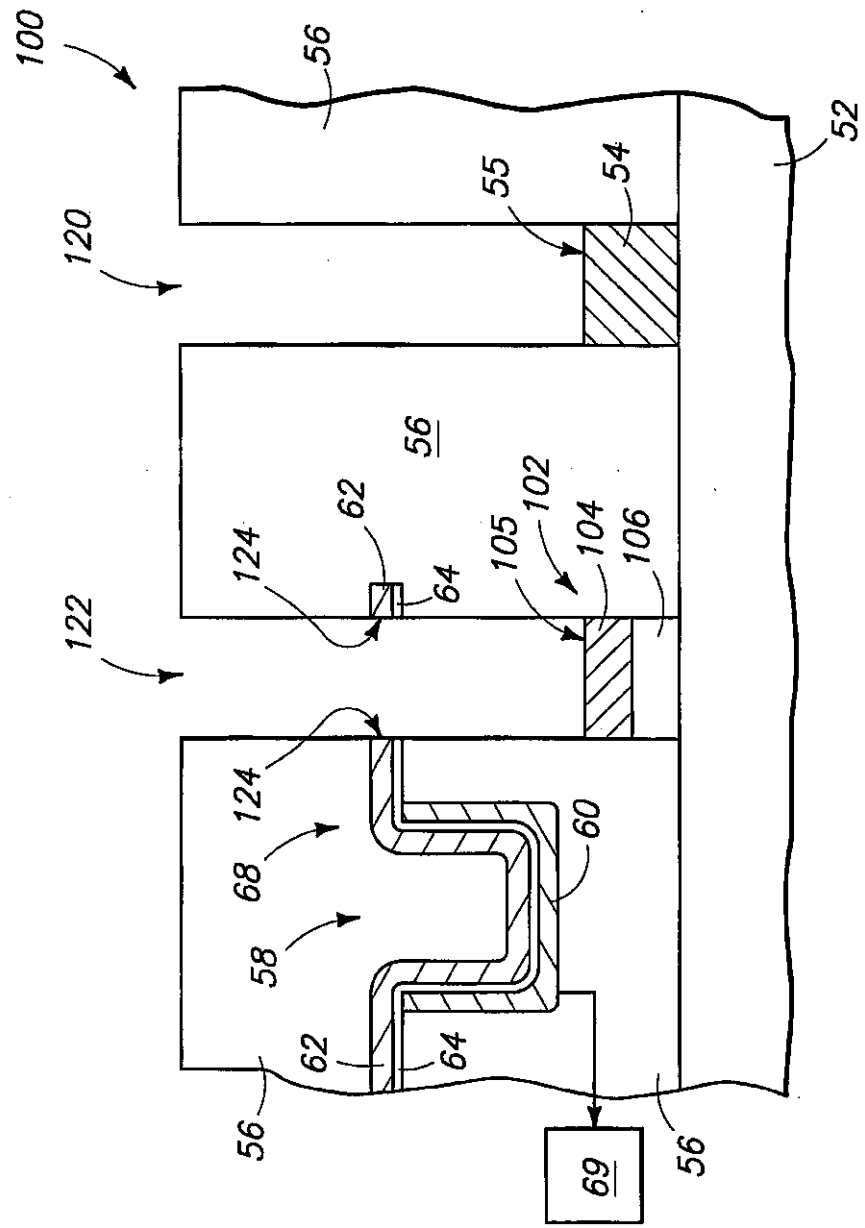

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69

FIG 11

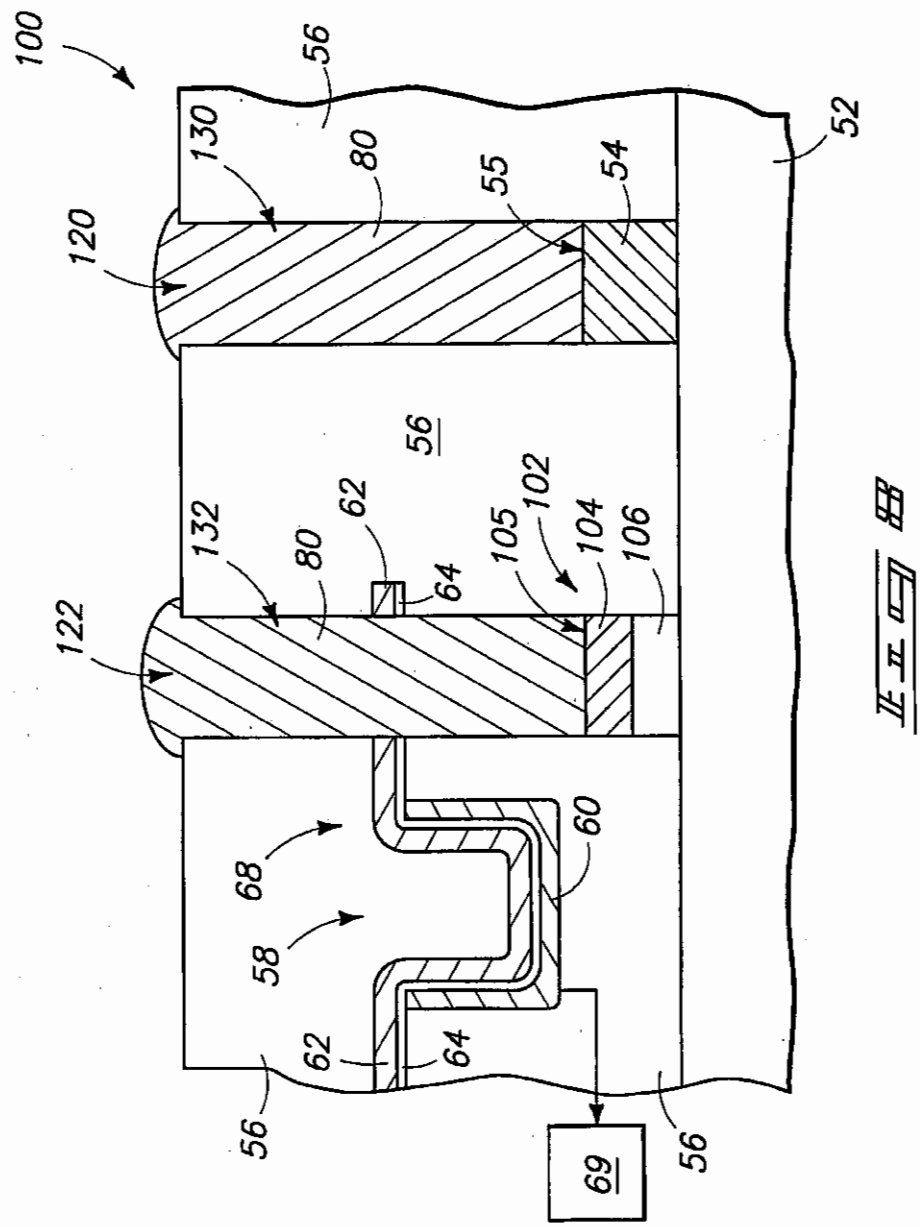

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FIG 8

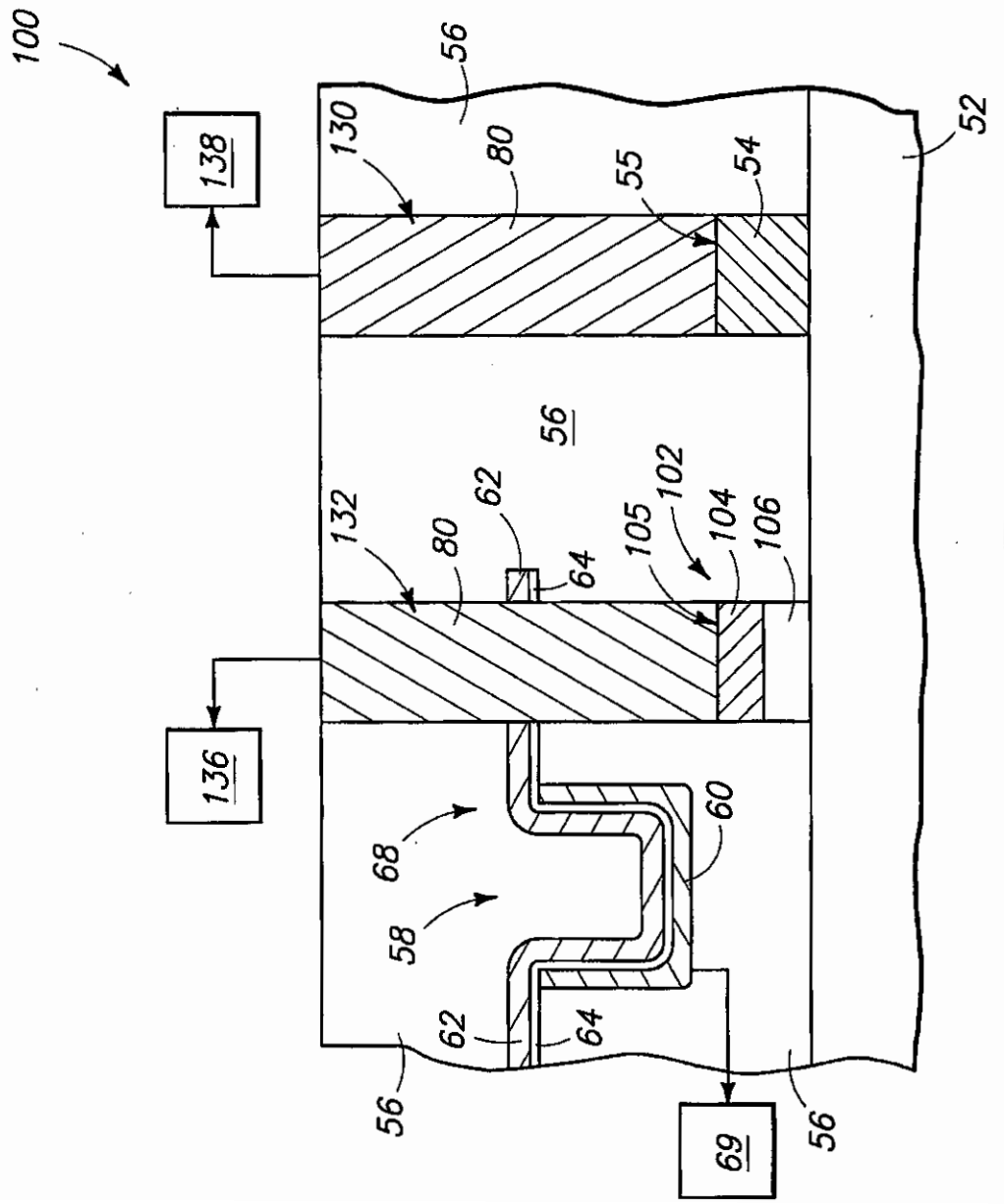

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FIG 5

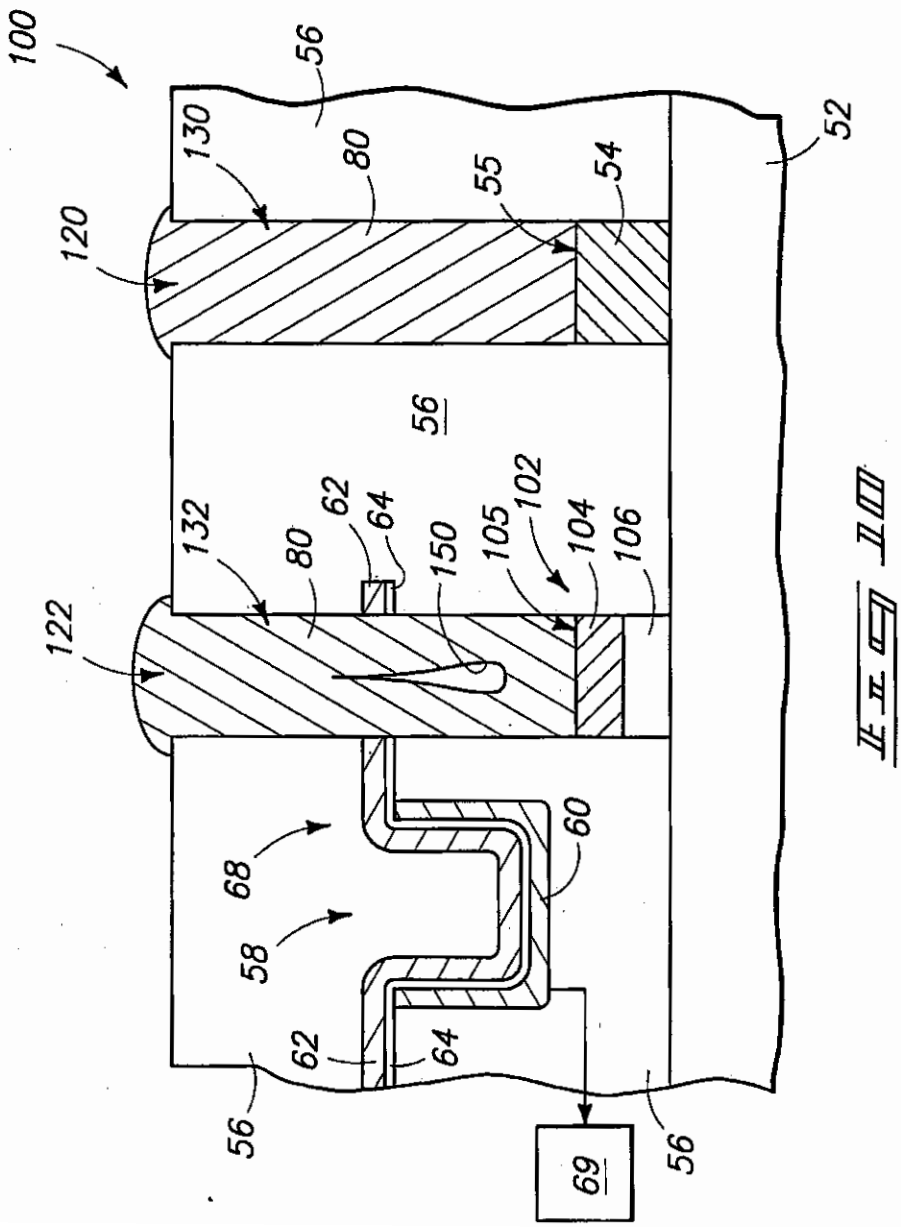

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FIG 10

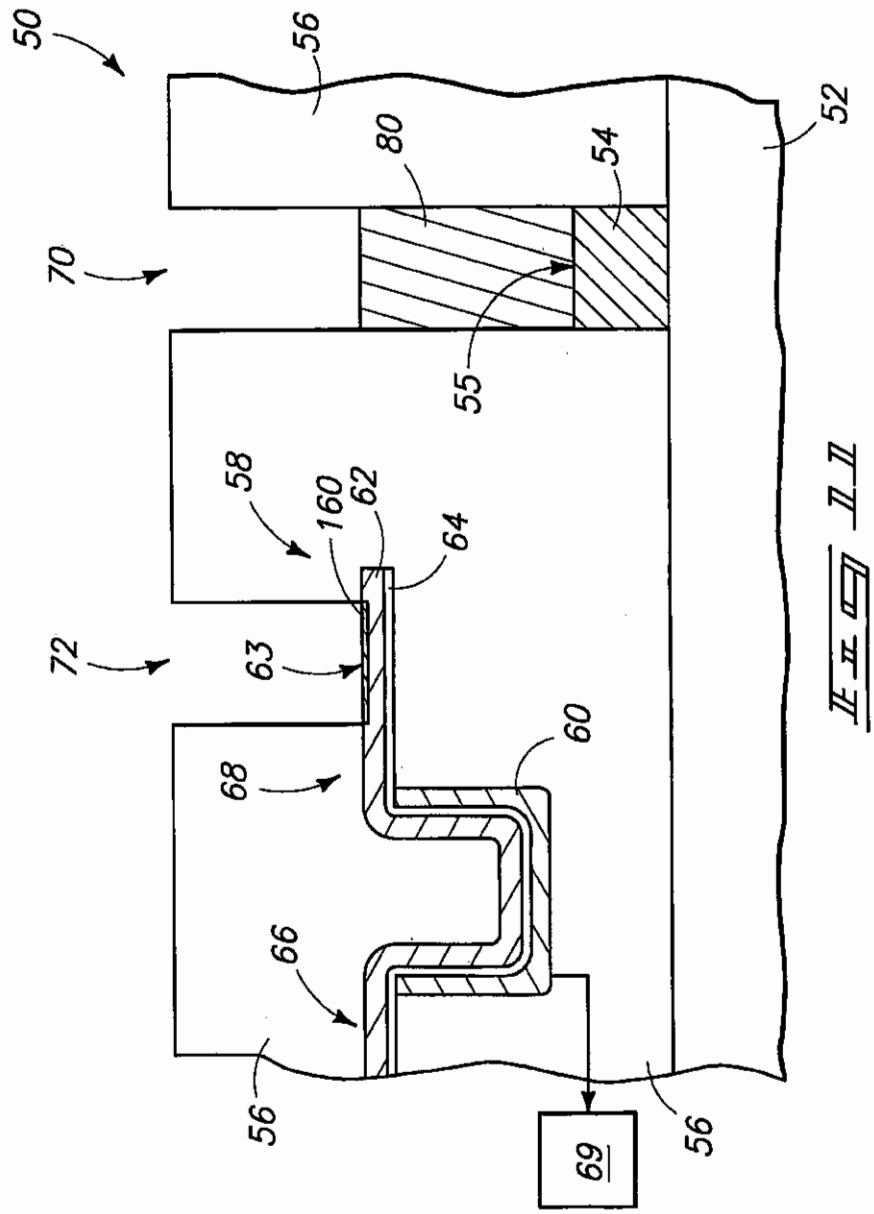


FIG. 11

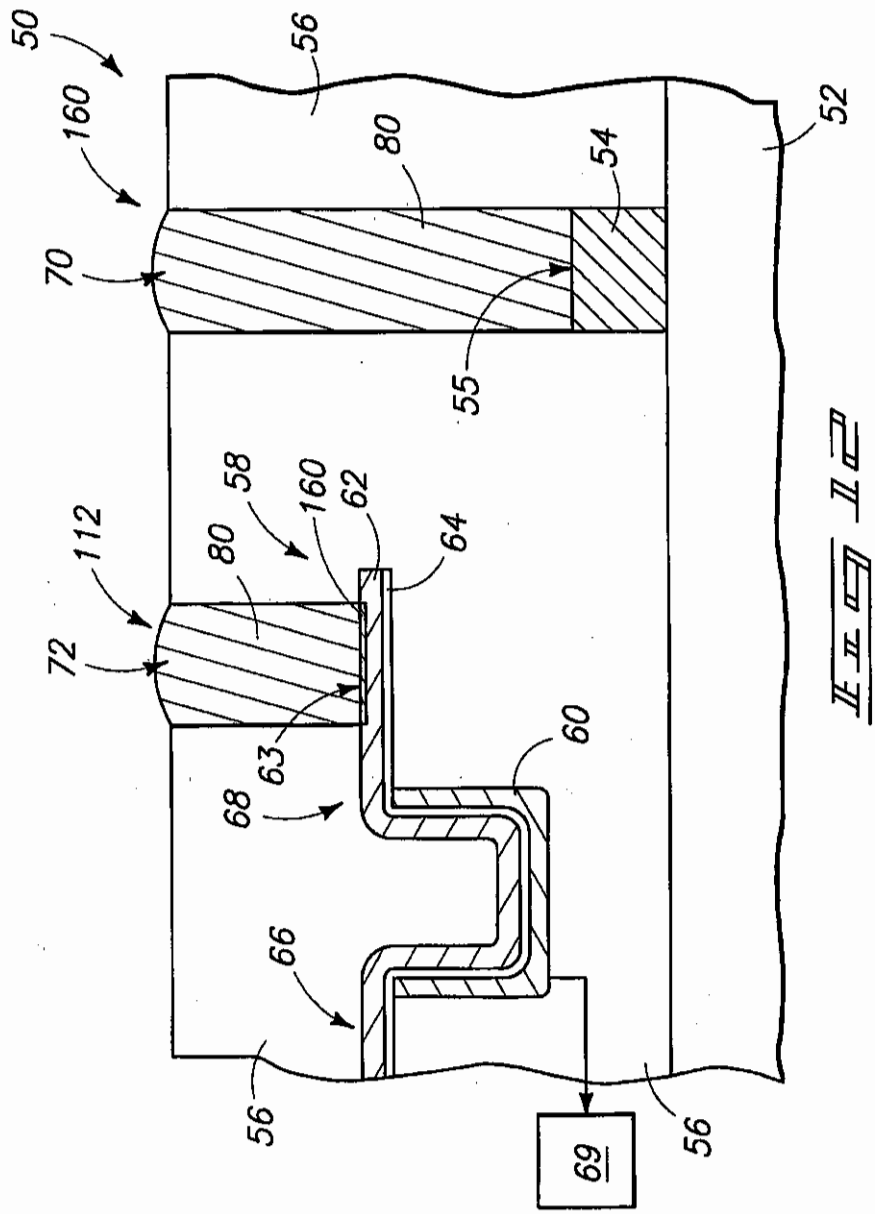


FIG 12

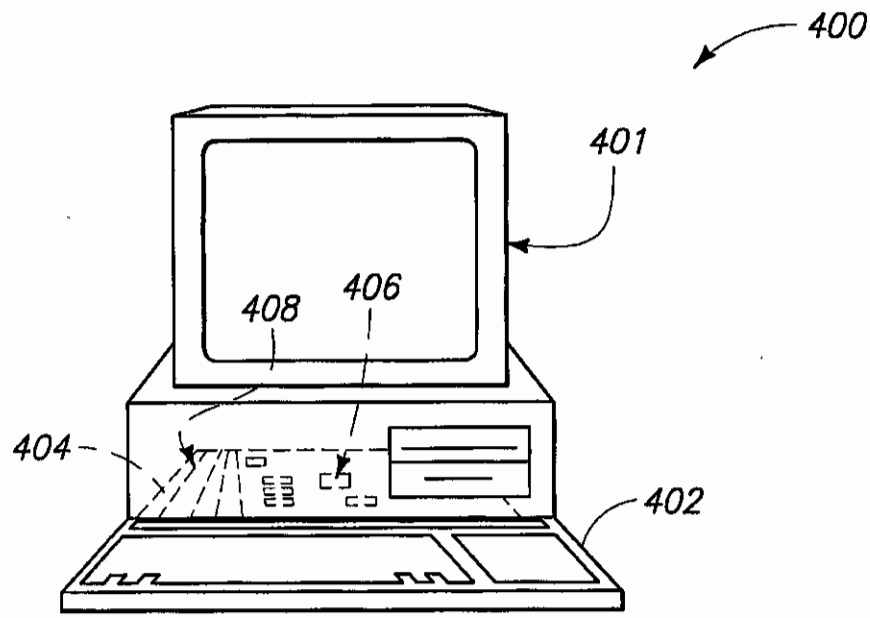


Fig 13

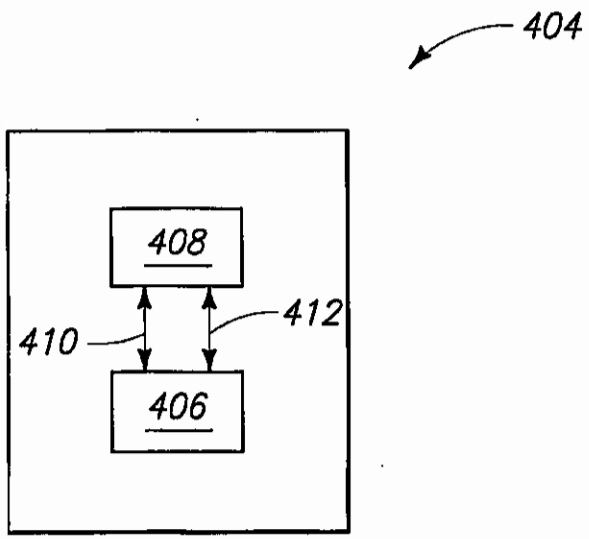


Fig 14

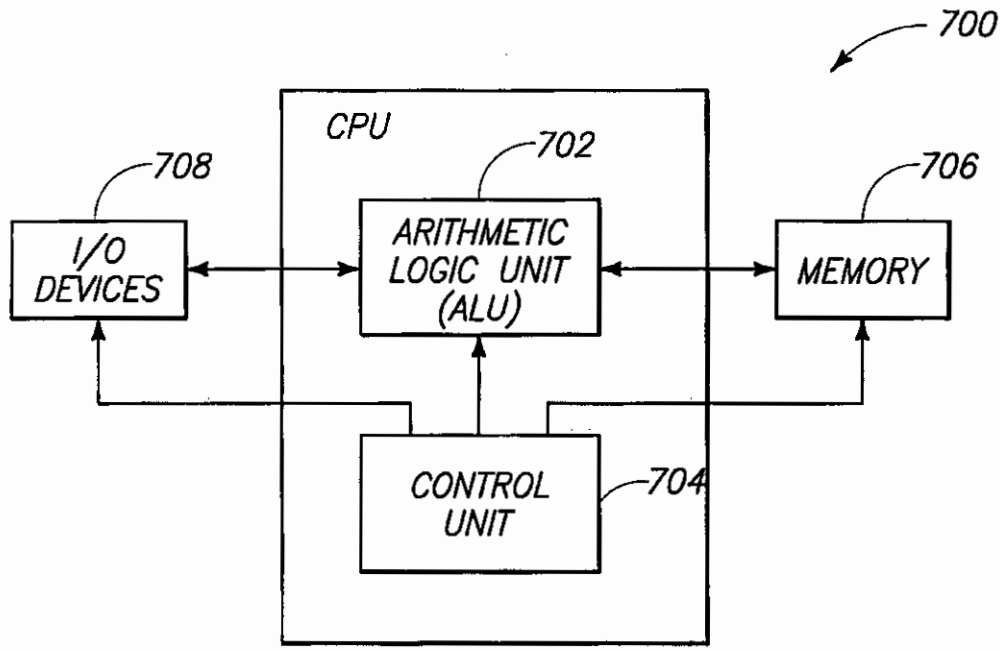

700
CPU
702
708
706
I/O DEVICES
ARITHMETIC LOGIC UNIT (ALU)
MEMORY
CONTROL UNIT
704

Fig 15

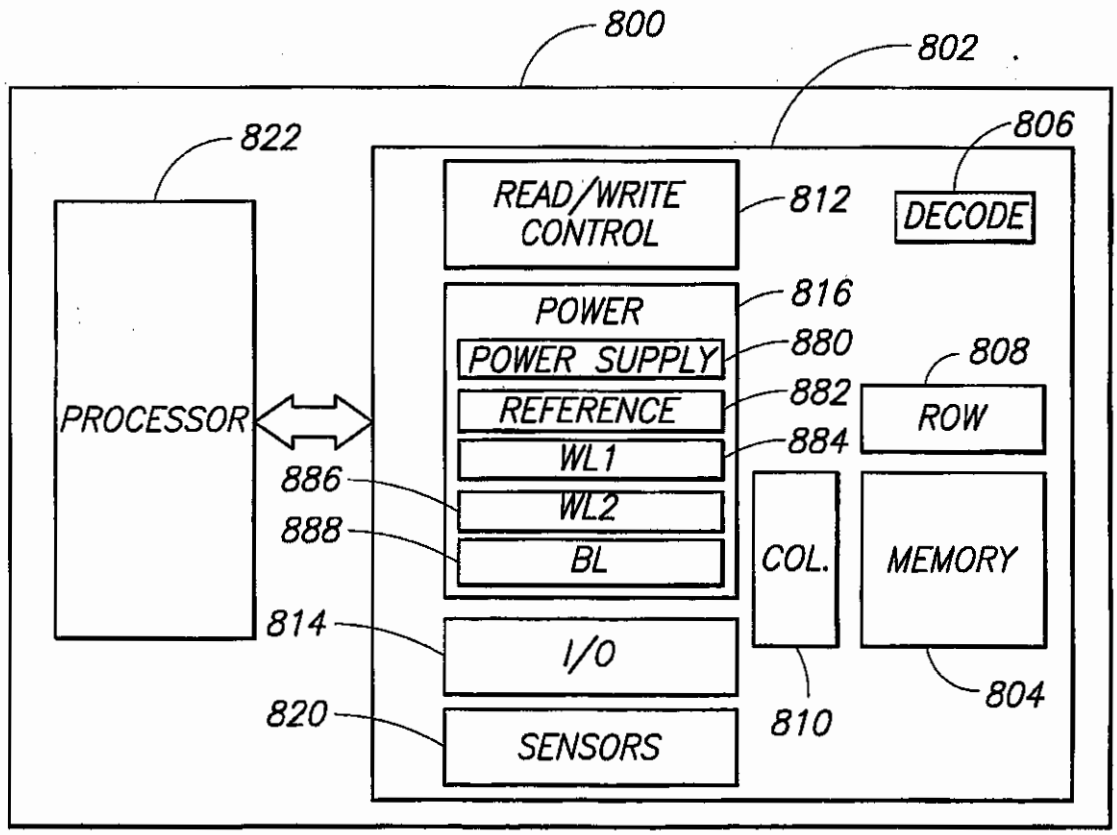

800
802
822
806
READ/WRITE CONTROL
812
DECODE
816
POWER
POWER SUPPLY
880
808
PROCESSOR
882
REFERENCE
ROW
884
WL1
886
WL2
888
BL
COL.
MEMORY
814
I/O
804
820
SENSORS
810

Fig 16