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(54) Title: CARRIER AGGREGATION FRONT-END ARCHITECTURE

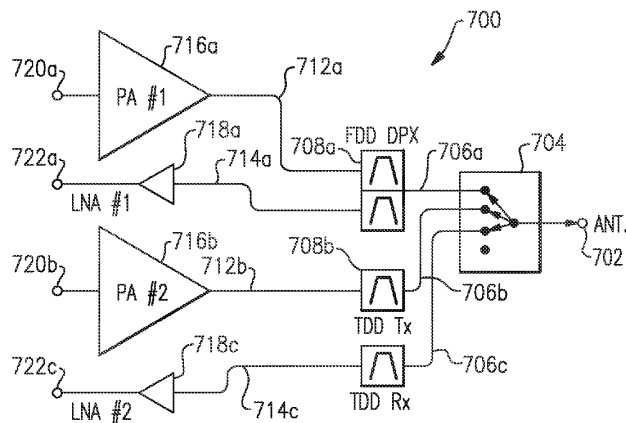


FIG. 7

(57) Abstract: A carrier aggregation circuit that includes a primary signal path for processing a first frequency band, the first frequency band being a TDD frequency band, and a secondary signal path for processing a second frequency band. The primary signal path includes a first TDD filter configured to process receive signals in the first frequency band and a second TDD filter configured to process transmit signals in the first frequency band, the first TDD filter and the second TDD filter including one or more corresponding initial stages that are identical. Related front-end architectures, front-end modules, and wireless devices for carrier aggregation are also provided.

[Continued on next page]

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