

ABSTRACT OF THE DISCLOSURE

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A differential comparator includes first and second transistors for comparing an input signal to a reference signal during a comparison phase and circuitry for balancing the quiescent currents through the transistors. The circuitry includes a first current source connected to the source of the first transistor, a second current source connected to the source of the second transistor and a selectively enabled impedance means connected between the sources of the two transistors. Prior to each comparison phase the currents through the first and second transistors are balanced (i.e. made approximately equal) by application of equal voltages to the gates of the two transistors and cross-coupling the source potentials of the first and second transistors to control the second and first current sources.

This Sheet Has Been Replaced

