

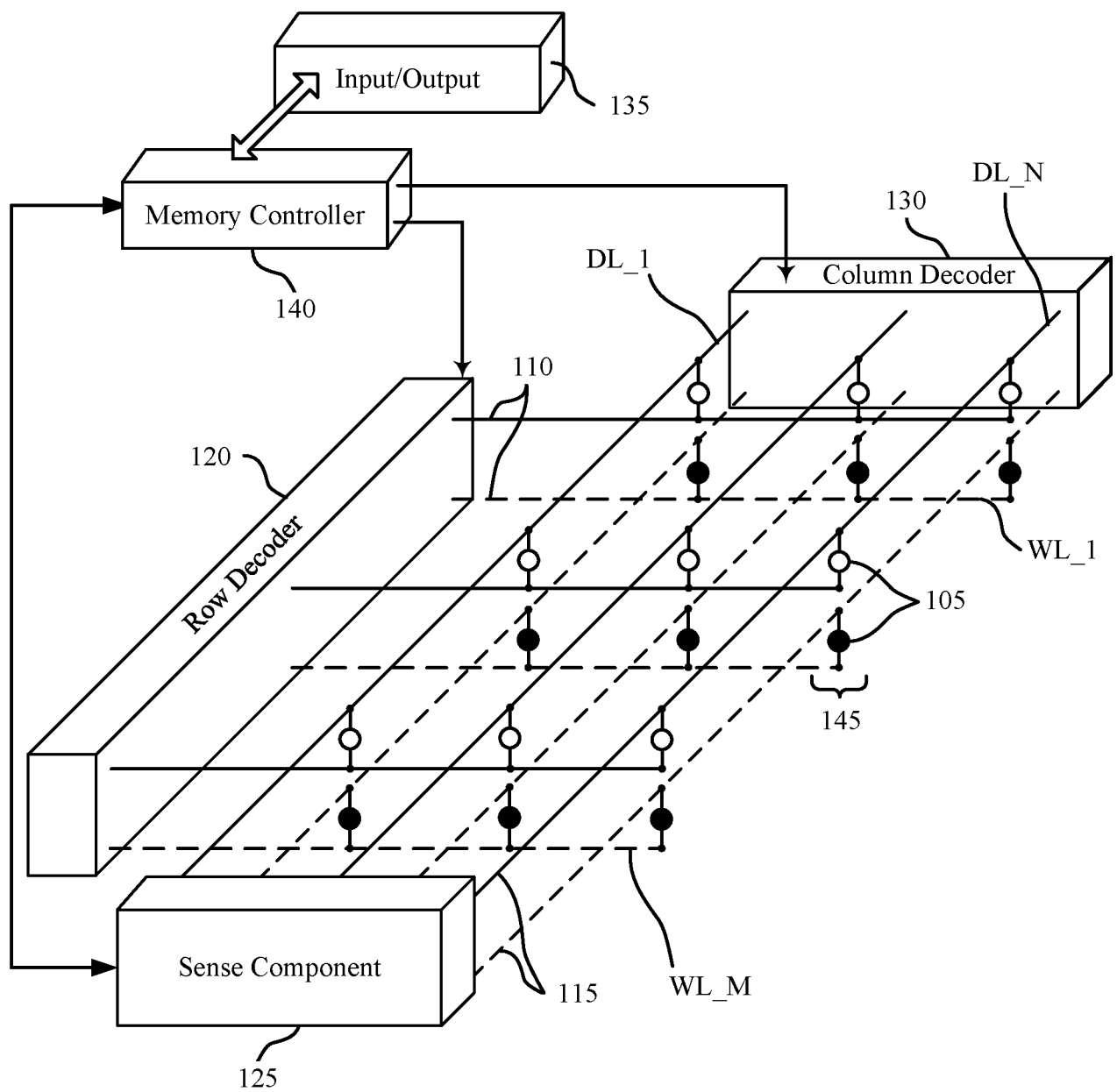


FIG. 1

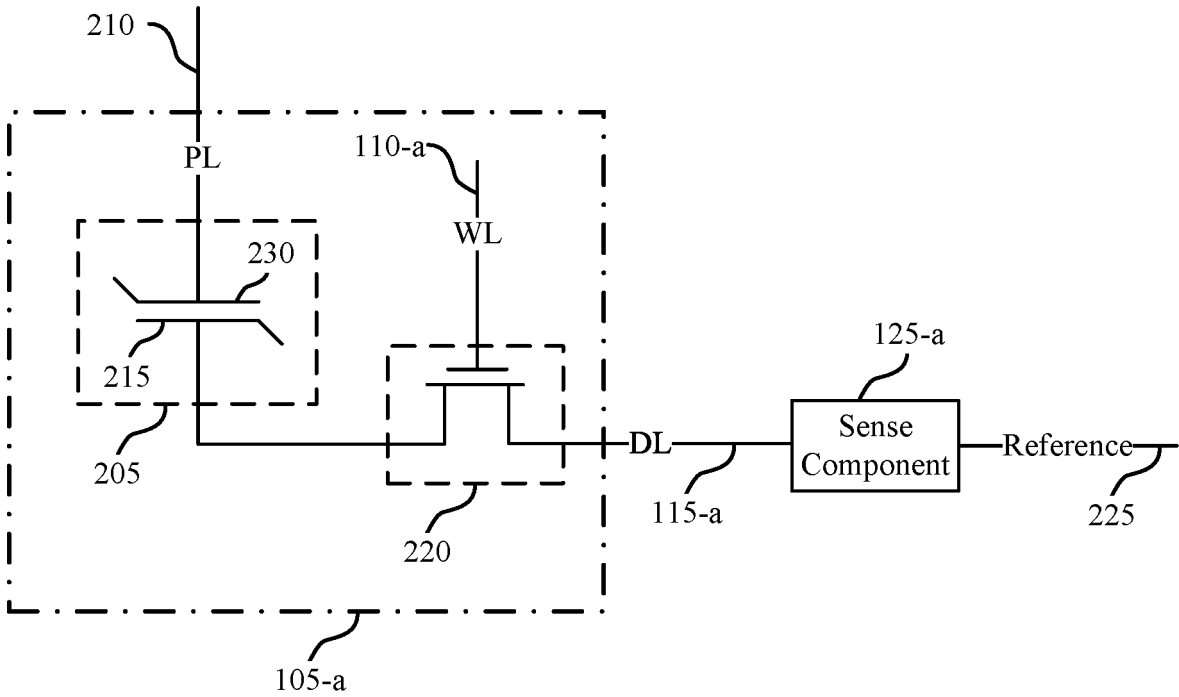


FIG. 2

200

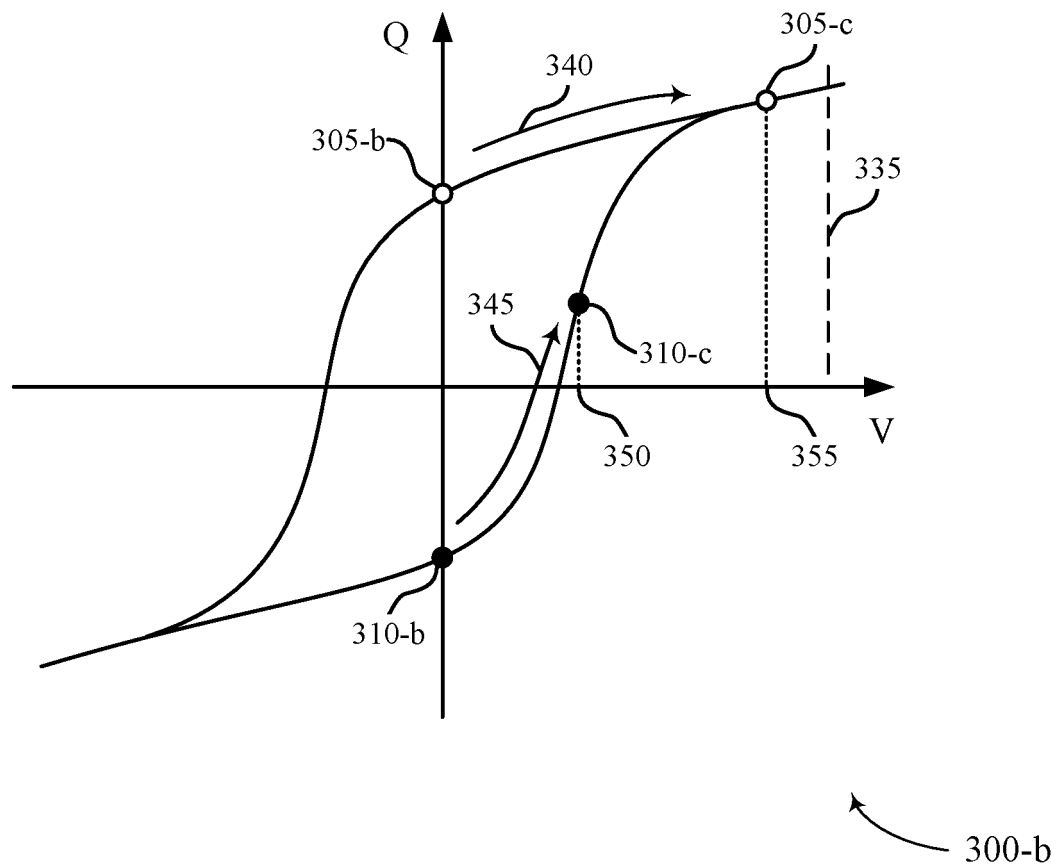
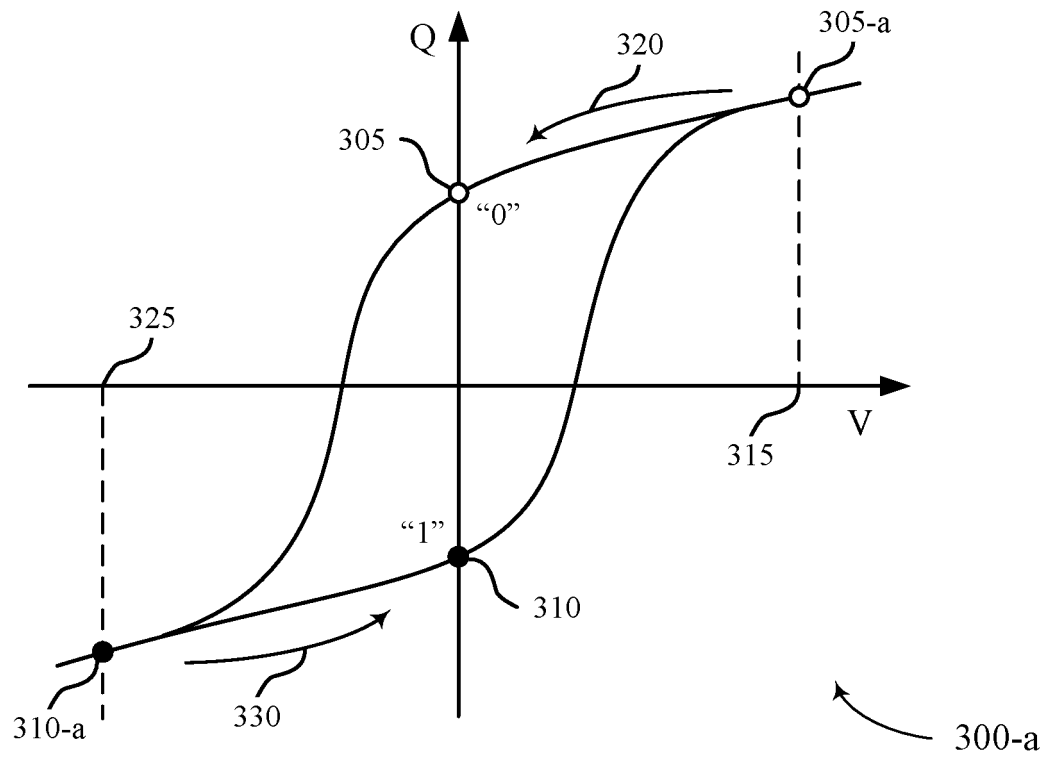
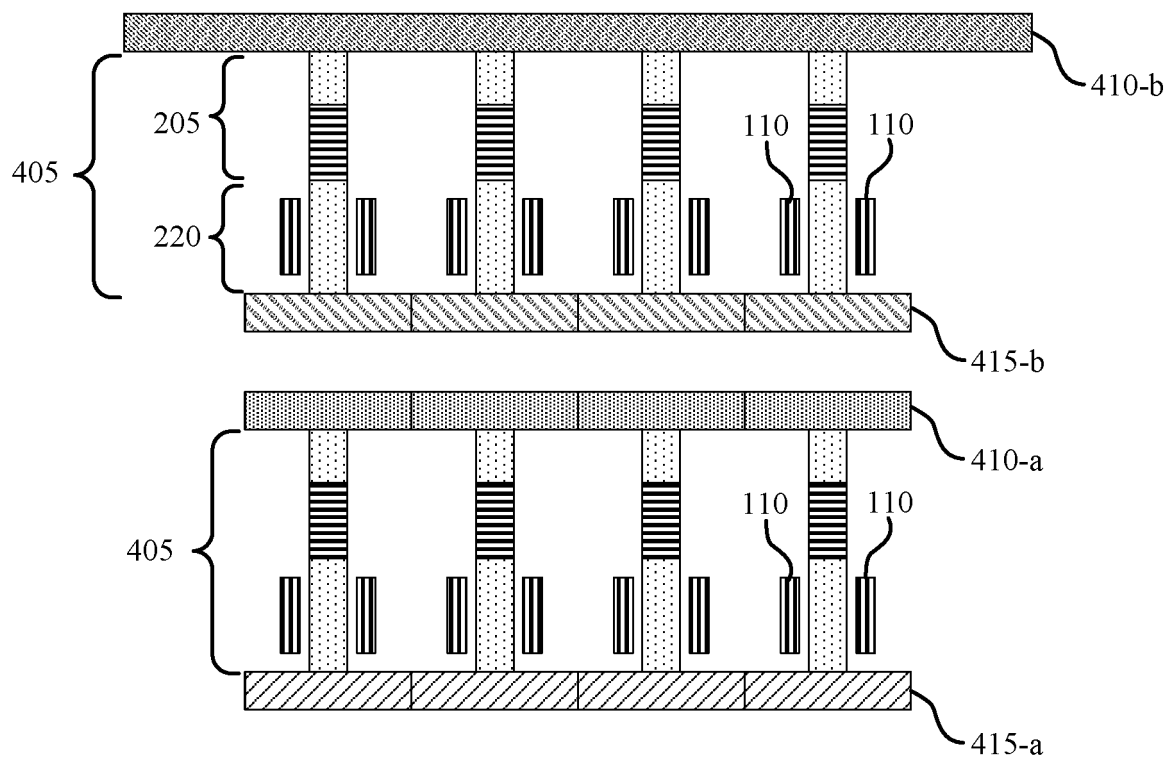


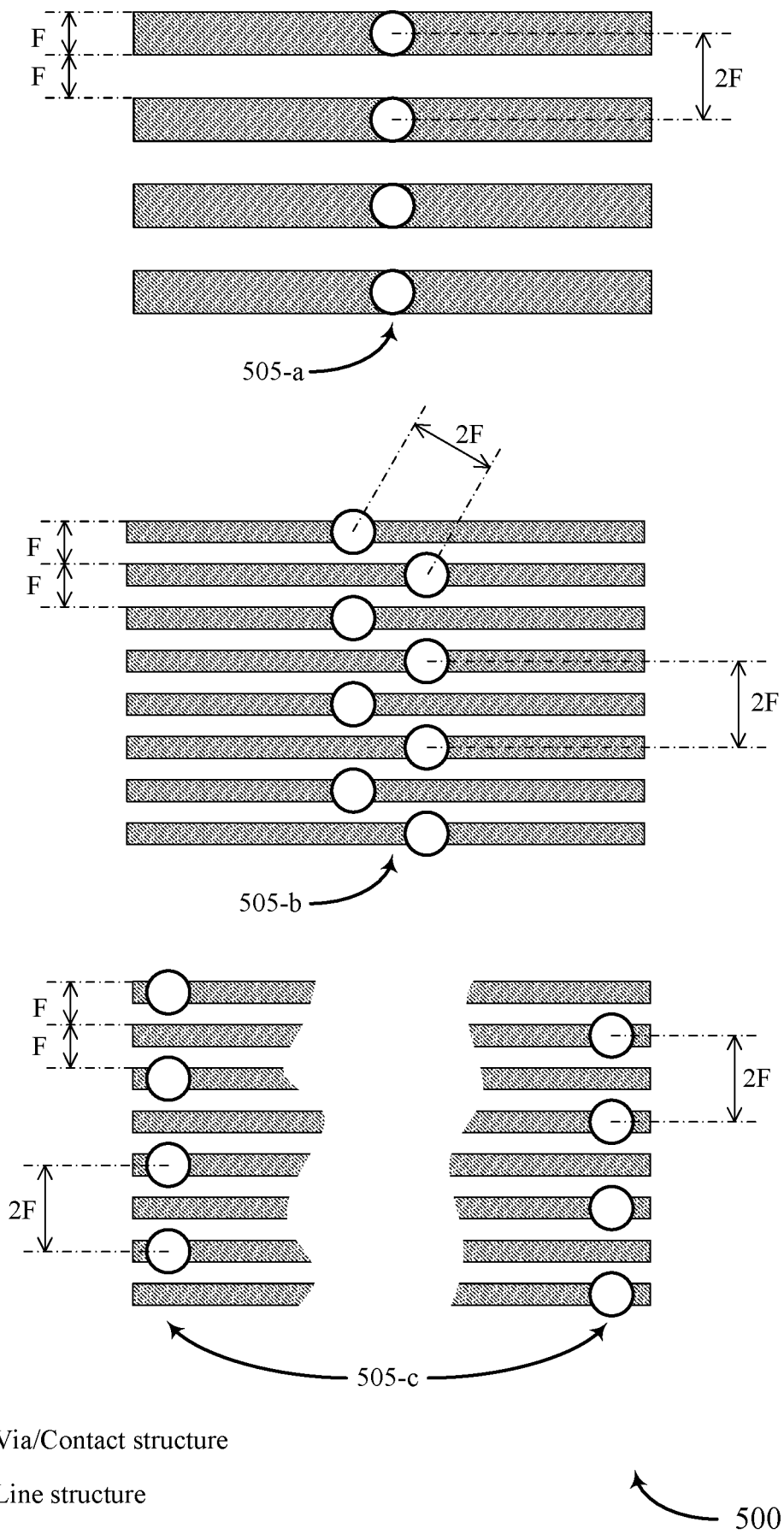
FIG. 3

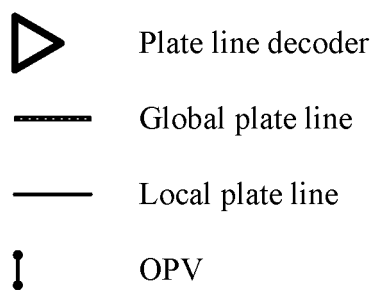
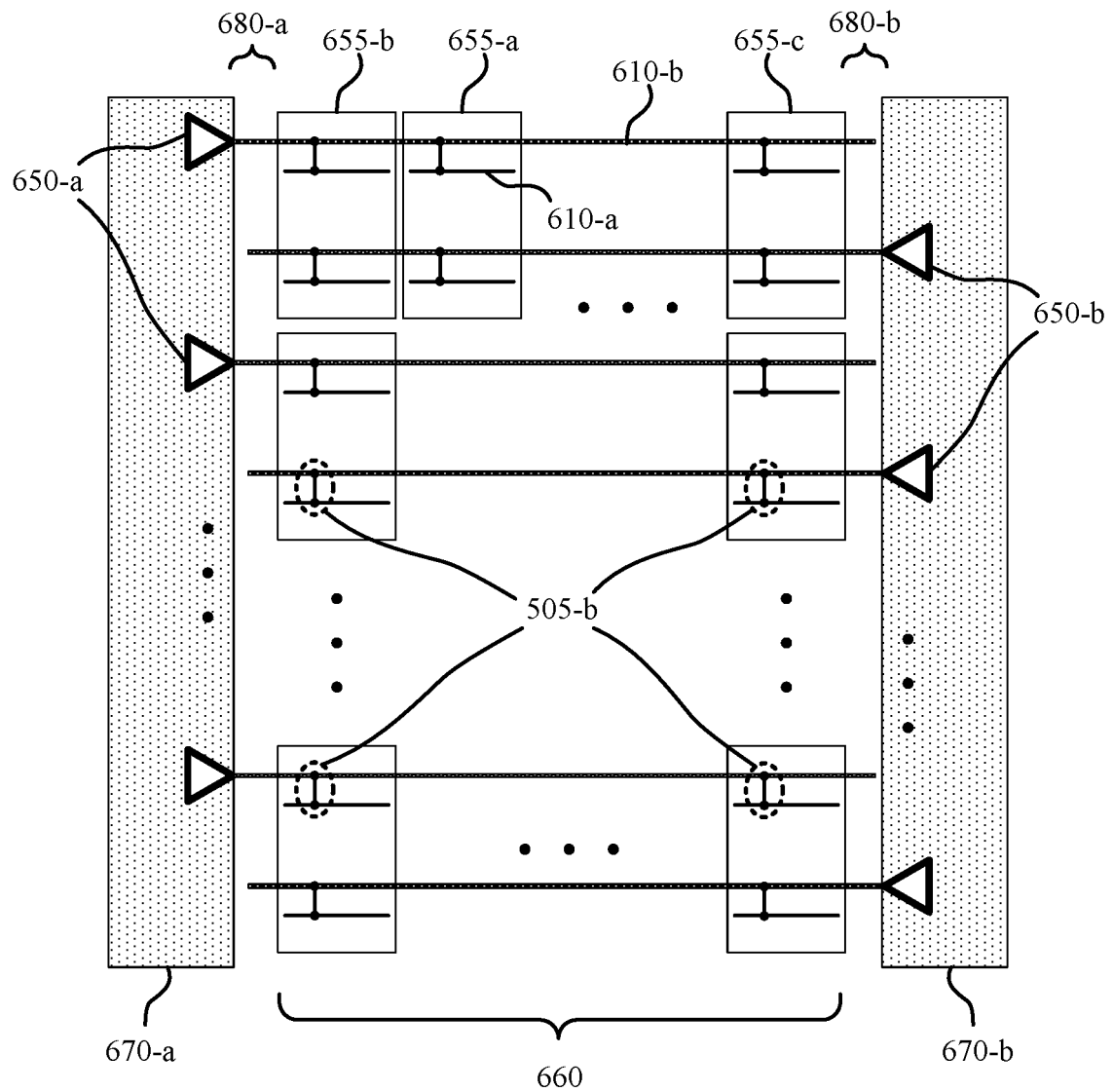


400

- | | | | |
|---|---------------------------|---|---------------------------|
|  | Global plate line |  | Local plate line |
|  | Digit line 2 (upper deck) |  | Digit line 1 (lower deck) |
|  | Ferroelectric capacitor |  | Word line |

FIG. 4





600

FIG. 6

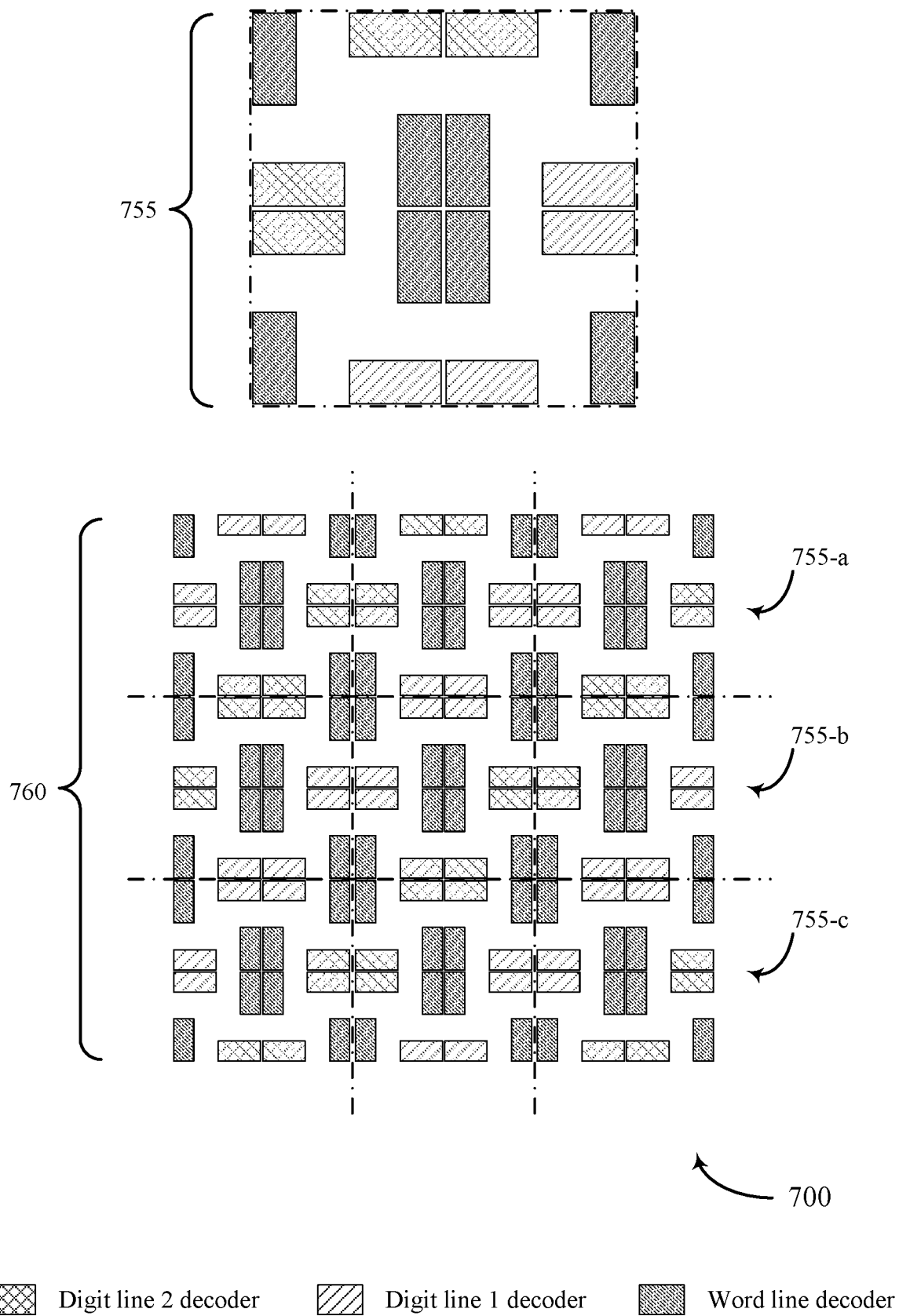


FIG. 7

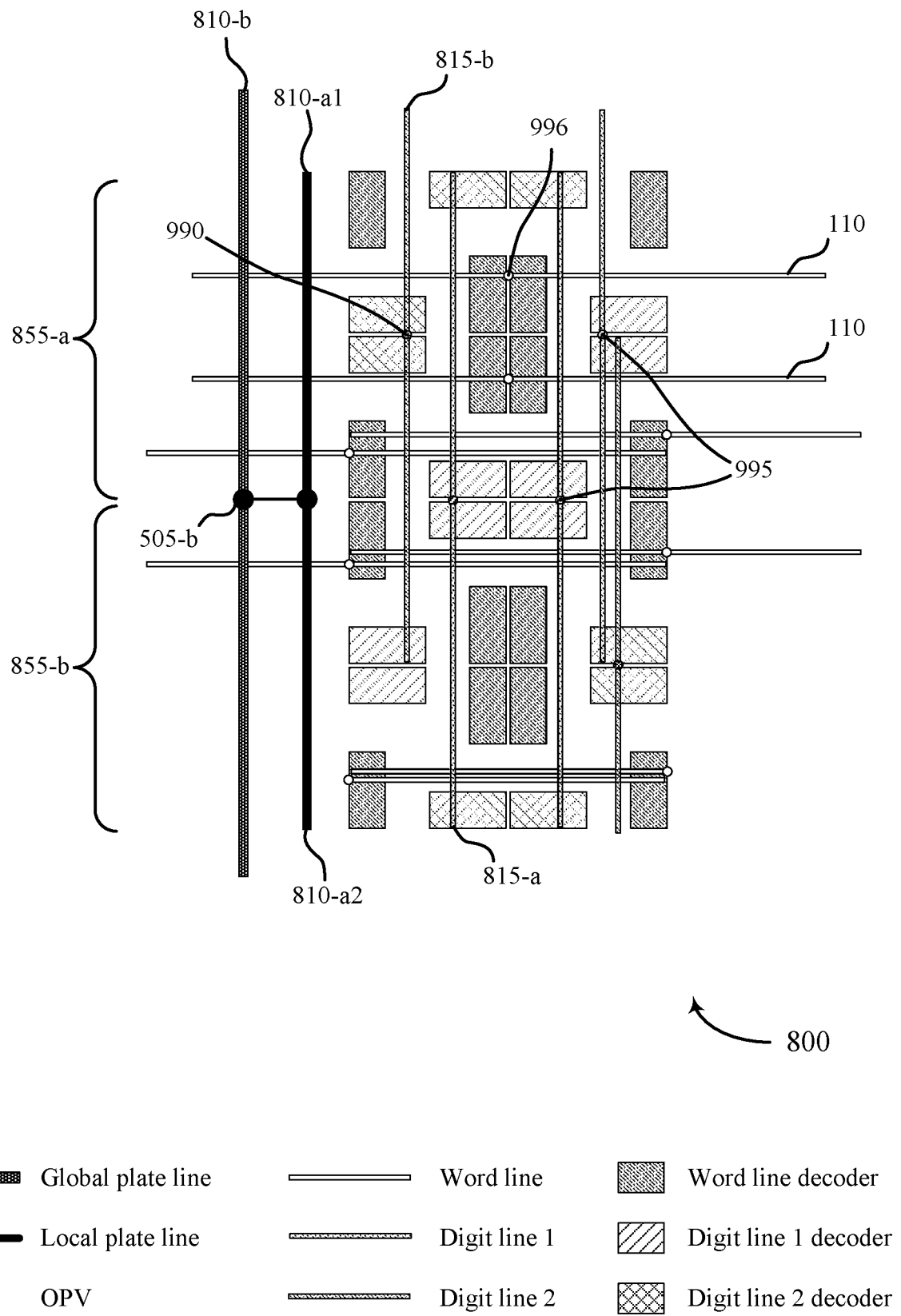
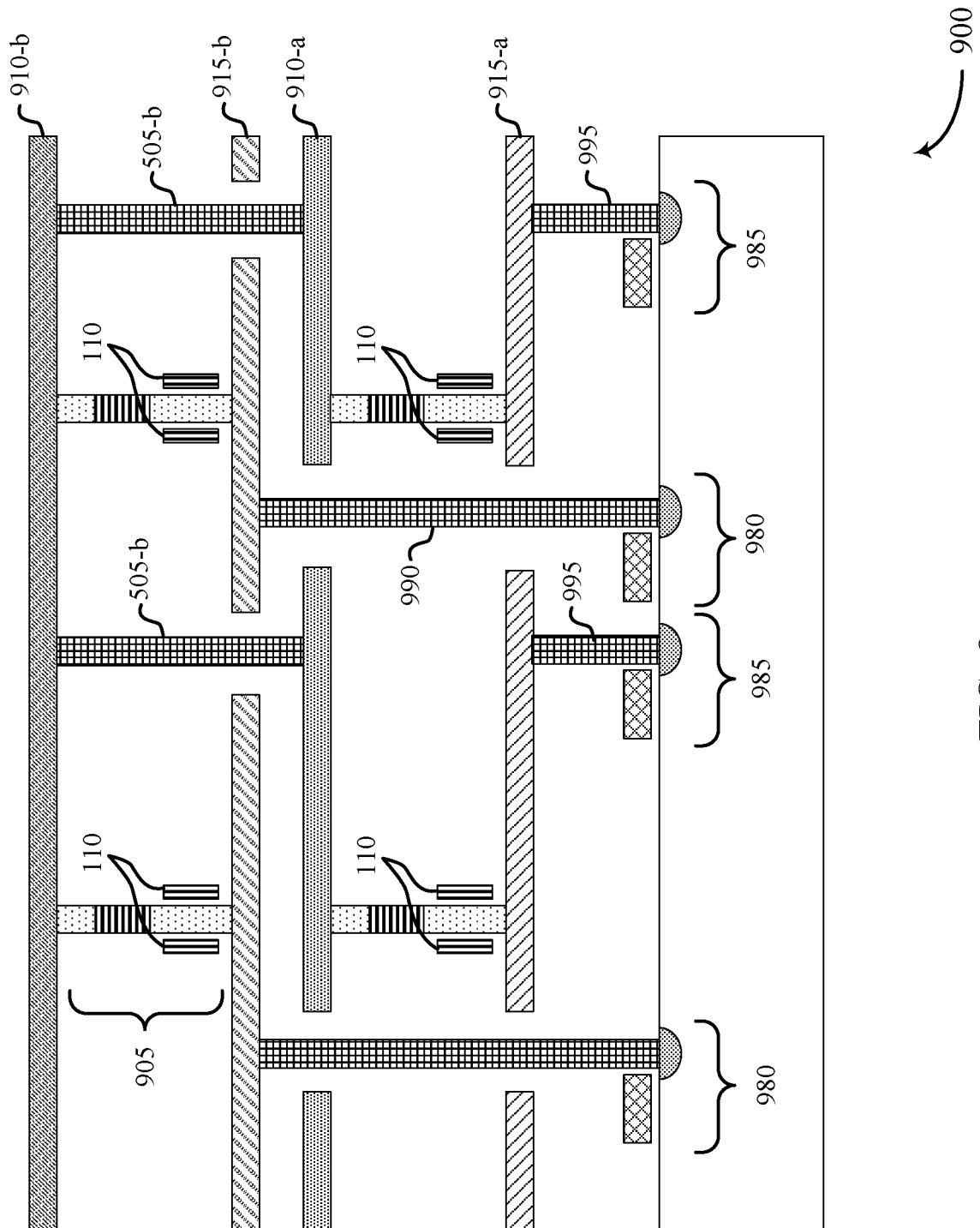
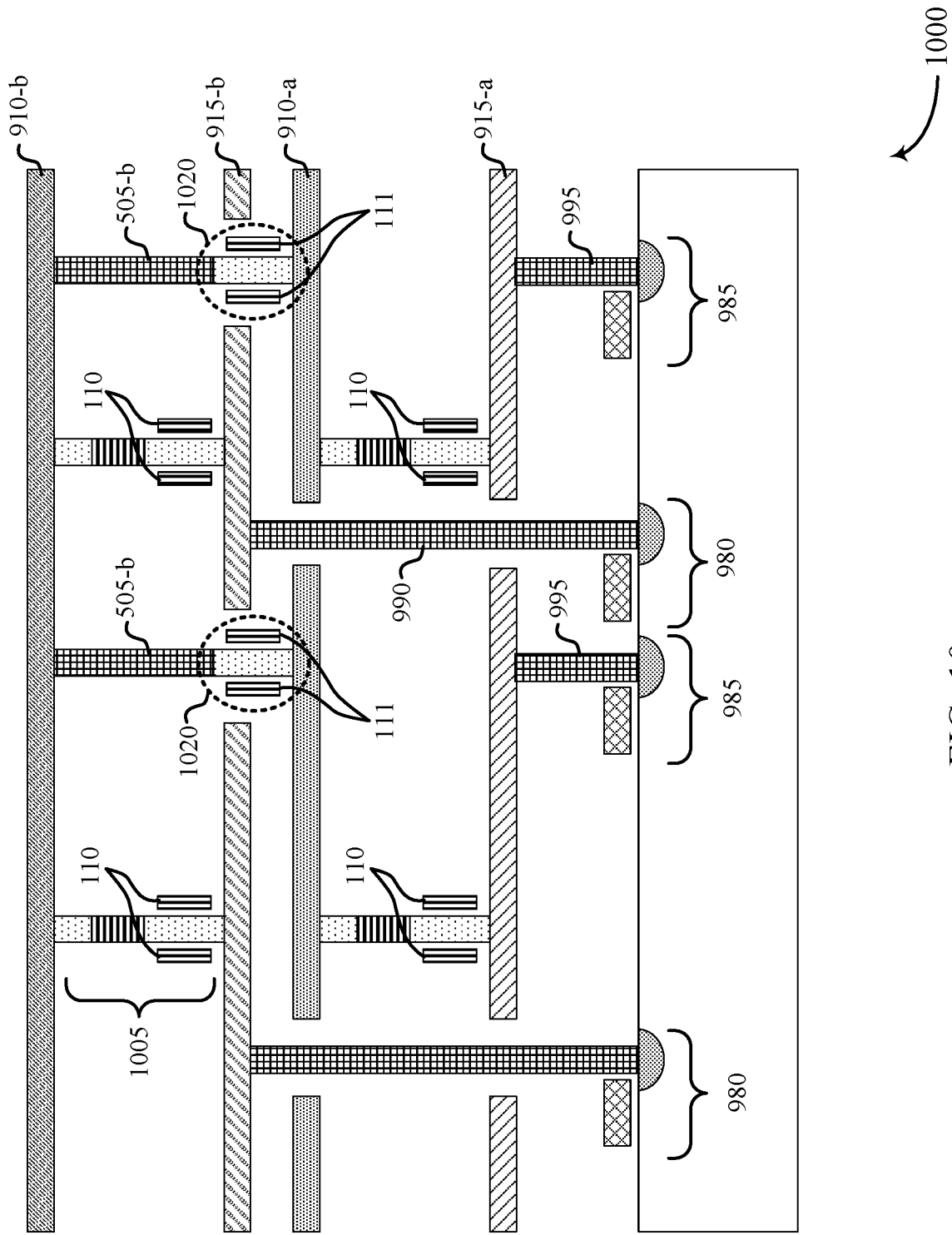


FIG. 8





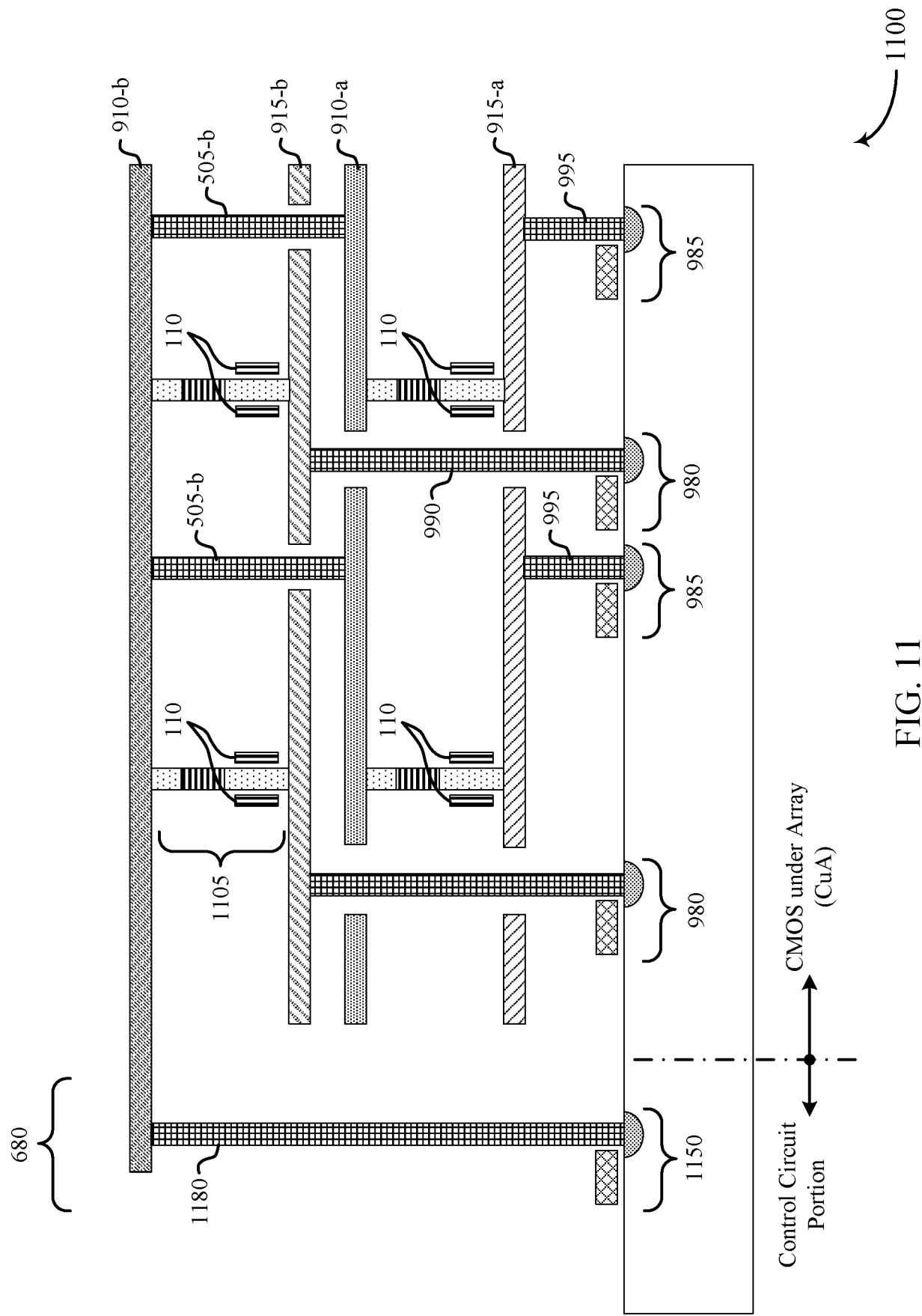
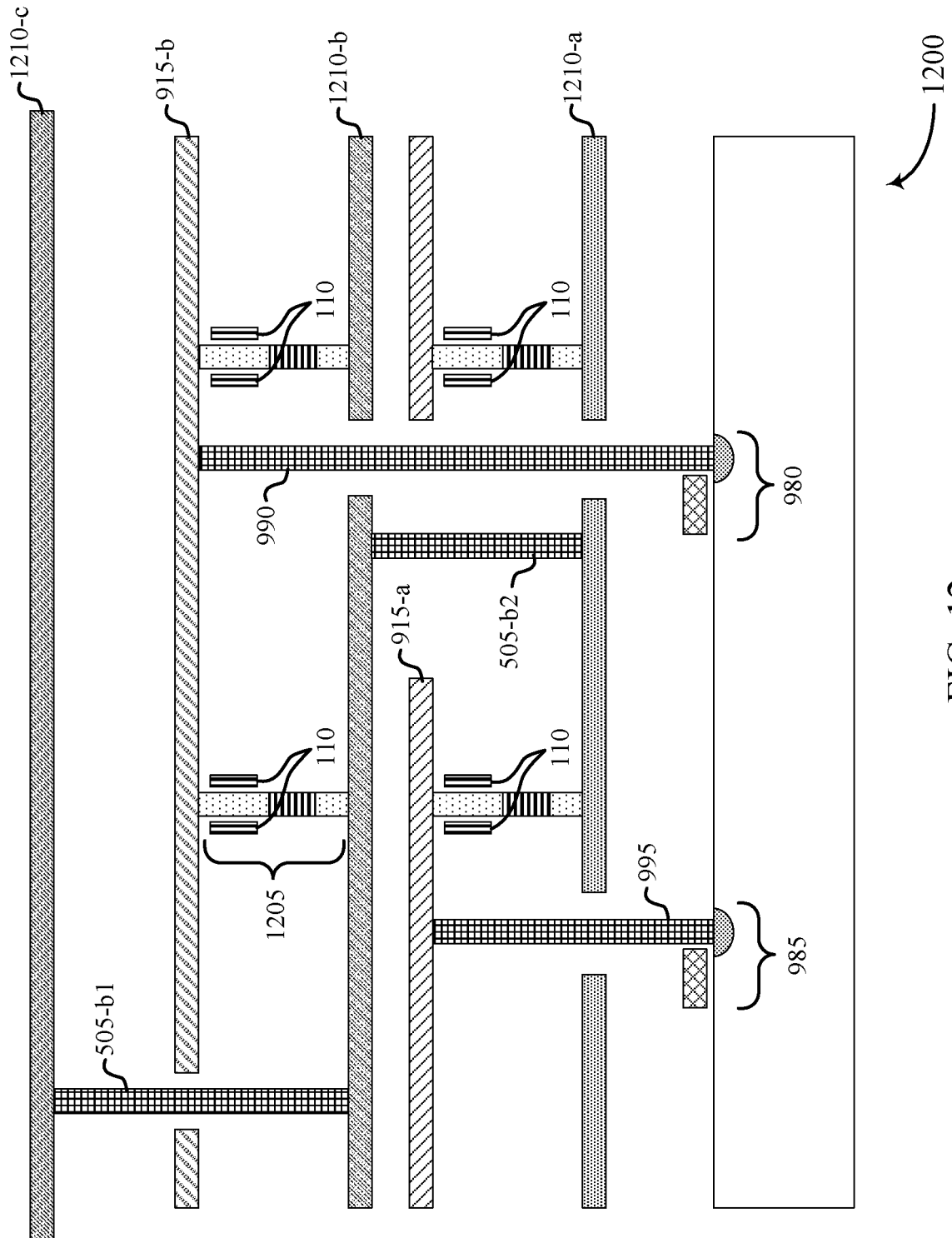


FIG. 11



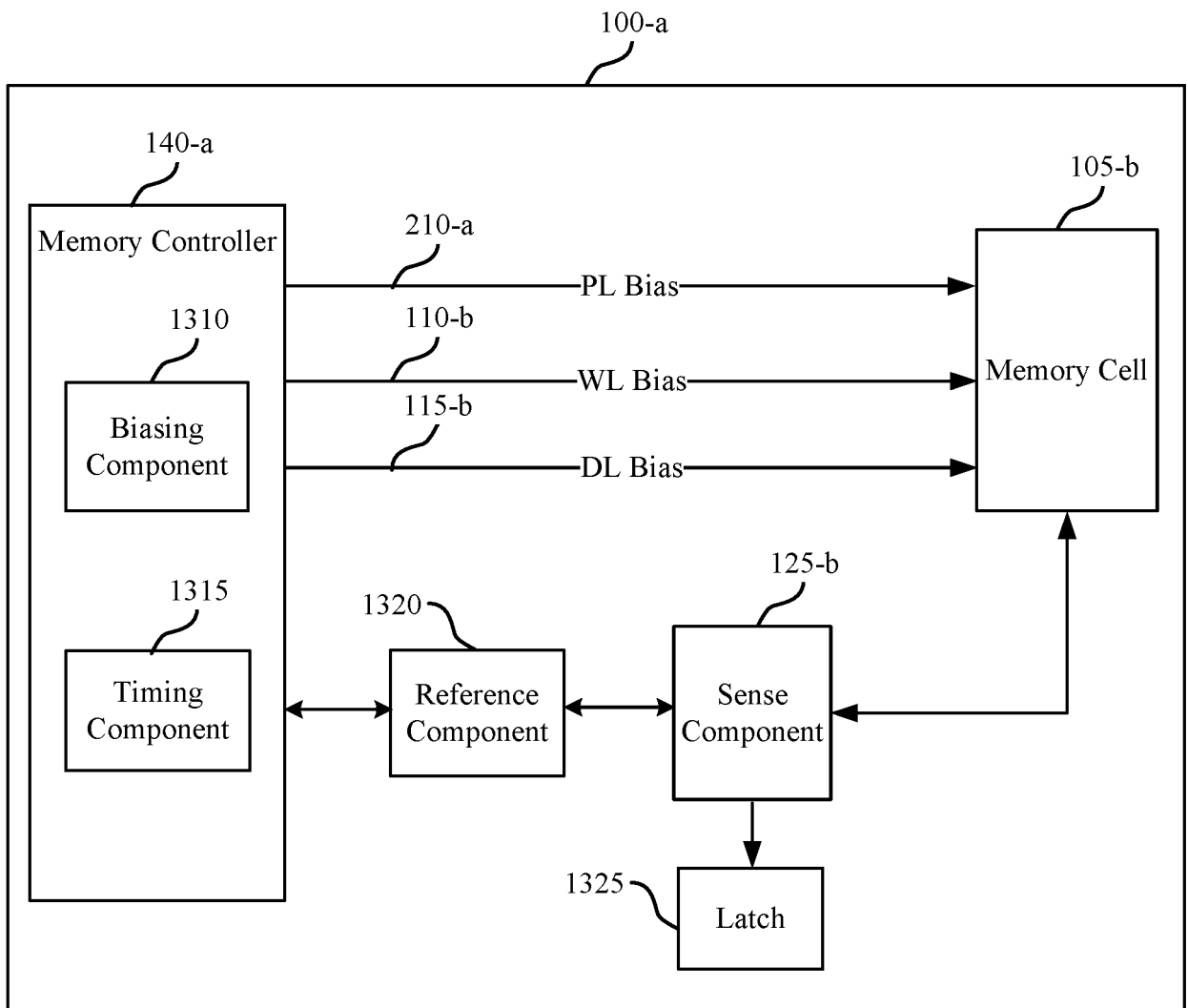


FIG. 13

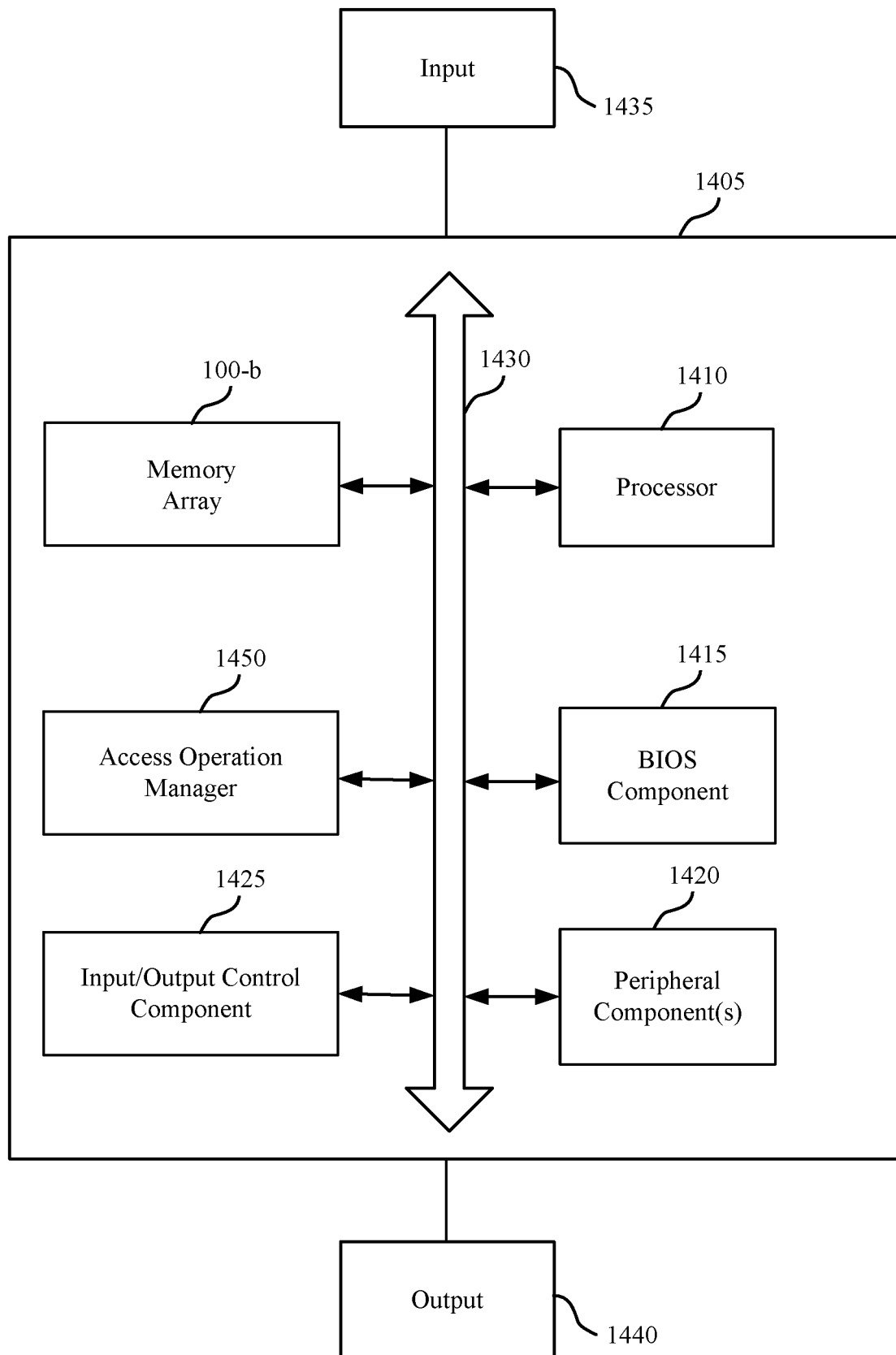


FIG. 14

1400

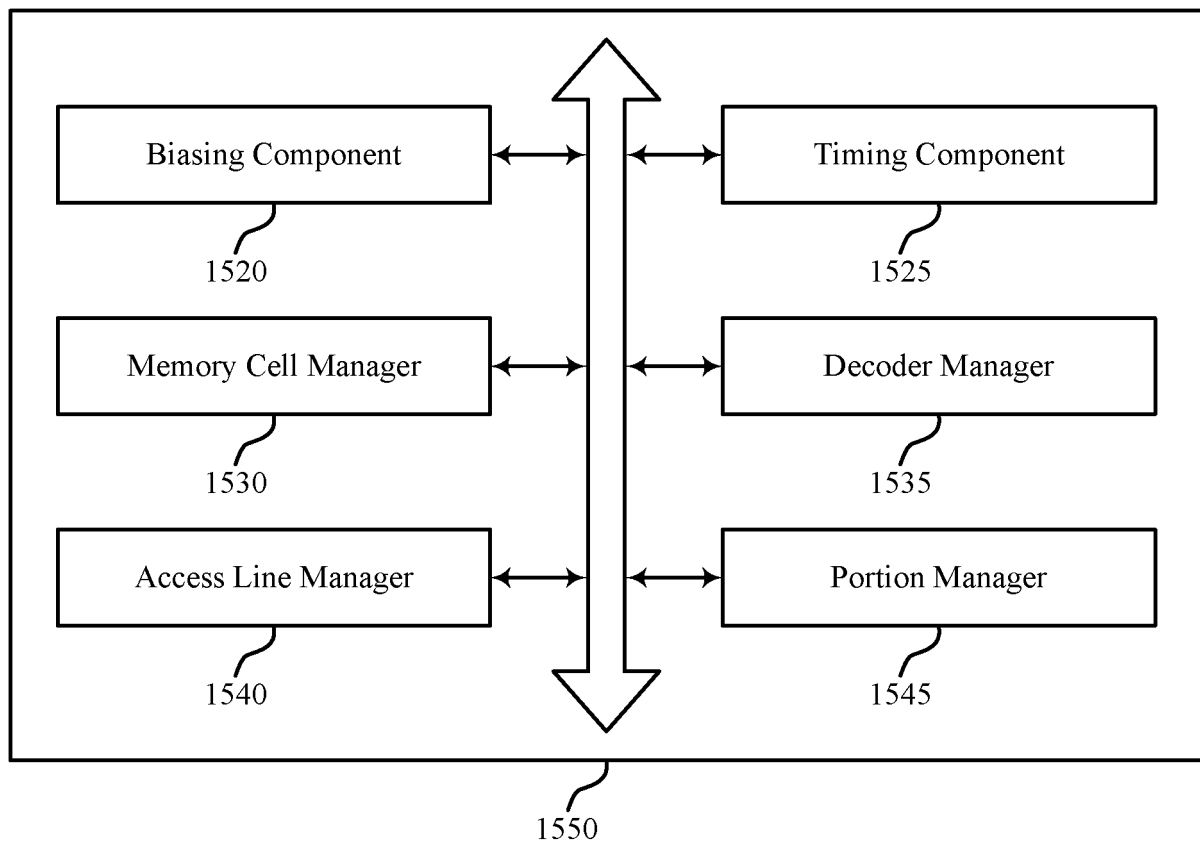


FIG. 15

1500

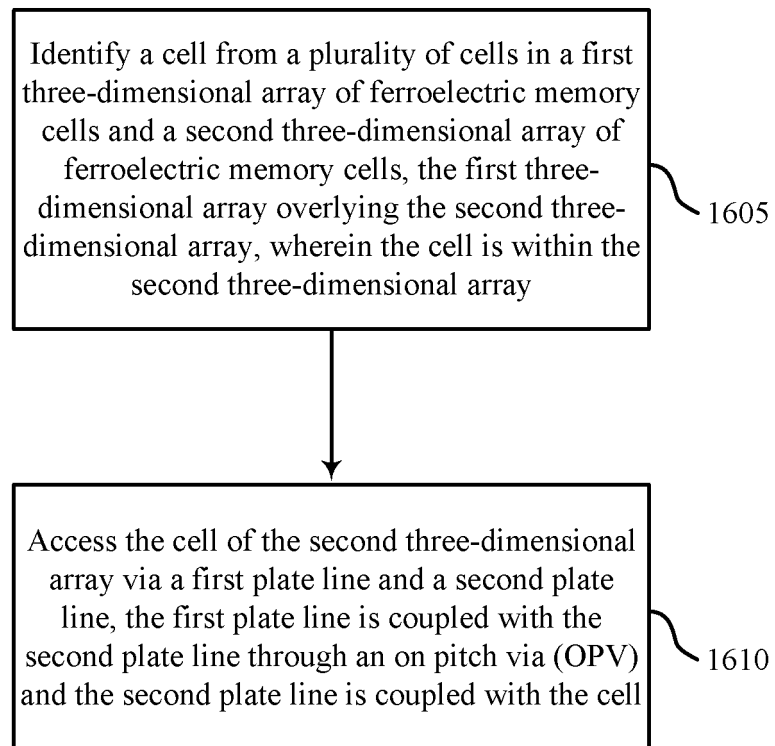


FIG. 16

1600