

A B S T R A C T

A storage device includes a prober, and a stocker capable of storing a plurality of semiconductor wafers each of which includes a plurality of nonvolatile
5 memory dies. A controller in the prober writes first identification information and a first check code to a first semiconductor wafer conveyed from the stocker to a stage in the prober when a set of identification information and an check code having correct
10 correspondence is not stored in the first semiconductor wafer, and acquires a first logical-to-physical address translation table associated with the identification information of the first semiconductor wafer from a host computer or the first semiconductor wafer when the
15 set of the identification information and the check code having the correct correspondence is stored in the first semiconductor wafer.